

GENERAL FEATURES

• $V_{DS} = 50V, I_D = 0.22A$

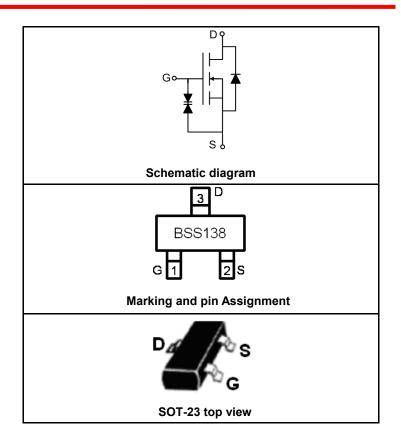
 $R_{DS(ON)} < 6\Omega$ @ V_{GS} =4.5V $R_{DS(ON)} < 3.5\Omega$ @ V_{GS} =10V

ESD Rating: 1000V HBM

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

APPLICATION

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
BSS138	BSS138	SOT-23	Ø180mm	8 mm	3000 units

ABSOLUTE MAXIMUM RATINGS(TA=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	50	V	
Gate-Source Voltage	V _G s	±20	V	
Design Comment Continuous @ Comment Duled (Nate 4)	I _D	0.22	Α	
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM}	0.88	А	
Maximum Power Dissipation	P _D	0.36	W	
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	°C	

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	350	°C/W	I
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0 V I_D =250 μ A	50			V



Zero Ceta Valtago Drain Current		V _{DS} =30V,V _{GS} =0V			100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =50V,V _{GS} =0V			1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V			10	uA	
Gate-Source Breakdown Voltage	BV _{GSO}	V _{DS} =0V, I _G =±250uA	±20			V	
ON CHARACTERISTICS (Note 3)				•	•		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =1mA	8.0		1.5	V	
Drain-Source On-State Resistance		V _{GS} =10V, I _D =0.22A			3.5	Ω	
Dialii-Source Oil-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =0.22A			6		
Forward Transconductance	G FS	V _{DS} =10V,I _D =0.22A		0.1		S	
DYNAMIC CHARACTERISTICS (Note4)	<u>.</u>						
Input Capacitance	C _{lss}			30		PF	
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, F=1.0MHz		15			
Reverse Transfer Capacitance	C _{rss}			6			
SWITCHING CHARACTERISTICS (Note 4)							
Turn-on Delay Time	t _{d(on)}			2.6			
Turn-On Rise Time	t _r	V _{DD} =30V,V _{GS} =10V,		9		nS	
Turn-Off Delay Time	t _{d(off)}	$R_{GEN}=6\Omega$, $I_D=0.22A$		20		115	
Turn–Off Fall Time	t _f			6			
Total Gate Charge	Qg			1.7	2.4		
Gate–Source Charge	Q_{gs}	V _{DS} =25V,I _D =0.22A,V _{GS} =10V		0.1		nC	
Gate–Drain Charge	Q_{gd}			0.4			
DRAIN-SOURCE DIODE CHARACTERISTIC	S	-		1	ı		
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =0.44A			1.4	V	
		1	ı				

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature.
 Surface Mounted on FR4 Board, t ≤ 10 sec.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
 Guaranteed by design, not subject to production testing.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

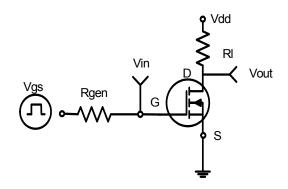


Figure 1:Switching Test Circuit

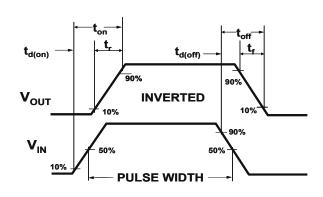


Figure 2:Switching Waveforms

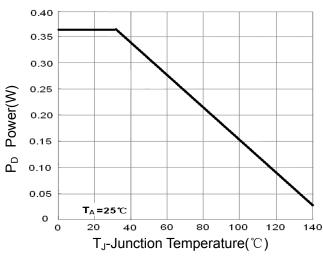


Figure 3 Power Dissipation

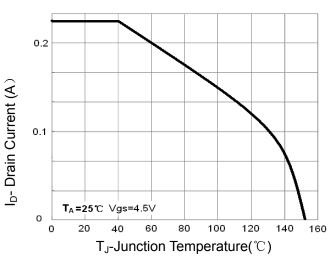


Figure 4 Drain Current

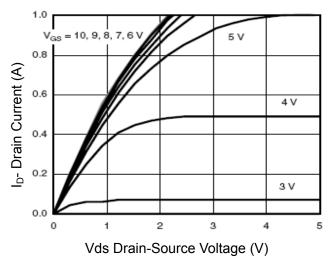


Figure 5 Output CHARACTERISTICS

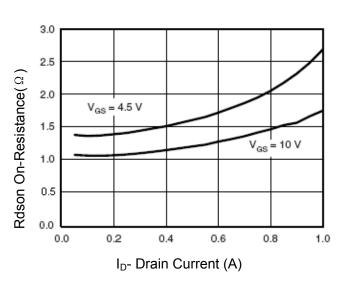


Figure 6 Drain-Source On-Resistance



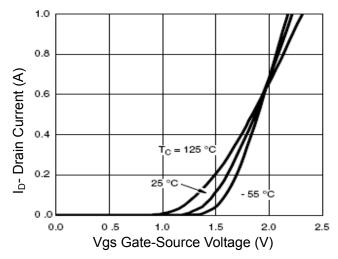


Figure 7 Transfer Characteristics

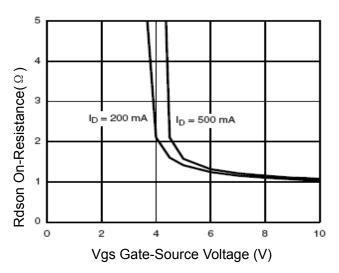


Figure 9 Rdson vs Vgs

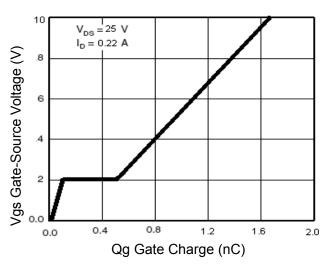


Figure 11 Gate Charge

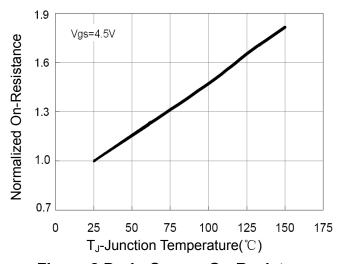


Figure 8 Drain-Source On-Resistance

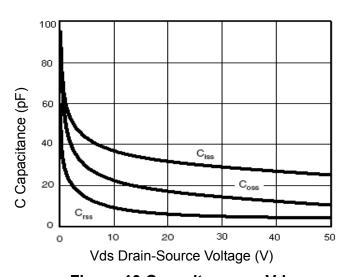


Figure 10 Capacitance vs Vds

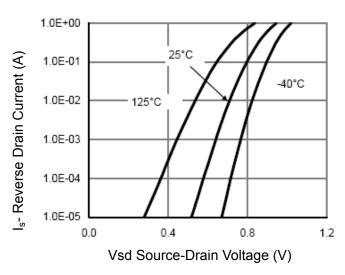


Figure 12 Source- Drain Diode Forward



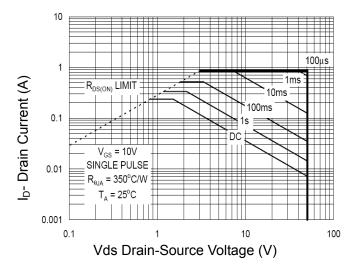


Figure 13 Safe Operation Area

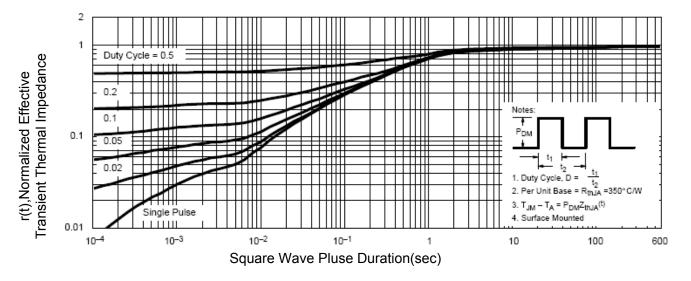
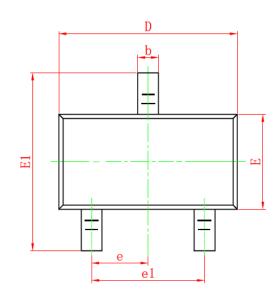


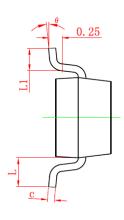
Figure 14 Normalized Maximum Transient Thermal Impedance

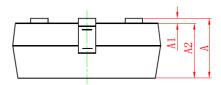


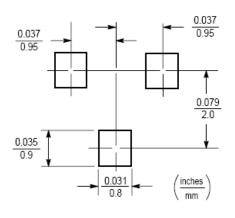
SOT-23 PACKAGE INFORMATION

Dimensions in Millimeters (UNIT:mm)









Symbol	Dimensions in Millimeters			
Symbol	MIN.	MAX.		
Α	0.900	1.150		
A1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
С	0.080	0.150		
D	2.800	3.000		
E	1.200	1.400		
E1	2.250	2.550		
е	0.950TYP			
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.500		
θ	0° 8°			

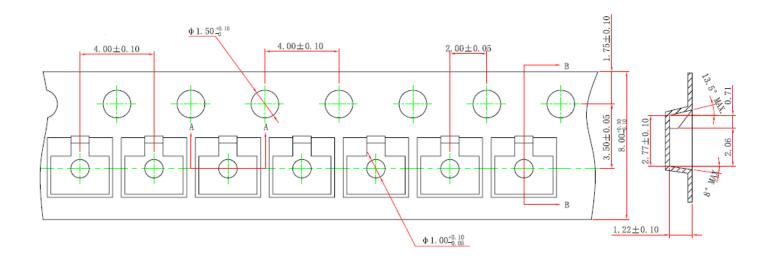
NOTES

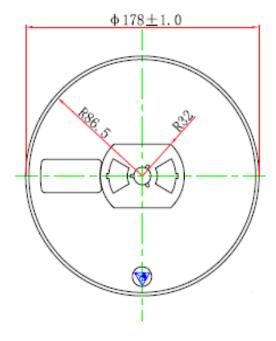
- All dimensions are in millimeters.
 Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

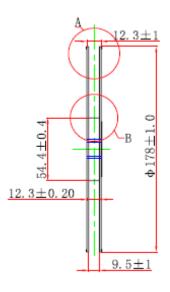


SOT 23 Tape and Reel Information

Dimensions in Millimeters (UNIT:mm)







NOTES:

- 1. All dimensions are in millimeters.
- 10 Sprocket hole pitch cumulative tolerance ±0.20MAX
- 3. General tolerance ±0.25



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