

# Model VM32PA2.0

### 32 Channel

# Programmable Amplifier >100 kHz to 2.0 MHz VME Board

### Description

Frequency Devices' Model VM32PA2.0 comprises a family of VMEbus amplifier boards offering software programmable differential amplifiers in a single width B-size (6U) VME form factor. VM32PA2.0 boards provide simultaneous access to 32, DC-coupled wideband signals while providing programmable gain from -12dB to +36dB in 6dB steps for signal bandwidths from >100 kHz to 2.0 MHz. VM32PA2.0 boards may be configured with 8, 16, or 32 channels. The boards conform to VME revision C.1 as an A16/D16 Slave. Available options include AC-coupled input.

### Features/Benefits:

- Simultaneous access over 32 channels offers a low cost, versatile and convenient way to provide amplification.
- Three active read/write registers provide programming and set-up verification.
- Phase match of ±2.0° and gain accuracy of ±0.1dB provides precision performance solutions to design engineers, system integrators and OEM's.
- High channel count density without sacrificing performance maximizes chassis utilization.

### Signal conditioning applications include:

- · Sonar, navigation and aerospace
- · Engine test and simulation
- · Acoustic and vibration analysis
- · Satellite and telecommunications
- Laboratory R & D
- Automatic test equipment (ATE)
- Industrial process control

### **Ordering Information**

### 8, 16, or 32Channels

A- AC Coupled Input

Optional



U.S. Selling Price (1-4)

Orders for Export Minimum Order Value \$150.00 Lead-Time: 6-8 weeks A.R.O VM32PA2.0-8 . .\$1,700.00 VM32PA2.0-16 . . 1,800.00 VM32PA2.0-32 . . 2,200.00 Add Price of PGA Modules U.S. Selling Price + 20% F.O.B. Haverhill, MA Accept Visa, Mcard, Amex

### GAIN AMPLIFIER

PGA5-2.0

-12 dB to +36 dB in 6 dB steps

# VM32PA2.0-32-PGA5-2.0-A

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## VM32PA2.0

### Wideband Programmable Gain Amplifier

### Specifications

(@ 25°C and rated power Input)

### 32 CHANNEL VME SIGNAL CONDITIONING BOARD

### Analog Input

1. Impedance

- 2. Linear input range
- 3. CMRR

4. Maximum input range 5. AC coupling (Optional Fixed Freq.)

### Analog Output

6. Impedance
 7. Drive capability
 8. Linear operating range

- 9. Offset voltage
- 10. Offset temp. coeff.

#### Programmable Amplifier

Signal bandwidth (-3 dB)
 Amplitude match
 Phase match
 Noise voltage density, RTI
 Distortion (2 V pk-pk)

- 16. Gain programming
- 17. Gain accuracy

#### VMEbus

18. Interface 19. Registers

### Power Supply

20. From VME Backplane

### Environmental

21. Operating
 22. Storage
 23. Humidity

### Mechanical

24. Card Size25. No. of Input Channels26. No. of Output Channels27. Mating Connectors

28. Weight

1 MΩ differential, 500 kΩ to ground, each leg 8 V pk-pk, each leg ≥ 50 dB, DC to 100 kHz ≥ 40 dB, 100 kHz to 2 MHz 20 V pk-pk, each leg 20 Hz to 1.0 kHz

50  $\Omega$ , single-ended  $\geq$  50  $\Omega$  load impedance 2 V pk-pk < 25 mV typ. at output 1.5 mV/°C at output 25 mV/°C referred to input

2 MHz 0.2 dB over specified bandwidth, chan. to chan.  $\pm 2.0^{\circ}$  over specified bandwidth 16 nV/ $\sqrt{Hz}$   $\leq$  -60 dB, 20 Hz to 100 kHz  $\leq$  -50 dB, 100 kHz to 2 MHz 0.25X to 64X in factors of 2  $\pm$  0.1 dB

A16/D16, D08 (EO), Slave Three active R/W registers in 64 byte blocks

+5V – 1.0A max. ±12 - 1.25A max.

0°C to +70°C -25°C to +85°C 0 - 95% non-condensing

VMEbus 6U single slot 9.17 x 6.3 inches, (233 x 160 mm)
32 Differential
32 Single Ended, Two groups of 16
Input: Male high density 78-pin D sub, Quantity 1
Output: Female high density 44-pin D sub, Quantity 2
1.0 LBs., (454 grams)

We hope the information given here will be helpful. The information is based on data and our best knowledge, and we consider the information to be true and accurate. Please read all statements, recommendations or suggestions herein in conjunction with our conditions of sale which apply to all goods supplied by us. We assume no responsibility for the use of these statements, recommendations or suggestions, nor do we intend them as a recommendation for any use which would infringe any patent or copyright. PR-VMVM32PA2.0-00

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