

10/100 BASE-T ETHERNET ISOLATION TRANSFORMER
P/N: 16PT6149F DATA SHEET

A. GENERAL SPECIFICATIONS:

1. 16 PIN, DIP PACKAGE WITH COMMON CHOKE TO MEET IEEE802.3.
2. MORE ECONOMAICAL THAN DISCRETE TRANSFORMERS.2TRANSFORMERS PER PACKAGE.
3. OPERATING TEMPANGE:0C°C TO 70C°C.
4. STORAGE TEMP RANG: -25°C TO + 125°C.

B. ELECTRICAL SPECIFICATIONS: (@ 25°C)

- 1.PRIMARY INDUCTANCE, L_p : PIN 1-2 = 350 uH Min, PIN 16-15 = 550 uH Min
@100KHz/0.1Vrms 8mA DC
2. LEAKAGE INDUCTANCE (LL, uH Max): PIN 1-2 = 0.5 uH Max, PIN 16-15 = 0.7uH Max
3. PRIMARY CAPACITANCE (CW/W pF Max): PRI/SEC 28 pF Max
4. TURN RATIO: PIN 1-2/6-7 1CT: 1CT; 11-10/15-16 1CT: 1.25CT
5. HI-POT ISOLATION(Vrms Min): 1500
6. INSERTION LOSS (dB Max) 1-100 MHz: -1.1
7. RISE TIME (ns TYP): 2.5
8. RETURN LOSS (dB Min): 30MHz 40MHz 50MHz 60MHz 80MHz
-16 -14 -13.5 -13 -12
9. CMRR (dB Min) MHz: 0.1-60 = -42TX/40RX; 60-100 =- 40TX/-35RX
10. CROSSTALK (dB Min): 0.1-60 MHz = -40; 60-100MHz = -38

C. SCHEMATIC & DIMENSIONS & MARKING: (UNIT m/m)

