

HSC106D

Sensitive Gate Silicon Controlled Rectifier

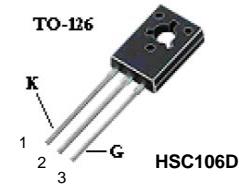
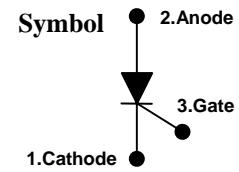
V_{DRM} = 400 V
 $I_{T(RMS)}$ = 4.0A

FEATURES

- Repetitive Peak Off-State Voltage (V_{RM} = 400V)
- R.M.S On-state Current ($I_{T(RMS)}$ =4.0A)
- Average On-state Current ($I_{T(AV)}$ =2.55A)
- Sensitive Gate Triggering (0.2mA_{Max}@25°C)

General Description

Glassivated PNPN devices designed for high volume consumer applications such as temperature, light and speed control ; process and remote control, and warning systems where reliability of operation is important.



Absolute Maximum Ratings (T_j=25°C)

Symbol	Parameter	Value	Units
V_{DRM}	Repetitive Peak Off-State Voltage (Forward)	400	V
V_{RRM}	Repetitive Peak Off-State Voltage (Reverse)	400	V
$I_{T(RMS)}$	On-State R.M.S Current (180° Condition Angles, T _C =80°C)	4.0	A
$I_{T(AV)}$	On-State Average Current (180° Condition Angles, T _C =80°C)	2.55	A
I_{TSM}	Surge On-State Current (1/2 Cycle, 60Hz, Sine Wave, Non-repetitive, T _j = 110 °C)	20	A
I^2t	Circuit Fusing Considerations (t=8.3mS)	1.65	A ² s
P_{GM}	Forward Peak Gate Power Dissipation (Pulse Width ≤1.0μsec, T _c =80°C)	0.5	W
$P_{G(AV)}$	Forward Average Gate Power Dissipation (Pulse Width ≤1.0μsec, T _c =80°C)	0.1	W
V_{GRM}	Reverse Peak Gate Voltage	6.0	V
I_{FGM}	Forward Peak Gate Current (Pulse Width ≤1.0 μsec, T _c =80°C)	0.2	A
T_{STG}	Storage Temperature Range	-40 to +150	°C
T_j	Operating Junction Temperature	-40 to +110	°C

Electrical Characteristics (T_a=25°C)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I _{GT}	Gate Trigger Current ⁽¹⁾	V _{AK} = 6VDC, R _L = 100Ω, T _J = 25°C V _{AK} = 6VDC, R _L = 100Ω, T _J = -40°C		15 35	200 500	uA
V _{GT}	Gate Trigger Voltage ⁽¹⁾	V _{AK} = 6VDC, R _L = 100Ω, T _J = 25°C V _{AK} = 6VDC, R _L = 100Ω, T _J = -40°C	0.4 0.5	0.6 0.75	0.8 1.0	V
V _{GD}	Non Trigger Gate Voltage	V _{AK} = 12VDC, R _L = 100Ω, T _C = 110°C	0.2			V
I _H	Holding Current	V _{AK} = 12VDC, Gate open, Initiating current=20mA, T _J = 25°C T _J = -40°C T _J = 110°C		0.19 0.33 0.07	3.0 6.0 2.0	mA
I _L	Latching Current	V _{AK} = 12VDC, I _G = 20mA, Gate Open, T _J = 25°C T _J = 110°C		0.2 0.35	5.0	mA
I _{DRM} I _{RRM}	Repetitive or Reverse Peak Blocking Current	V _{AK} = V _{DRM} or V _{RRM} , T _C = 25°C T _C = 110°C			10 100	uA uA
V _{TM}	Peak Forward On-State Voltage ⁽²⁾	I _{FM} =1A			2.2	V
dv/dt	Critical Rate of Rise Off state Voltage	V _{AK} = V _{DRM} , Exponential waveform, R _{GK} = 1kΩ, Gate open, T _J =110°C		8.0		V/uS

⁽¹⁾ R_{GK} Current is not included in measurement⁽²⁾ Pulse Test : Pulse width ≤ 2.0mS, Duty Cycle ≤ 2%**Thermal Characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
R _{TH(J-C)}	Thermal Resistance	Junction to Case			3.0	°C/W
R _{TH(J-A)}	Thermal Resistance	Junction to Ambient			75	°C/W
TL	Maximum Lead Temperature for Soldering Purpose 1/8", from case for 10second				260	°C

Performance Curves

Fig 1. Average Current Derating

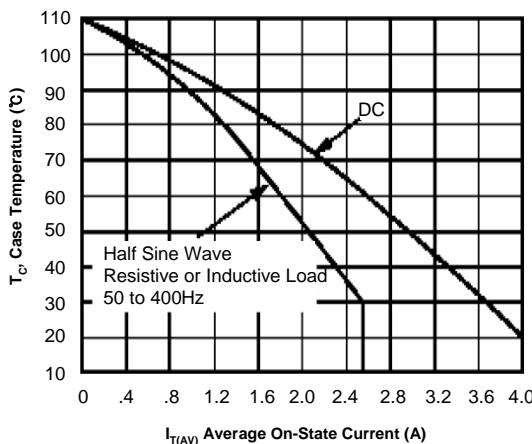


Fig 2. Maximum On-State Power Dissipation

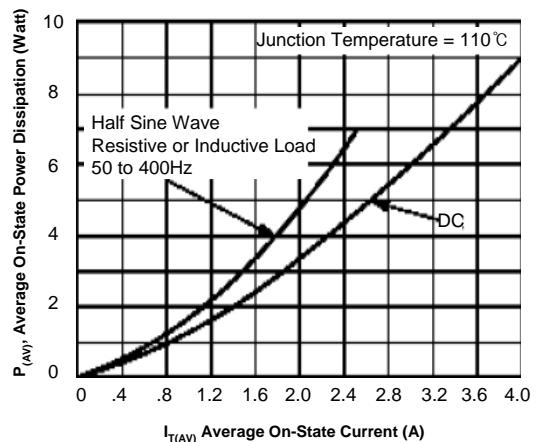


Fig 3. Typical Gate Trigger Current vs Junction Temperature

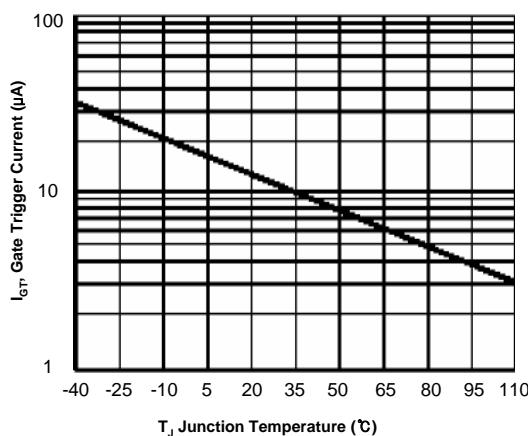


Fig 4. Typical Holding Current vs Junction Temperature

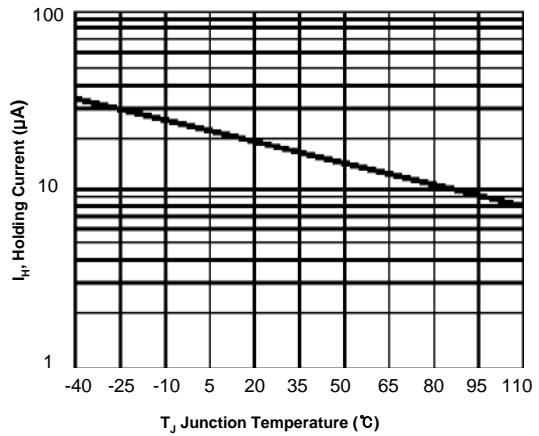


Fig 5. Typical Gate Trigger Voltage vs Junction Temperature

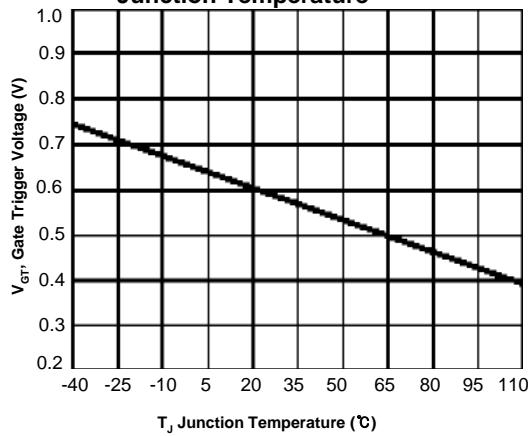
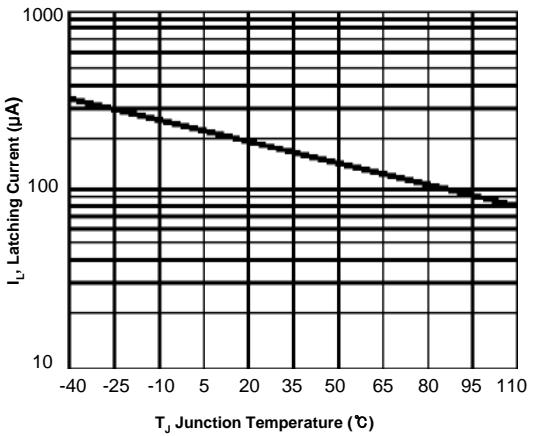
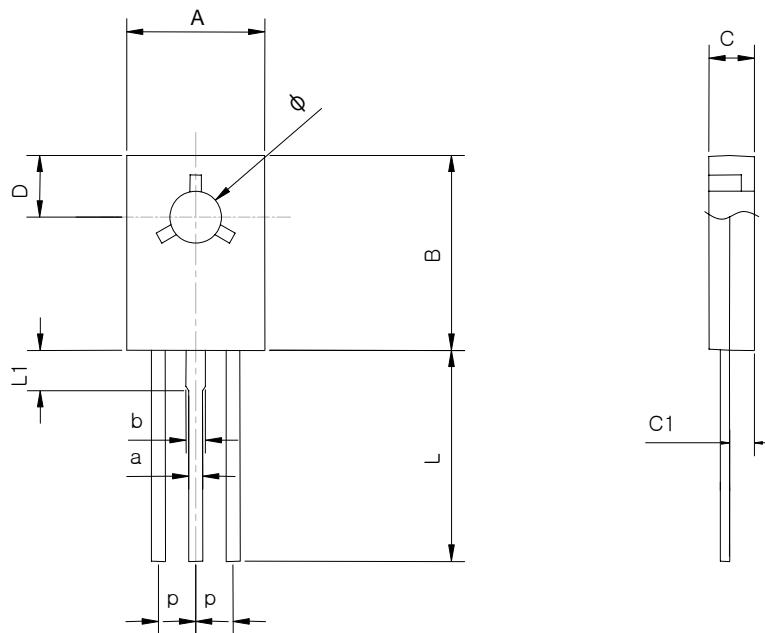


Fig 6. Typical Latching Current vs Junction Temperature



Package Dimension

**HSC106D
(TO-126)**

Dimension Table

Unit : [mm]

Symbol	Dimension		Symbol	Dimension	
	Min	Max		Min	Max
A	-	8.5	L1	2.3	2.7
B	-	12.0	φ	3.0	3.4
C	-	2.8	a	0.7	0.86
C1	1.27 Typ		b	1.2 Typ	
D	3.6	4.0	p		2.3
L	-	13.0			