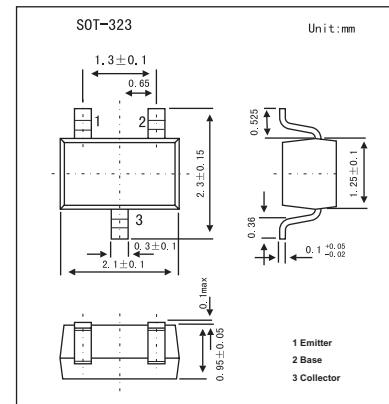


PNP Epitaxial Planar Silicon Transistors

2SA1688

■ Features

- Very small-sized package.
- High power gain.



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-30	V
Collector-emitter voltage	V _{C EO}	-20	V
Emitter-base voltage	V _{EBO}	-5	V
Collector current	I _C	-30	mA
Collector dissipation	P _C	150	W
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector cutoff current	I _{CBO}	V _{CB} = -10V , I _E = 0			-0.1	μA
Emitter cutoff current	I _{EBO}	V _{EB} = -4V , I _C = 0			-0.1	μA
DC current Gain	h _{FE}	V _{CE} = -6V , I _C = -1mA	60		270	
Gain bandwidth product	f _T	V _{CE} = -6V , I _C = -1mA	150	230		MHz
Reverse transfer capacitance	C _{RE}	V _{CB} = -6V , f = 1MHz		1.1	1.7	pF
Base-collector time constant	b _{B'C'C}	V _{CE} = -6V , I _C = -1mA, f = 31.9MHz		11	20	ps
Voltage gain	PG	 Unit (capacitance : F) L1 : 1mmφ plated wire 10mmφ ST, tap : 2T from V _{BE} side L2 : 1mmφ plated wire 10mmφ TT, tap : 1T from V _{CE} side L3 : 1mmφ enamel wire 10mmφ ST V _{CE} = -6V, I _C = -1mA, f = 100MHz		22		dB
Noise figure	NF			2.5		dB

■ h_{FE} Classification

Marking	E		
Rank	3	4	5
h _{FE}	60~120	90~180	135~270