



LIGITEK

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INFRARED EMITTING DIODES

LVIR3333

DATA SHEET

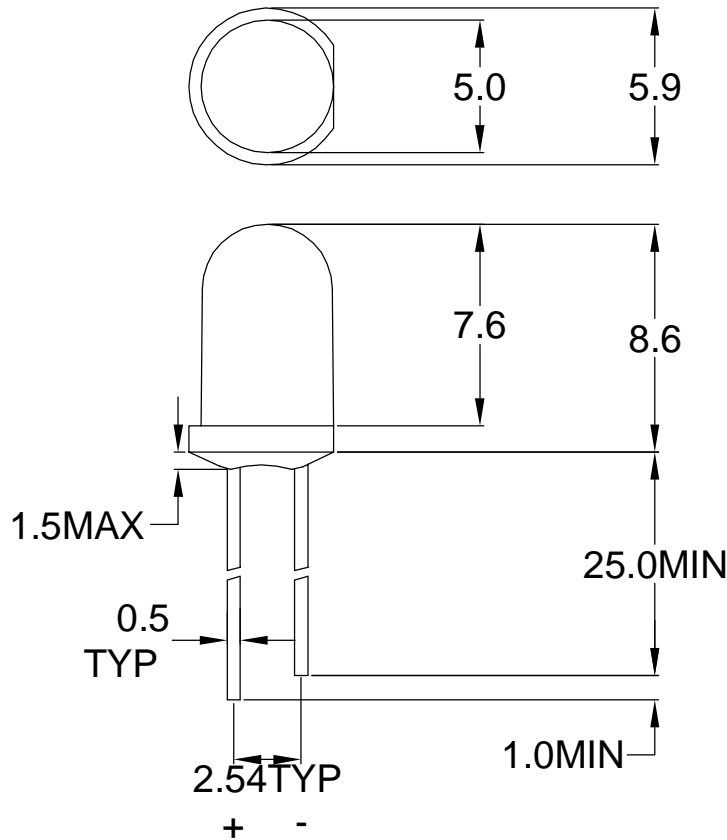
DOC. NO : QW0905-LVIR3333

REV. : A

DATE : 18 - Oct - 2004



### Package Dimensions



Note : 1.All dimension are in millimeter tolerance is  $\pm 0.25\text{mm}$  unless otherwise noted.  
2.Specifications are subject to change without notice.

### Features:

1. High radiant intensity.
2. Suitable for pulsed applications.
3. Low average degradation.

### Descriptions:

The LVIR3331series are high power solution grown efficiency Gallium Arsenide infrared emitting diodes encapsulated in water clear plastic T-1 3/4 package individually

### Device Selection Guide:

PART NO	MATERIAL	LENS COLOR
LVIR3333	GaAIAs	Water Clear

**Absolute Maximum Ratings at Ta=25**

Parameter	Symbol	Ratings	UNIT
		VIR	
Forward Current	IF	50	mA
Peak Forward Current (300PPS,1 μ s Pulse)	IFP	3	A
Power Dissipation	PD	100	mW
Reverse Voltage	Vr	5	V
Operating Temperature	Topr	-55 ~ +100	
Storage Temperature	Tstg	-55 ~ +100	
Soldering Temperature	Tsol	Max 260 for 5 sec Max (2mm from body)	

**Electrical Optical Characteristics (Aa=25 )**

PARAMETER	SYMBOL	Min.	Typ.	Max.	UNIT	TEST CONDITION
Radiant Intensity	Le	10	17		mW/sr	IF=20mA
Aperture Radiant Incidence	Ee	1.2	1.6		mW/cm <sup>2</sup>	IF=20mA
Peak Emission Wavelength	peak		940		nm	IF=20mA
Spectral Line Half Width			50		nm	IF=20mA
Forward Voltage	VF		1.2	1.6	V	IF=20mA
Reverse Current	IR			100	μ A	VR=5V
Viewing Angle	2 1/2		20		deg	

Note : 1. The forward voltage data did not including ±0.1V testing tolerance.  
2. The radiant intensity data did not including ±15% testing tolerance.



### Typical Electro-Optical Characteristics Curve VIR CHIP

Fig.1 Forward Current vs. DC Forward Voltage

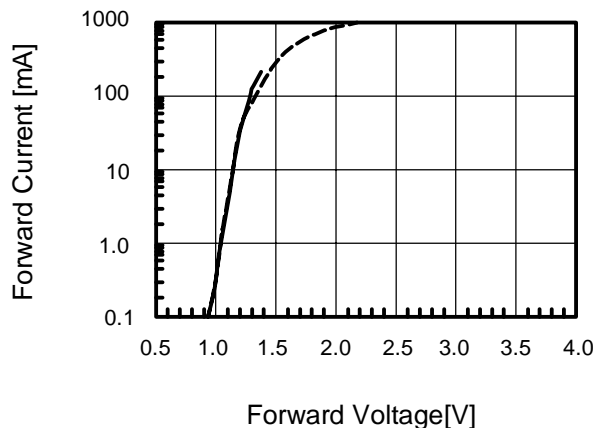


Fig.2 Relative Radian Intensity vs.wavelength

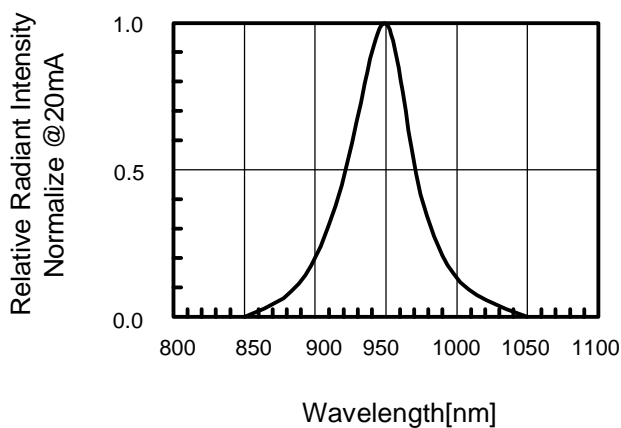


Fig 3. Relative Radiant Power vs. Forward Peak Current

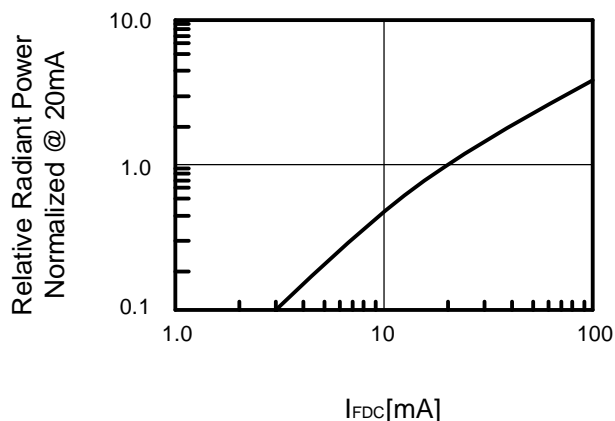


Fig.4 Relative Radiant Power vs. Forward Peak Current

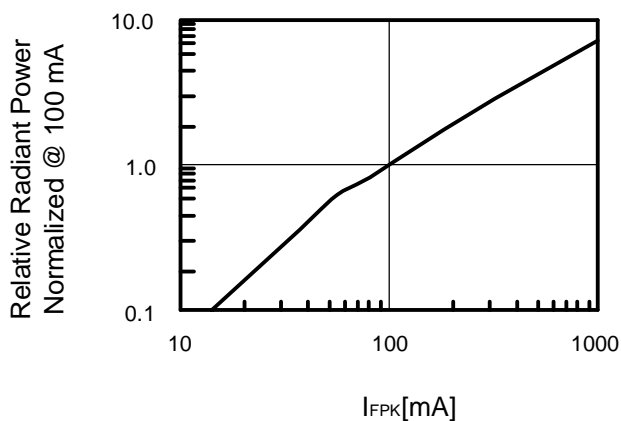


Fig.5 Forward DC Voltage vs. Temperature

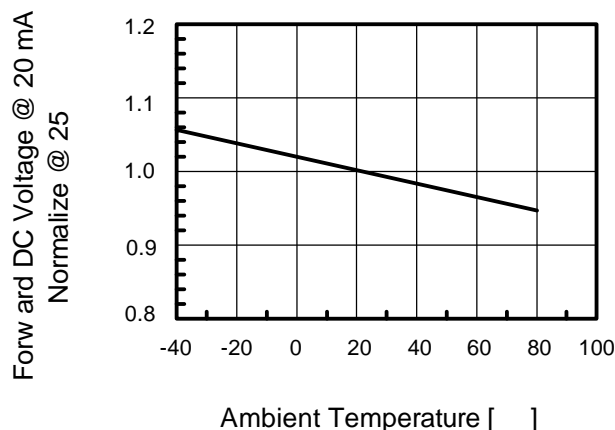
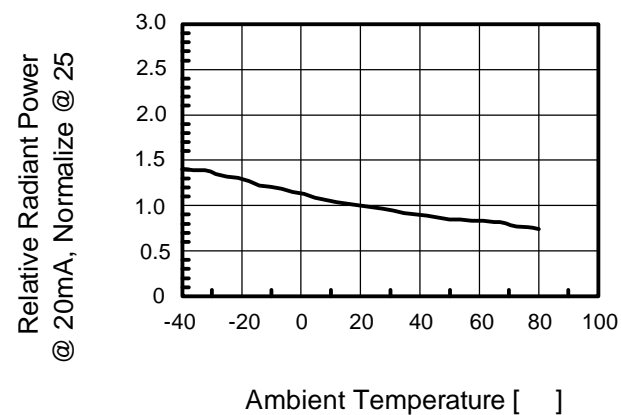


Fig.6 Relative Radiant Power vs. Temperature





Reliability Test:

Test Item	Test Condition	Description	Reference Standard
Operating Life Test	1. Under Room Temperature 2. If=20mA 3. t=1000 hrs (-24hrs, +72hrs)	This test is conducted for the purpose of determining the resistance of a part in electrical and thermal stressed.	MIL-STD-750: 1026 MIL-STD-883: 1005 JIS C 7021: B-1
High Temperature Storage Test	1. Ta=105 ±5 2. t=1000 hrs (-24hrs, +72hrs)	The purpose of this is the resistance of the device which is laid under condition of high temperature for hours.	MIL-STD-883:1008 JIS C 7021: B-10
Low Temperature Storage Test	1. Ta=-40 ±5 2. t=1000 hrs (-24hrs, +72hrs)	The purpose of this is the resistance of the device which is laid under condition of low temperature for hours.	JIS C 7021: B-12
High Temperature High Humidity Test	1. Ta=65 ±5 2. RH=90%~95% 3. t=240hrs±2hrs	The purpose of this test is the resistance of the device under tropical for hours.	MIL-STD-202:103B JIS C 7021: B-11
Thermal Shock Test	1. Ta=105 ±5 & -40 ±5 (10min) (10min) 2. total 10 cycles	The purpose of this is the resistance of the device to sudden extreme changes in high and low temperature.	MIL-STD-202: 107D MIL-STD-750: 1051 MIL-STD-883: 1011
Solder Resistance Test	1. T.Sol=260 ±5 2. Dwell time= 10±1sec.	This test intended to determine the thermal characteristic resistance of the device to sudden exposures at extreme changes in temperature when soldering the lead wire.	MIL-STD-202: 210A MIL-STD-750: 2031 JIS C 7021: A-1
Solderability Test	1. T.Sol=230 ±5 2. Dwell time=5±1sec	This test intended to see soldering well performed or not.	MIL-STD-202: 208D MIL-STD-750: 2026 MIL-STD-883: 2003 JIS C 7021: A-2