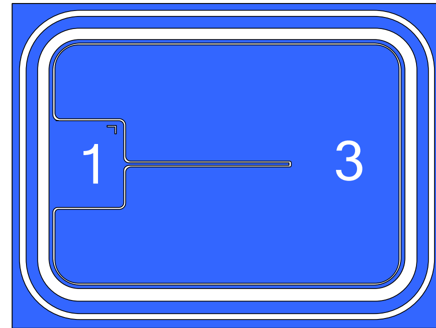
3VD324600YL HIGH VOLTAGE MOSFET CHIPS

DESCRIPTION

- 3VD324600YL is a High voltage N-Channel enhancement mode power MOS-FET chip fabricated in advanced silicon epitaxial planar technology;
- Advanced termination scheme to provide enhanced voltage-blocking capability;
- Avalanche Energy Specified;
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode;
- The chips may packaged in TO-220 type and the typical equivalent product is 4N60A;
- The packaged product is widely used in AC-DC power suppliers, DC-DC converters and H-bridge PWM motor drivers;
- Die size: 3.78mm*2.78mm;
- Chip Thickness: 300±20μm;
- Top metal: Al, Backside Metal: Ag.



1-Gate PAD 3-Source PAD

CHIP TOPOGRAPHY

ABSOLUTE MAXIMUM RATINGS (T_{amb}=25°C)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	±30	V
Drain Current	I_D	4.0	A
Power Dissipation (TO-220 Package)	P_D	106	W
Operation Junction Temperature	T_J	-55～+150	°C
Storage Temperature	Tstg	-55～+150	°C

ELECTRICAL CHARACTERISTICS (T_{amb}=25°C)

Parameter	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Drain -Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250μA	600	-	-	V
Gate Threshold Voltage	V_{TH}	V_{GS}= V_{DS}, I_D=250μA	2.0	-	4.0	V
Drain-Source Leakage Current	I_{DSS}	V_{DS}=600V, V_{GS}=0V	-	-	1.0	μA
Static Drain- Source On State Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=2.0A	-	-	2.1	Ω
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0V	-	-	±100	nA
Source-Drain Diode Forward on Voltage	V_{FSD}	I_S=4.0A, V_{GS}=0V	-	-	1.4	V