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PRODUCT CMOS Type Series Regulator

TYPE

BH6733HFV

O BLOCK DIAGRAM and APPLICATION CIRCUIT



	O PIN DESC	RIPTION		
ouт 스 UT	Pin No.	Pin Name	Description	
	1	VIN	Input Pin	
	2	N.C.	No Connect	
	3	VOUT	Output Pin	
	4	SS	Slow Start Pin	
	5	GND	Ground Pin	
S	6	стру	Output Control Pin	
	0	SIDI	(High:ON, Low:OFF)	

CIN = 4.7 μ F (Ceramic) COUT = 4.7 μ F (Ceramic) CSS = 4700pF (Ceramic)

Fig.1 Block Diagram and Application Circuit

O ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Parameter	Symbol	Limit	Unit
Power Supply Voltage	VMAX	-0.3 ~ +6.5	V
Power Dissipation	Pd	680 (*1)	mW
Maximum junction temperature	TjMAX	+125	°C
Operating Temperature Range	Topr	-40 ~ +85	°C
Storage Temperature Range	Tstg	-55 ~ +125	S°

(*1) Pd derated at $6.8 \text{mW/}^{\circ}\text{C}$ for temperature above Ta=25°C,

mounted on 70mm × 70mm × 1.6mm glass-epoxy PCB.

Status of this document

The Japanese version of this document is the official specification.

Please use the translation version of this document as a reference to expedite understanding of the official version. If there is any uncertainty in translation version of this document, official version takes priority.

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O OPERATING RANGE (Operating Condition are Limited by Pd)

Parameter	Symbol	Limit	Unit
Power Supply Voltage	VIN	2.5 ~ 5.5	V
Maximum Output Current	IMAX	300	mA

O ELECTRICAL CHARACTERISTICS (Ta=25°C, VIN=STBY=5.0V, CIN=4.7 µ F, COUT=4.7 µ F, CSS=4700pF unless otherwise noted.)

Parameter		Symphol	Limit			الم ال	Conditions
		Symbol	Min	Тур	Max	Unit	Conditions
[Regulator]							
Output Voltag	e	VOUT	3.234	3.300	3.366	V	IOUT=0.1mA
Circuit Currer	nt	ICC	1	90	140	μA	IOUT=0mA, VIN Pin Monitor
Circuit Current (STBY)		ISTBY	I	Ι	1.0	μA	STBY=0V
Ripple Rejection Ratio		R.R.	55	70	Ι	dB	VIN=5.0V, VRR=-10dBv, fRR=1kHz, IOUT=10mA
Dropout Voltage		VSAT	1	0.7	1.0	V	VIN=0.95*VOUT, IOUT=300mA
Line Regulation		VDL	1	2	10	mV	VIN=VOUT+1.0V to 5.5V, IOUT=0.1mA
Load Regulation		VDLO	-	20	100	mV	IOUT=1mA to 300mA
[Over Current Protection]							
Limit Current		ILMAX	350	600	Ι	mA	Vo=VOUT*0.9
Short Current		ISHORT	50	240	420	mA	Vo=0V
[Stand-by]							
STBY Pull Down Resistor		RSTBY	675	1350	2000	kΩ	
Discharge Resistor		RDSCG	0.8	1.2	2.0	kΩ	VIN=5.0V, STBY=0V
STBY	ON	VSTBH	2.5	Ι	5.5	V	VIN=5.0V, STBY=L \rightarrow H STBY Threshold Voltage
Control Voltage	OFF	VSTBL	-0.3	-	1.1	V	VIN=5.0V, STBY=H \rightarrow LSTBY Threshold Voltage

•This product is not designed for protection against radio active rays.

O OPERATING CONDITION

Parameter	Symbol	MIn	Тур	Max	Unit	Condition
Input Capacitor	CIN	2.2(*2)	4.7	-	μF	Ceramic Capacitor Recommended
Output Capacitor	COUT	2.2(*2)	4.7	_	μF	Ceramic Capacitor Recommended

(*2) Make sure that the output capacitor value is not kept lower than this specified level across a variety of temperature, DC bias characteristic. And also make sure that the capacitor value cannot change as time progresses.



O POWER DISSIPATION REDUCATION



Fig.2 Power Dissipation Reduction

O PACKAGE DIMENSIONS (HVSOF6)



[unit : mm]





O ORERATION NOTES

1.) Absolute Maximum Ratings

This product is produced with strict quality control, however, may be destroyed if it is operated beyond its absolute maximum ratings. If the device is destroyed in exceeding the recommended maximum ratings, the failure mode will be difficult to determine (E.g. short mode, open mode). Therefore, physical protection counter-measures (like fuse) should be implemented when operating conditions are beyond the absolute maximum ratings specified.

2.) GND Potential

GND potential must be the lowest potential no matter what may happen. Actually, including transitional states, all pins except GND must not be the voltage below GND.

3.) Setting of Heat

Consider Pd of actually using states, carry out the heat design that have adequate margin.

4.) Pin Short and Mistake Fitting

When mounting the IC on the PCB, pay attention to the orientation of the IC. If there is a placement mistake, the IC may be burned up.

5.) Actions in Strong Magnetic Field

Using the IC within a strong magnetic field may cause a malfunction.

6.) Mutual Impedance

Use short and wide wiring tracks for the power supply and ground to keep the mutual impedance as small as possible. Use a capacitor to keep ripple to a minimum.

7.) Voltage of STBY Pin

For standby mode, set STBY voltage below 1.1V. For normal operation, set the pin voltage beyond 2.5V. It is not recommended to set STBY voltage between 1.1V and 2.5V, and it may cause improper operation. And, please be sure to turn STBY Control Voltage ON from OFF after applying Operating Voltage on VIN pin.

8.) Over Current Protection Circuit

Over current and short circuit protection is built-in at the output, and IC destruction is prevented at the time of load short circuit. These protection circuits is effective in the destructive prevention by the sudden accident, please avoid use to which a protection circuit operates continuously.

9.) Thermal Shutdown

This IC has Thermal Shutdown Circuit (TSD Circuit). When the temperature of IC Chip is higher than 175°C, the output turned off by TSD Circuit. TSD Circuit is only designed for protecting IC from thermal over load, therefore it is not recommended that you design the application as TSD working in normally condition.

10.) SS Pin

SS pin can drive quite small current, because the pin is directly connected to reference voltage circuit. It may be that output voltage is dropping when the load of SS pin is more than 100nA. If the pin is connected to a capacitor, please use a ceramic capacitor for small leak current. Please take care that output noise is smaller as SS pin capacitor is larger, but startup time is longer.

11.) Input / Output Capacitor

Mounting input capacitor between input pin and GND (as close to pin as possible), and also output capacitor between output pin and GND (as close to pin as possible) is recommended. The input capacitor reduces the output impedance of the voltage supply source connected to the VIN. The higher value the output capacitor goes, the more stable the whole operation becomes. This leads to high load transient response. Please confirm the whole operation on actual application board. Fig4 is the relation of IOUT and ESR.. Please confirm the whole operation on actual application board. Generally, ceramic capacitor has wide range of tolerance, temperature coefficient, and DC bias characteristic. And also its value goes lower as time progresses. Please choose ceramic capacitors after obtaining more detailed data by asking capacitor makers.



Fig.4 Stable region (Example)

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