RoHS



# Vishay General Semiconductor

## **Surface Mount Ultrafast Plastic Rectifier**

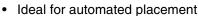


DO-214AA (SMB)

PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub> 2.0 A						
V <sub>RRM</sub>	50 V to 200 V					
I <sub>FSM</sub>	50 A					
t <sub>rr</sub>	20 ns					
$V_{F}$	0.90 V					
T <sub>J</sub> max.	150 °C					

#### **FEATURES**

Glass passivated chip junction



- · Ultrafast recovery times for high efficiency
- · Low forward voltage, low power losses
- High forward surge capability

Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **TYPICAL APPLICATIONS**

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer, automotive and telecommunication.

#### **MECHANICAL DATA**

Case: DO-214AA (SMB)

Epoxy meets UL 94V-0 flammability rating

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test, HE3 suffix for high reliability grade (AEC Q101 qualified), meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	ES2A	ES2B	ES2C	ES2D	UNIT	
Device marking code		EA	EB	EC	ED		
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	100	150	200	V	
Maximum RMS voltage	$V_{RMS}$	35	70	105	140	V	
Maximum DC blocking voltage	$V_{DC}$	50	100	150	200	٧	
Maximum average forward rectified current at $T_L = 110 ^{\circ}\text{C}$	I <sub>F(AV)</sub>	2.0					
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	50				А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150 °					

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS SYMBOL ES2A ES2B ES2C ES2D						ES2D	UNIT
Maximum instantaneous forward voltage <sup>(1)</sup>	2.0 A		V <sub>F</sub>	0.90				٧
Maximum DC reverse current at rated DC blocking voltage		T <sub>A</sub> = 25 °C T <sub>A</sub> = 100 °C	I <sub>R</sub>	10 350				μΑ

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	ES2A	ES2B	ES2C	ES2D	UNIT
Max. reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A},$ $I_{rr} = 0.25 \text{ A}$		t <sub>rr</sub>	20				ns
Maximum reverse recovery time	$I_F = 2.0 \text{ A}, V_R = 30 \text{ V},$ $dI/dt = 50 \text{ A}/\mu\text{s},$ $I_r = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C T <sub>J</sub> = 100 °C	t <sub>rr</sub>	30 50				ns
Maximum stored charge	$I_F = 2.0 \text{ A}, V_R = 30 \text{ V},$ $dI/dt = 50 \text{ A}/\mu\text{s},$ $I_r = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C T <sub>J</sub> = 100 °C	Q <sub>rr</sub>	10 25			nC	
Typical junction capacitance	4.0 V, 1 MHz		CJ	18			pF	

#### Note:

(1) Pulse test: 300 ms pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER SYMBOL ES2A ES2B ES2C ES2D UNI						
Typical thermal resistance (1)	$R_{ hetaJA} \ R_{ hetaJL}$			°C/W		

#### Note:

(1) Units mounted on P.C.B. 5.0 x 5.0 mm (0.013 mm thick) land areas

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
ES2D-E3/52T	0.096	52T	750	7" diameter plastic tape and reel			
ES2D-E3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel			
ES2DHE3/52T (1)	0.096	52T	750	7" diameter plastic tape and reel			
ES2DHE3/5BT (1)	0.096	5BT	3200	13" diameter plastic tape and reel			

### Note:

(1) Automotive grade AEC Q101 qualified

### **RATINGS AND CHARACTERISTICS CURVES**

 $(T_A = 25 \, ^{\circ}C \text{ unless otherwise noted})$ 

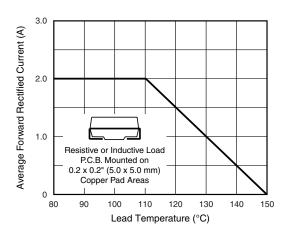


Figure 1. Maximum Forward Current Derating Curve

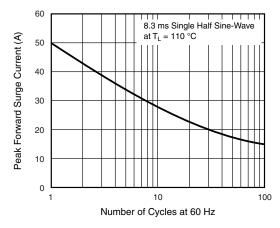


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current



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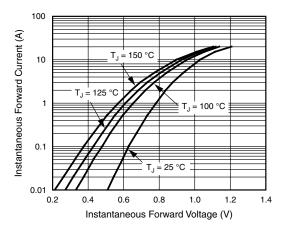


Figure 3. Typical Instantaneous Forward Characteristics

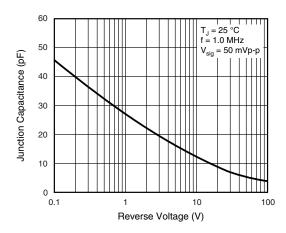


Figure 5. Typical Junction Capacitance

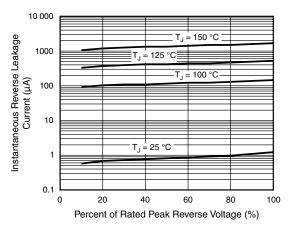
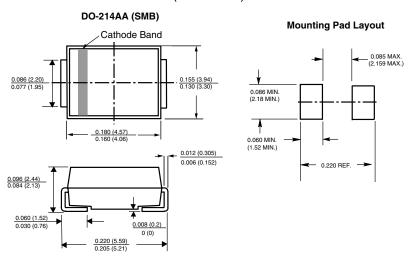


Figure 4. Typical Reverse Leakage Characteristics

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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