

Low Operating Current Fundamental Quartz Crystal Oscillator IC

■GENERAL DESCRIPTION

The NJU6215 series is a C-MOS quartz crystal oscillator IC realized excellent frequency stability for fundamental (up to 60MHz) oscillation, and consists of an oscillation amplifier, 4-stage divider, 3-state output buffer and a built-in LDO.

The operating voltage is from 1.62V to 3.63V, and the LDO holds down the characteristic change of the oscillation amplifier for operating voltage variation, and has been stabilized oscillation frequency.

The 4-stage divider generates only one frequency selected of $f_0, f_0/2, f_0/4, f_0/8$ and $f_0/16$ internal circuits is output.

The 3-state output buffer is C-MOS compatible of high fan-out.

The NJU6215 is suitable for the battery drive application such as cellular phones, because it has achieved low operating current by optimizing the tristate buffer.

■FEATURES

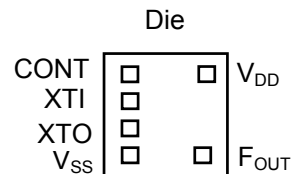
- Low Operating Current 1mA typ.@40MHz/1.8V/5pF
- Frequency Stability $\pm 1\text{ppm}@V_{DD}\pm 10\%$
- Operating Voltage 1.62 to 3.63V
- Maximum Oscillation Frequency Up to 50MHz
- 4-Stage Divider Maximum Divider $f_0/16$
- Built-in LDO
- Oscillation Stop and Output Stand-by Function
- 3-State Output Buffer
- Variable Pull-up Resistance on-Die
- Oscillation Capacitors C_g and C_d on-Die
- Package Outline Die/Wafer
- C-MOS Technology

■PACKAGE OUTLINE



NJU6215XC-X

■PAD LOCATION



■LINE-UP TABLE

Type No.	F_{OUT}	Internal Connect			Cg/Cd	
		Short	Open	N		
NJU6215	A	f_0	A	B	-	9.3/10.7pF
	*B	$f_0/2$	B	A	2	9.3/10.7pF
	*C	$f_0/4$	B	A	4	9.3/10.7pF
	*D	$f_0/8$	B	A	8	9.3/10.7pF
	*E	$f_0/16$	B	A	16	9.3/10.7pF

* Under Development

■EXAMPLE OF PART NUMBER

- 1) NJU6212AW-L
 $F_{OUT}=f_0$, Wafer Thickness=140um
- 2) NJU6212CC-L
 $F_{OUT}=f_0/4$, Die Thickness=140um

■COORDINATES

No	Pad Name	X	Y
1	CONT	-189	231
2	XTI	-189	77
3	XTO	-189	-77
4	V_{SS}	-189	-231
5	F_{OUT}	215	-231
6	V_{DD}	222	231

Starting Point: Die Center Unit[um]

Die Size: 0.70x0.75mm

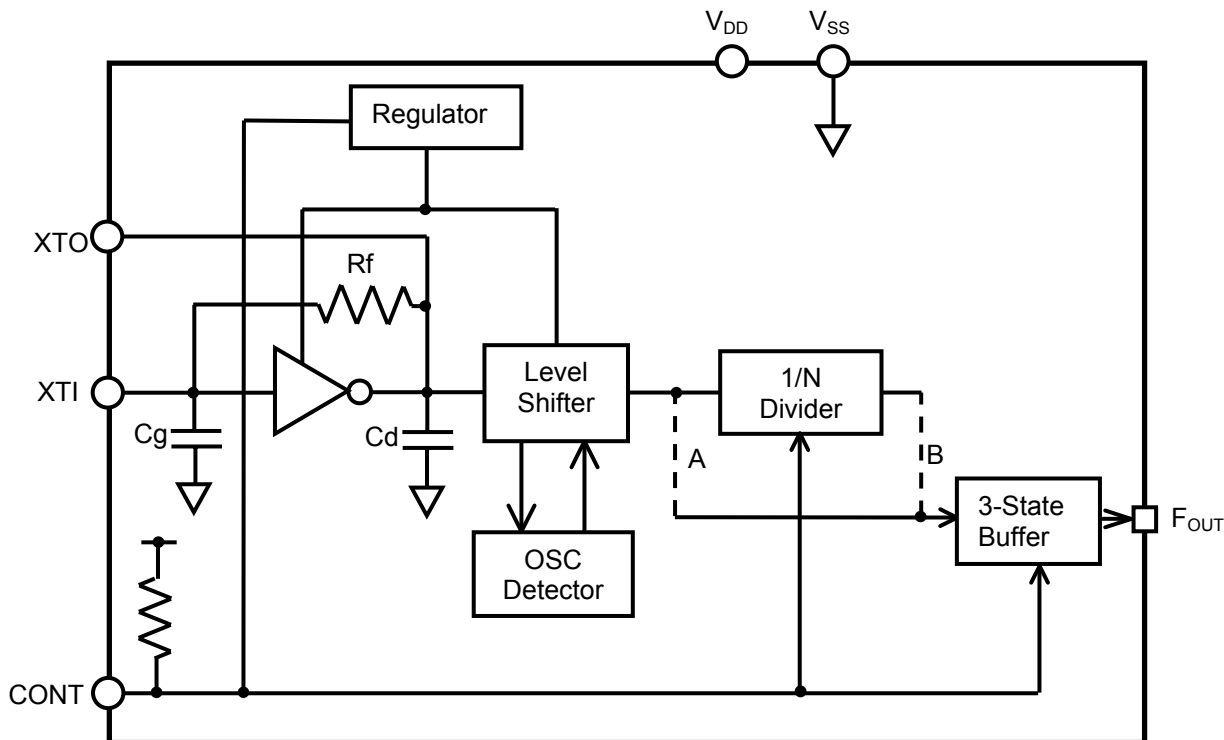
Die Thickness (C-L): $140\pm 10\text{um}$

Wafer Thickness (W-L): $140\pm 10\text{um}$

Pad size: 90x90um

Die Substrate: V_{SS} level

■BLOCK DIAGRAM



■TERMINAL DESCRIPTION

SYMBOL	FUNCTION
CONT	Oscillation and 3-state Output Buffer Control
	CONT F _{OUT}
	H or OPEN Output either one frequency selected of f ₀ , f ₀ /2, f ₀ /4, f ₀ /8 and f ₀ /16 (Note1)
	L Oscillation Stop and High impedance Output
XTI XTO	Quartz Crystal Connecting Terminals
V _{SS}	V _{SS} =0V : GND
F _{OUT}	Frequency Output
V _{DD}	V _{DD} =1.62 to 3.63V

Note1) Refer to the line-up table.

■ABSOLUTE MAXIMUM RATINGS

(V_{SS}=0V, Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V _{DD}	-0.5 to +7.0	V
Input Voltage	V _{IN}	-0.5 to V _{DD} +0.5	V
Output Voltage	V _O	-0.5 to V _{DD} +0.5	V
Input Current	I _{IN}	±10	mA
Output Current	I _O	±25	mA
Operating Temperature Range	T _{opr}	-40 to +85	°C
Storage Temperature Range	T _{stg}	-55 to +125	°C

Note2) If the supply voltage(V_{DD}) is less than 7.0V, the input voltage must not over the V_{DD} level though 7.0V is limit specified.

Note3) Decoupling capacitor should be connected between V_{DD} and V_{SS} due to the stabilized operation for the circuit.

■ ELECTRICAL CHARACTERISTICS

(Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Voltage	V _{DD}		1.62		3.63	V
Output Frequency Stability	df/f	V _{DD} ±10%		±1		ppm

(V_{DD}=1.8V, Ta=+25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I _{DD}	A version, f ₀ =50MHz, C _L =5pF		1.4	1.65	mA
Oscillation Stopping Current	I _{STB}	CONT=V _{SS} , No load			1	uA
Input Voltage	V _{IH}		1.26		1.8	V
	V _{IL}		0		0.54	V
Output Voltage	V _{OH}	I _{OH} =0.6mA	1.62			V
	V _{OL}	I _{OL} =0.6mA			0.18	V
Input Current	I _{IN}	CONT=0.8V _{DD}		1.9	2.7	uA
		CONT=0.2V _{DD}		0.1	0.25	uA
3-state Off Leakage Current	I _{OZ}	CONT=V _{SS} , F _{OUT} = V _{DD} or V _{SS}			±0.1	uA
Feedback Resistance	R _f			270		kΩ
Internal Capacitor	C _g	fosc=50MHz		9.3		pF
Internal Capacitor	C _d	fosc=50MHz		10.7		pF
Oscillation Frequency	fosc	Recommendation Note4)			50	MHz
Output Signal Symmetry	SYM	C _L =5pF, @V _{DD} /2	45	50	55	%
Output Signal Rise Time	t _r	C _L =5pF, 0.1V _{DD} to 0.9V _{DD}		3.2	5.0	ns
Output Signal Fall Time	t _f	C _L =5pF, 0.9V _{DD} to 0.1V _{DD}		3.2	5.0	ns
Output Disable time	t _{POZ}	C _L =5pF, R _L =1kΩ			100	ns
Output Enable Time	t _{PZO}	C _L =5pF			1	ms

Note4) The oscillation frequency range has used NJRC's standard crystal for measurement. However it is not guaranteed. (Refer to EXAMPLE OF CRYSTAL PARAMETERS FOR MEASUREMENT CIRCUITS)

($V_{DD}=2.5V, T_a=+25^{\circ}C$)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I_{DD}	A version, $f_0=50MHz, C_L=5pF$		1.8	2.3	mA
Oscillation Stopping Current	I_{STB}	CONT= V_{SS} , No load			1	μA
Input Voltage	V_{IH}		1.75		2.5	V
	V_{IL}		0		0.75	V
Output Voltage	V_{OH}	$I_{OH}=0.8mA$	2.25			V
	V_{OL}	$I_{OL}=0.8mA$			0.25	V
Input Current	I_{IN}	CONT= $0.8V_{DD}$		4.2	9.0	μA
		CONT= $0.2V_{DD}$		0.3	0.6	μA
3-state Off Leakage Current	I_{OZ}	CONT= V_{SS} , $F_{OUT}=V_{DD}$ or V_{SS}			± 0.1	μA
Feedback Resistance	R_f			270		k Ω
Internal Capacitor	C_g	fosc=50MHz		9.3		pF
Internal Capacitor	C_d	fosc=50MHz		10.7		pF
Oscillation Frequency	fosc	Recommendation Note4)			50	MHz
Output Signal Symmetry	SYM	$C_L=5pF, @V_{DD}/2$	45	50	55	%
Output Signal Rise Time	t_r	$C_L=5pF, 0.1V_{DD}$ to $0.9V_{DD}$		2.2	3.7	ns
Output Signal Fall Time	t_f	$C_L=5pF, 0.9V_{DD}$ to $0.1V_{DD}$		2.2	3.7	ns
Output Disable time	t_{POZ}	$C_L=5pF, R_L=1k\Omega$			100	ns
Output Enable Time	t_{PZO}	$C_L=5pF$			1	ms

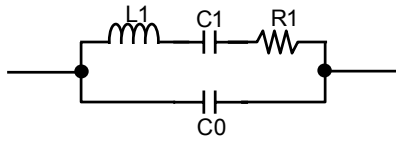
Note4) The oscillation frequency range has used NJRC's standard crystal for measurement. However it is not guaranteed. (Refer to EXAMPLE OF CRYSTAL PARAMETERS FOR MEASUREMENT CIRCUITS)

($V_{DD}=3.3V, T_a=+25^{\circ}C$)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I_{DD}	A version, $f_0=50MHz, C_L=5pF$		2.4	3.0	mA
Oscillation Stopping Current	I_{STB}	CONT= V_{SS} , No load			1	μA
Input Voltage	V_{IH}		2.31		3.3	V
	V_{IL}		0		0.99	V
Output Voltage	V_{OH}	$I_{OH}=1.2mA$	2.97			V
	V_{OL}	$I_{OL}=1.2mA$			0.33	V
Input Current	I_{IN}	CONT= $0.8V_{DD}$		7.8	16.0	μA
		CONT= $0.2V_{DD}$		0.6	1.2	μA
3-state Off Leakage Current	I_{OZ}	CONT= V_{SS} , $F_{OUT}=V_{DD}$ or V_{SS}			± 0.1	μA
Feedback Resistance	R_f			270		k Ω
Internal Capacitor	C_g	$f_{osc}=50MHz$		9.3		pF
Internal Capacitor	C_d	$f_{osc}=50MHz$		10.7		pF
Oscillation Frequency	f_{osc}	Recommendation Note4)			50	MHz
Output Signal Symmetry	SYM	$C_L=5pF, @V_{DD}/2$	45	50	55	%
Output Signal Rise Time	t_r	$C_L=5pF, 0.1V_{DD}$ to $0.9V_{DD}$		1.8	3.0	ns
Output Signal Fall Time	t_f	$C_L=5pF, 0.9V_{DD}$ to $0.1V_{DD}$		1.8	3.0	ns
Output Disable time	t_{POZ}	$C_L=5pF, R_L=1k\Omega$			100	ns
Output Enable Time	t_{PZO}	$C_L=5pF$			1	ms

Note4) The oscillation frequency range has used NJRC's standard crystal for measurement. However it is not guaranteed. (Refer to EXAMPLE OF CRYSTAL PARAMETERS FOR MEASUREMENT CIRCUITS)

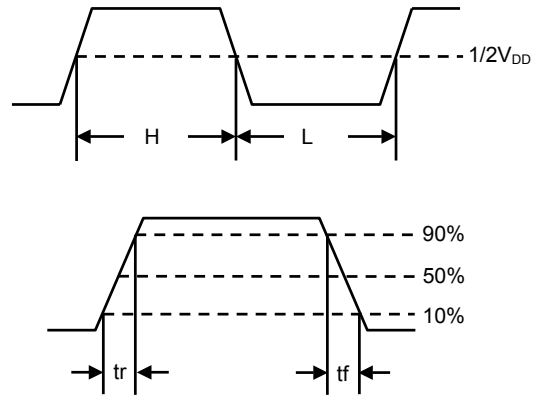
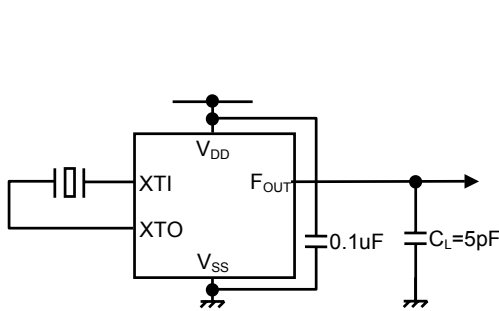
EXAMPLE OF CRYSTAL PARAMETERS FOR MEASUREMENT CIRCUITS



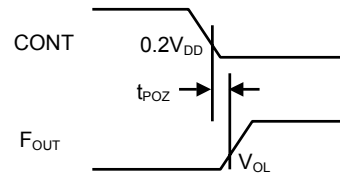
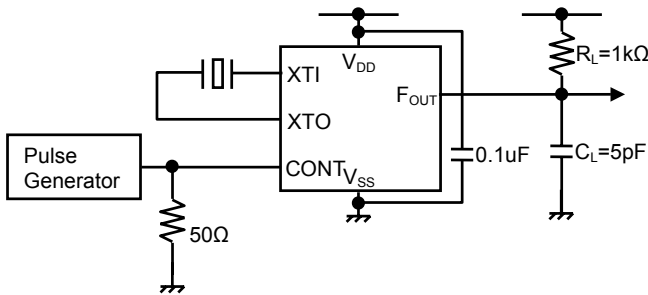
f[MHz]	R1[Ω]	L1[mH]	C1[fF]	C0[pF]
50	18.21	4.34	2.34	0.96

MEASUREMENT CIRCUITS

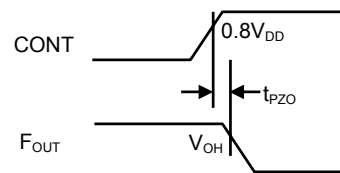
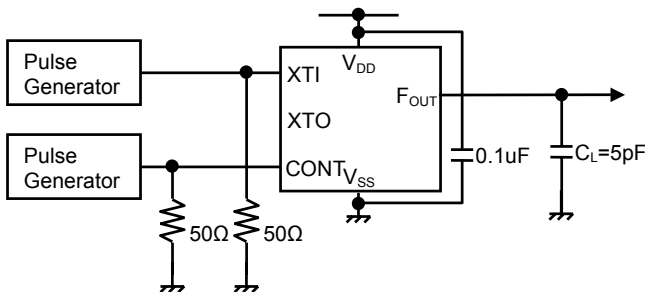
(1) Operating Current, Output Signal Symmetry, Output Signal Rise/Fall Time ($C_L=5\text{pF}$)



(2) Output Disable Time ($C_L=5\text{pF}, R_L=1\text{k}\Omega$)



(3) Output Enable Time ($C_L=5\text{pF}$)



[CAUTION]

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