

### Features

- 1 dBm Output Level at 5797.5 MHz
- Channel Step Size : 1250 kHz
- 1/2<sup>nd</sup> Harmonic : < -10 dBc
- Spurious Level : < -50 dBc
- Lock Time : < 100 us
- 40 mA Current Consumption

### Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.

### Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		5797.5	
Output Power	dBm	-2	1	3
Supply Voltage	V	4.75	5	5.25
Current Consumption	mA		40	45
Channel Step Size	kHz		1250	
1/2 <sup>nd</sup> Harmonics ( 1/2* Fo)	dBc		-20	-10
Spurious Level	dBc		-60	-50
Lock Time	us		40	100
Reference Frequency	MHz		20	
Reference Input Level	dBm			
Phase Noise (C / N)				
@ 1 kHz			-80	-70
@ 10 kHz	dBc/Hz		-80	-70
@ 100 kHz			-95	-90
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	25	80
Package Type & Size	mm	SMT, 15.0W×15.0L×3.5H		

- 1) Measurement conditions are as follows: T = 25°C, V<sub>CC</sub> = 5 V, Freq. = 5797.5 MHz, 50 ohm system.
- 2) Frequency stability of internal ref. TCXO is +/- 2.5 ppm (max.) over the operating temperature range.

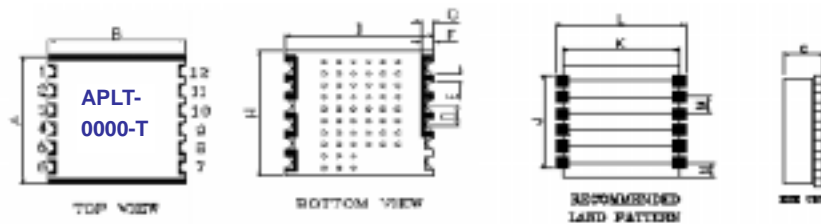
### More Information

Website: [www.asb.co.kr](http://www.asb.co.kr)  
E-mail: [sales@asb.co.kr](mailto:sales@asb.co.kr)

Tel: (82) 42-528-7223  
Fax: (82) 42-528-7222

ASB Inc., 4th Fl. Venture Town Bldg., 367-17 Goijeong-Dong, Seo-Gu, Daejeon 302-716, Korea

### Outline Drawing



Dimension													
Symbol	A	B	C	D	E	F	G	H	I	J	K	L	M
Mm	14.60	14.60	3.50	2.03	0.51	0.61	1.50	15.24	15.24	11.68	14.02	17.07	2.54
	±0.1	±0.1						±0.2	±0.2				

Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	CLOCK	7	VCC (VCO)
2	DATA	10	RF OUT
3	ENABLE	11	VCC (PLL)
4	LOCK DETECT	12	GND
All other Pins are Grounded			