



## **Features GSIB-5S** 4.6 ±0.2 Glass passivated chip junction Ideal for printed circuit board 3.6 ±0.2 $-30 \pm 0.3$ High surge current capability ±0.2 ±0.2 High case dielectric strength 3.2 This series is UL listed under Recognized Component Index, 3.5 20 ± 0.3 file number E185029 11 ±0.2 S 2.5 ±0.2 4 ±0.2 17 -5 ±0.5 2.2 ±0.2 **Mechanical Data** Terminal: Plated leads solderable per MIL-STD 202E, 1±0.1 0.7 ±0.1 7.5 Method 208C -2.7 ±0.2 $10 \pm 0.2$ ±0.2 ±0.2 Case: UL-94 Class V-0 recognized Flame Retardant Epoxy Polarity: Polarity symbol marked on body Mounting position: any **Dimensions in millimeters**

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(single-phase, half -wave, 60HZ, resistive or inductive load rating at 25°C, unless otherwise stated, for capacitive load, derate current by 20%)

	Symbol	GSIB6 A05	GSIB6 A10	GSIB6 A20	GSIB6 A40	GSIB6 A60	GSIB6 A80	GSIB6 A100	units
Maximum repetitive peak reverse voltage	Vrrm	50	100	200	400	600	800	1000	V
Maximum RMS voltage	Vrms	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	Vdc	50	100	200	400	600	800	1000	V
Maximum average forward $Tc = 100^{\circ}C$ (Note 1)Rectified output current at $Ta = 25^{\circ}C$ (Note 2)	lf(av)	6.0 2.8							А
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method)	lfsm	150							A
Maximum instantaneous forward voltage drop per leg at 3.0A	Vf	1.0						V	
Rating for fusing (t < 8.3ms)	l²t	93						A <sup>2</sup> Se	
Maximum DC reverse current at rated DC blocking voltage per legTa = $25$ °C Ta = $125$ °C	Ir	10.0 250							μΑ
Maximum thermal resistance per leg (Note2) (Note1)	Rth(ja) Rth(jc)	22.0 3.4							°C/V
Operating junction and storage temperature range	Tj, Tstg	-55 to +150							°C

Note:

1. Unit case mounted onAl plate heatsink

2. Unit case mounted on P.C.B. with 0.5 x 0.5" (12 x 12mm) copper peads and 0.375" (9.5mm) lead length

3. Recommended mounting position is to bolt down on heatsink with silicone thermal compound for maximum heat transfer with #6 screw

## RATINGS AND CHARACTERISTIC CURVES GSIB6A05 THRU GSIB6A100

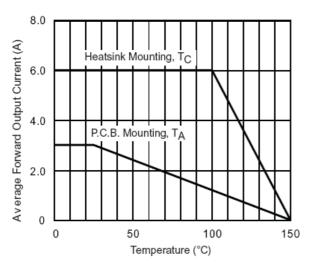


Figure 1. Derating Curve Output Rectified Current

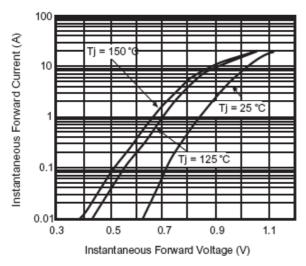


Figure 3. Typical Forward Characteristics Per Leg

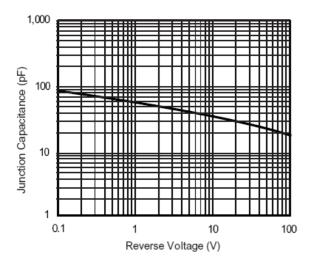


Figure 5. Typical Junction Capacitance Per Leg

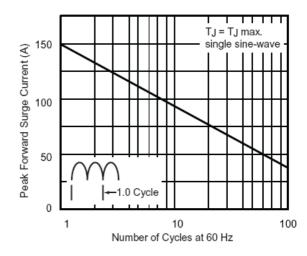


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Leg

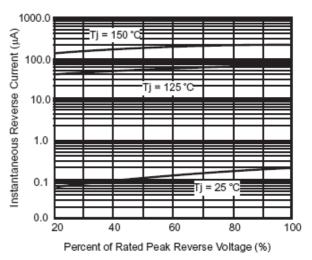


Figure 4. Typical Reverse Characteristics Per Leg

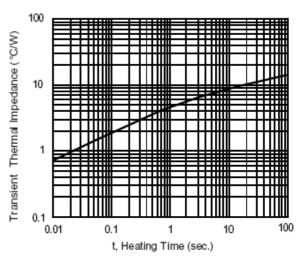


Figure 6. Typical Transient Thermal Impedance

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