STD37N05TZ

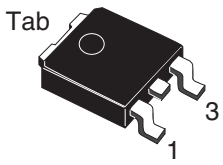
NPN power TRILINTON™

Features

- Integrated high voltage active clamping Zener
- Clamping energy capability 100% tested
- Very high current gain

Applications

- Engine ignition control
- Switching regulators
- Motor control
- Light ballast



DPAK

Description

The STD37N05TZ is a planar, monolithic, high voltage power TRILINTON™ with a built-in active Zener clamping circuit. This device has been specifically designed for unclamped, inductive applications such as ignition systems, switching regulators, and wherever high voltage and high robustness is required.

Figure 1. Internal schematic diagram

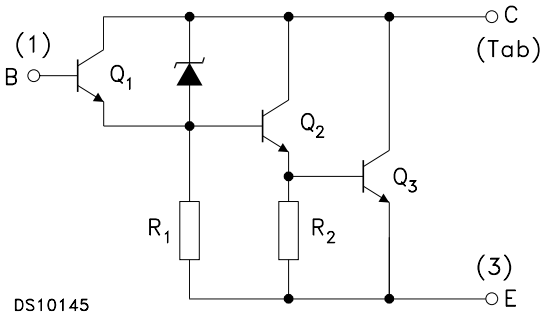


Table 1. Device summary

Order code	Marking	Package	Packaging
STD37N05TZT4	D37N05TZ	DPAK	Tape and reel

1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage (V_{BE} = 0)	370	V
V_{EBO}	Emitter-base voltage (I_C = 0)	13	V
I_C	Collector current	5	A
I_{CM}	Collector peak current (t_P < 5 ms)	8	A
I_B	Base current	1	A
P_{tot}	Total dissipation at T_c = 25 °C	45	W
T_{stg}	Storage temperature	-65 to 150	°C
T_J	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
$R_{thj\text{-}case}$	Thermal resistance junction-case	2.78	°C/W
$R_{thj\text{-}amb}$	Thermal resistance junction-ambient	72	°C/W

2 Electrical characteristics

(T_{case} = 25°C unless otherwise specified)

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{EBO}	Emitter cut-off current (I_C = 0)	V_{EB} = 13 V			100	µA
I_{CES}	Collector cut-off current (V_{BE} = 0)	V_{CE} = 370 V			100	µA
$V_{(BR)CES}$	Collector-emitter breakdown voltage (V_{BE} = 0)	I_C = 50 mA	370		660	V
$V_{CE(sat)}$ (1)	Collector-emitter saturation voltage	I_C = 2.5 A I_B = 1 mA I_C = 3 A I_B = 3 mA			4 4	V V
$V_{BE(sat)}$ (1)	Base-emitter saturation voltage	I_C = 3 A I_B = 3 mA			3.5	V
h_{FE}	DC current gain	I_C = 1 A V_{CE} = 5 V	7000			
$E_{s/b}$ (1)	Secondary breakdown energy	I_C = 4 A L = 10 mH	80			mJ

1. Pulsed duration = 300 ms, duty cycle ≤1.5%

2.1 Electrical characteristics (curves)

Figure 2. DC current gain

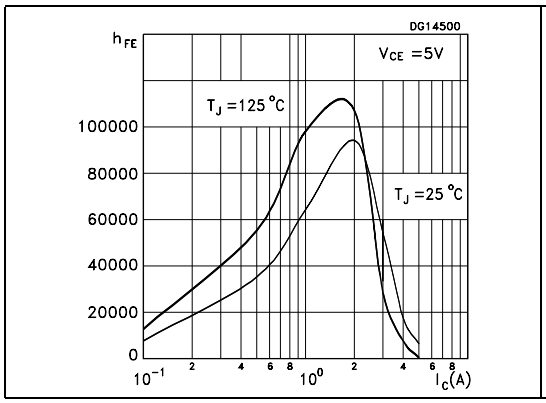


Figure 3. Collector-emitter saturation voltage

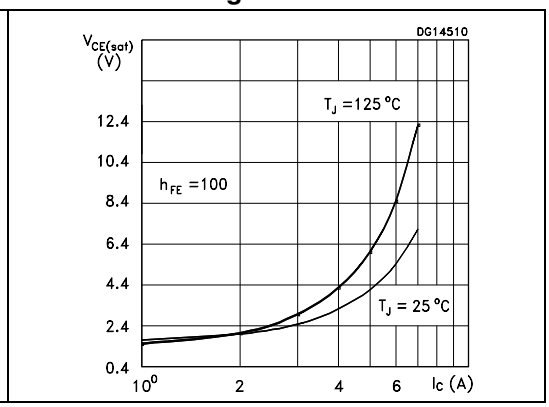


Figure 4. Collector-emitter saturation voltage

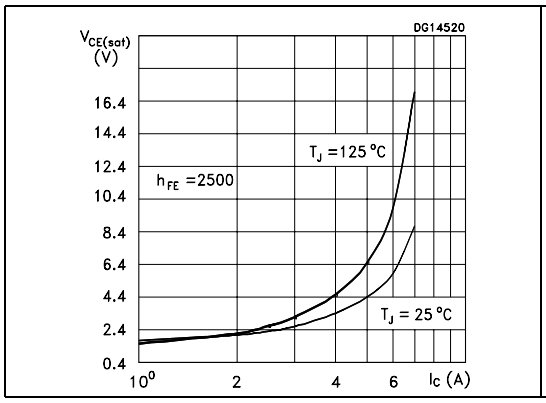
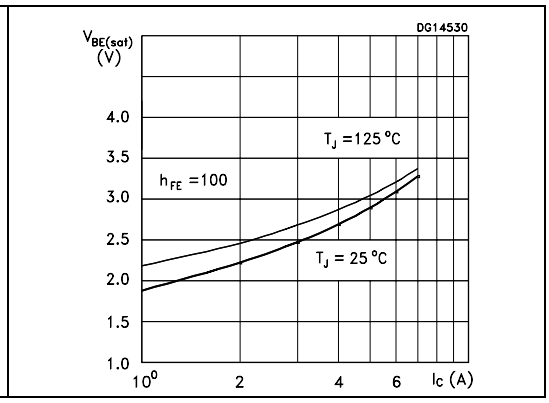


Figure 5. Base-emitter saturation voltage

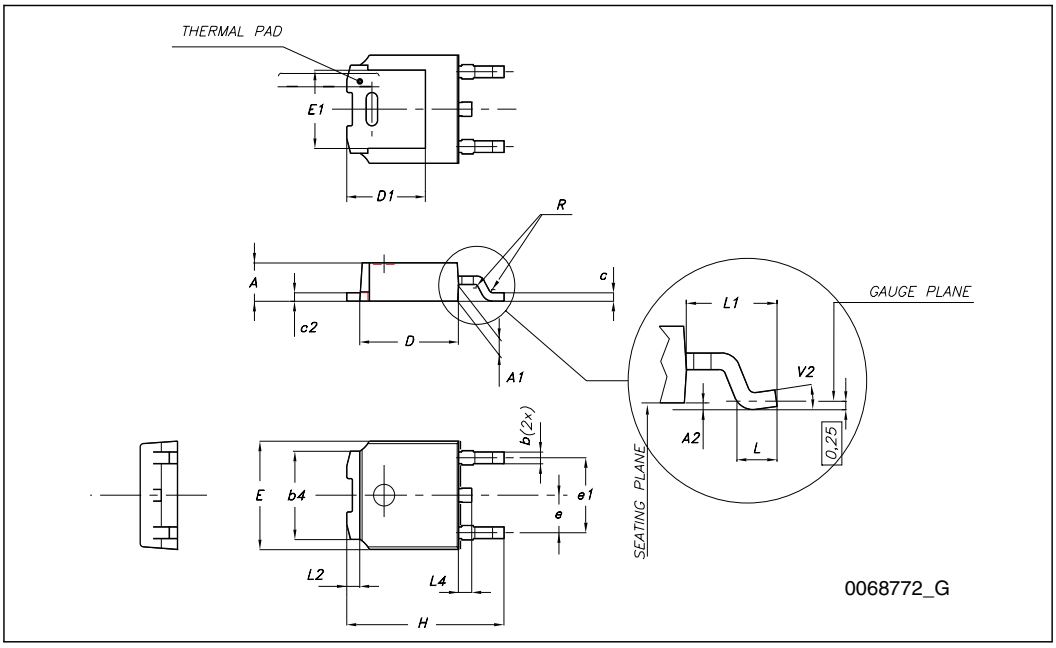


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

TO-252 (DPAK) mechanical data

DIM.	mm.		
	min.	typ	max.
A	2.20		2.40
A1	0.90		1.10
A2	0.03		0.23
b	0.64		0.90
b4	5.20		5.40
c	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
D1		5.10	
E	6.40		6.60
E1		4.70	
e		2.28	
e1	4.40		4.60
H	9.35		10.10
L	1		
L1		2.80	
L2		0.80	
L4	0.60		1
R		0.20	
V2	0 °		8 °



4 Revision history

Table 5. Document revision history

Date	Revision	Changes
02-Aug-2007	1	First release.
07-Aug-2007	2	Updated marking on *Table 1*
09-Jun-2008	3	Updated internal schematic diagram, *Figure 1.*

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