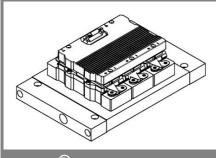
## SKiiP 1803GB122-3DW



## SKiiP<sup>®</sup> 3

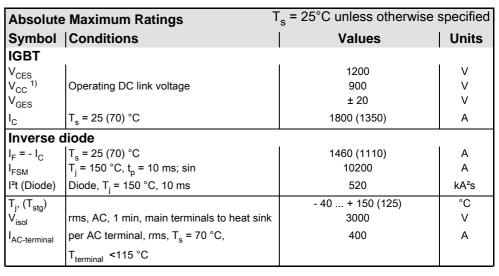
## 2-pack-integrated intelligent Power System

### Power section SKiiP 1803GB122-3DW

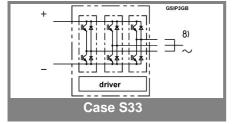
Data

#### **Power section features**

- SKiiP technology inside
- SPT (Soft Punch Through) IGBTs
- CAL diode technology
- · Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP<sup>®</sup> 3 System)
- IEC 60068-1 (climate) 40/125/56
- UL recognized File no. E63532
- with assembly of suitable MKP capacitor per terminal
- 8) AC connection busbars must be connected by the user; copper busbars available on request

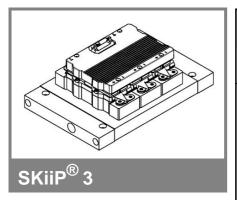


Characteristics				$T_s = 25$ °C unless otherwise specified					
Symbol  Conditions				min.	typ.	max.	Units		
IGBT	•							1	
V <sub>CEsat</sub>	I <sub>C</sub> = 900 A measured at	A, T <sub>j</sub> = 25 (1 terminal	125) °C;			2,3 (2,5)	2,6	V	
$V_{CEO}$	$T_i = 25 (1)$	25) °C; at t	erminal			1,1 (1)	1,3 (1,2)	V	
r <sub>CE</sub>	$T_i = 25 (125) ^{\circ}C$ ; at terminal					1,3 (1,7)	1,5 (1,9)	mΩ	
I <sub>CES</sub>	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>i</sub> = 25 (125) °C					mA			
$E_{on} + E_{off}$	$I_{\rm C}^{\rm J}$ = 900 A, $V_{\rm CC}$ = 600 V				mJ				
	T <sub>j</sub> = 125 °	T <sub>i</sub> = 125 °C, V <sub>CC</sub> = 900 V				476			
R <sub>CC+EE</sub>	terminal o	hip, T <sub>i</sub> = 25	5 °C			mΩ			
L <sub>CE</sub>	top, botto					nΗ			
C <sub>CHC</sub>	per phase	e, AC-side				3		nF	
Inverse o	diode								
$V_F = V_{EC}$	I <sub>F</sub> = 900 A measured at	A, T <sub>j</sub> = 25 (1 terminal	25) °C			1,95 (1,7)	2,1	V	
$V_{TO}$	T <sub>i</sub> = 25 (1	25) °C				1,1 (0,8)	1,2 (0,9)	V	
r <sub>T</sub>	$T_i = 25 (1)$	25) °C				0,9 (1)	1 (1,2)	mΩ	
E <sub>rr</sub>	$I_{\rm C} = 900  A$	A, V <sub>CC</sub> = 60	0 V			72		mJ	
	T <sub>j</sub> = 125 °	C, V <sub>CC</sub> = 9	00 V			92		mJ	
Mechani	Mechanical data								
$M_{dc}$	DC termin	nals, SI Uni	ts		6		8	Nm	
${\rm M}_{\rm ac}$		nals, SI Uni			13	2,4	15	Nm	
W	SKiiP® 3 System w/o heat sink					kg			
W	heat sink					kg			
						c.); "s" ref (acc.IEC			
R <sub>th(j-s)I</sub>	per IGBT						0,017	K/W	
R <sub>th(j-s)D</sub>	per diode						0,033	K/W	
Z <sub>th</sub>	R <sub>i</sub> (mK/W) (max. values)								
	1	2	3	4	1	2	3	4	
$Z_{th(j-r)I}$	1,4	6,8	7,8	0	69	0,35	0,02	1	
$Z_{th(j-r)D}$	2,6	4	17,7	17,7	50	5	0,25	0,04	
$Z_{th(r-a)}$	4,6	4,7	1,1	0,6	48	15	2,8	0,4	



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## SKiiP 1803GB122-3DW



# 2-pack-integrated intelligent Power System

2-pack integrated gate driver SKiiP 1803GB122-3DW

Data

#### Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlock of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute Maximum Ratings		a = 25°C unless otherwise specified		
Symbol	Conditions	Values	Units	
$V_{S2}$	unstabilized 24 V power supply	30	V	
$V_{i}$	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/μs	
$V_{isollO}$	input / output (AC, rms, 2s)	3000	V	
V <sub>isoIPD</sub>	partial discharge extinction voltage, rms, Q <sub>PD</sub> ≤10 pC;	1170	V	
V <sub>isol12</sub>	output 1 / output 2 (AC, rms, 2s)	1500	V	
f <sub>sw</sub>	switching frequency	10	kHz	
f <sub>out</sub>	output frequency for I <sub>peak(1)</sub> =I <sub>C</sub>	10	kHz	
$T_{op} (T_{stg})$	operating / storage temperature	- 40 <b>+</b> 85	°C	

Characte	eristics	(T <sub>a</sub> = 25°C)			
Symbol	Conditions	min.	typ.	max.	Units
$V_{S2}$	supply voltage non stabilized	13	24	30	V
I <sub>S2</sub>	V <sub>S2</sub> = 24 V	278+29*f/kHz+0,00015*(I <sub>AC</sub> /A) <sup>2</sup>			mA
V <sub>iT+</sub>	input threshold voltage (High)	12,3		12,3	V
$V_{iT-}$	input threshold voltage (Low)	4,6			V
R <sub>IN</sub>	input resistance		10		kΩ
$C_{IN}$	input capacitance		1		nF
t <sub>d(on)IO</sub>	input-output turn-on propagation time		1,3		μs
t <sub>d(off)IO</sub>	input-output turn-off propagation time		1,3		μs
t <sub>pERRRESET</sub>	error memory reset time		9		μs
$t_{TD}$	top / bottom switch interlock time		3,3		μs
I <sub>analogOUT</sub>	max. 5mA; 8 V corresponds to 15 V supply voltage for external components		1500		Α
I <sub>s1out</sub>	max. load current			50	mA
I <sub>TRIPSC</sub>	over current trip level				
	(I <sub>analog</sub> OUT = 10 V)		1875		Α
$T_tp$	over temperature protection	110		120	°C
U <sub>DCTRIP</sub>	$U_{DC}$ -protection ( $U_{analog OUT} = 9 V$ );	i	not mplemente	d	V
	(option for GB types)				

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