



CHENMKO ENTERPRISE CO.,LTD

Lead free devices

**SURFACE MOUNT
NPN Digital Silicon Transistor**

VOLTAGE 50 Volts CURRENT 100 mAmpere

CHDTC114GMPT

APPLICATION

- * Switching circuit, Inverter, Interface circuit, Driver circuit.

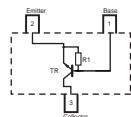
FEATURE

- * Small surface mounting type. (SOT-723)
- * High current gain.
- * Suitable for high packing density.
- * Low collector-emitter saturation.
- * High saturation current capability.
- * Internal isolated NPN transistors in one package.
- * Built in bias resistor($R_1=10k\Omega$, Typ.)

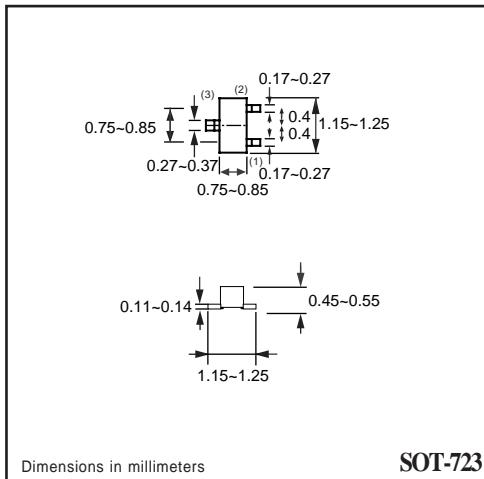
CONSTRUCTION

- * One NPN transistors and bias of thin-film resistors in one package.

CIRCUIT



SOT-723



Dimensions in millimeters

SOT-723

LIMITING VALUES

In accordance with the Absolute Maximum Rating System .

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-Base voltage		50	V
V_{CEO}	Collector-Emitter voltage		50	V
V_{EBO}	Emitter-Base voltage		5	V
$I_C(\text{Max.})$	Collector current		100	mA
P_D	Power dissipation	$T_{\text{amb}} \leq 25^\circ\text{C}$, Note 1	150	mW
T_{STG}	Storage temperature		-55 +150	°C
T_J	Junction temperature		-55 +150	°C
$R_{\theta J-S}$	Thermal resistance , Note 1	junction - soldering point	140	°C/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC (CHDTC114GMPT)

CHARACTERISTICS

$T_{amb} = 25^{\circ}\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
BVCBO	Collector-Base breakdown voltage	$I_c=50\mu\text{A}$	50.0	—	—	V
BVCEO	Collector-Emitter breakdown voltage	$I_c=1\text{mA}$	50.0	—	—	V
BVEBO	Emitter-Base breakdown voltage	$I_e=720\mu\text{A}$	5.0	—	—	V
VCE(sat)	Collector-Emitter Saturation voltage	$I_c=10\text{mA}; I_b=0.5\text{mA}$	—	—	0.3	V
I_{CBO}	Collector-Base current	$V_{CB}=50\text{V}$	—	—	0.5	μA
I_{EBO}	Emitter-Base current	$V_{EB}=4\text{V}$	300	—	580	μA
h_{FE}	DC current gain	$I_c=5\text{mA}; V_{CE}=5.0\text{V}$	30	—	—	
R_1	Input resistor		7.0	10	13	$\text{k}\Omega$
f_T	Transition frequency	$I_e=-5\text{mA}, V_{CE}=10.0\text{V}$ $f=100\text{MHz}$	—	250	—	MHz

Note

1. Pulse test: $t_p \leq 300\mu\text{s}; \delta \leq 0.02$.