

5-Line TVS Array

(Pb) Lead(Pb)-Free

Features:

- * 100 Watts Peak Pulse Power per Line(tp=8/20 s)
- * Monolithic Structure
- * Low Clamping Voltage
- * IEC Compatibility(EN6100-4)
61000-4-2(ESD): Air-15kV, Contact-8kV
61000-4-4(EFT): 40A-5/50ns
- * ESD Protection>40 kilovolts
- * Low Leakage Current
- * Protects Four(4) Bidirectional Lines and Five(5) Unidirectional Lines

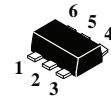
Main Applications:

- * Computer Notebooks
- * Communication Systems & Cellular Phones
- * Printers
- * Personal Digital Assistant(PDA)
- * Video Equipment

Mechanical Characteristics:

- * Molded JEDEC SOT-563 Package
- * Weight 3 milligrams

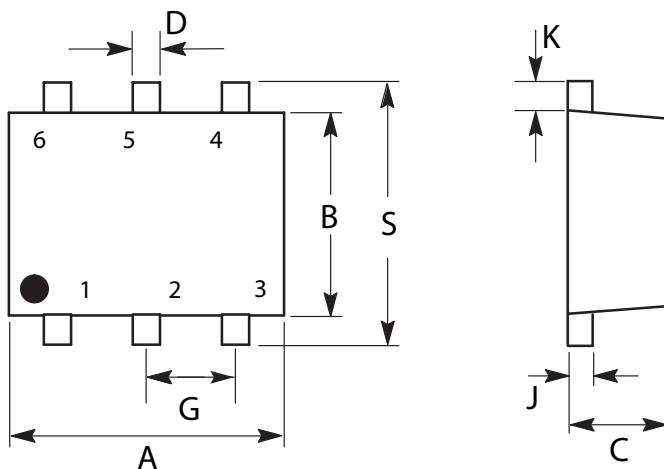
Peak Pulse Power
100 Watts
Reverse Working Voltage
5.0 VOLTS



SOT-563

SOT-563 Outline Dimensions

Unit:mm



SOT-563		
Dim	Min	Max
A	1.50	1.70
B	1.10	1.30
C	0.50	0.60
D	0.17	0.27
G	0.50 REF	
J	0.08	0.16
K	0.10	0.30
S	1.50	1.70

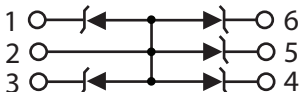
Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Units
Peak Pulse Power (t _p =8/20μs) See Figure 1	P _{PP}	100	W
IEC61000-4-2 Air(ESD) IEC61000-4-2 Contact(ESD)	ESD	15 8	kV
Operating Temperature	T _J	-55 to +150	°C
Storage Temperature	T _{STG}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Reverse Working Voltage	I _R =1uA	V _{RWM}		-	5.0	V
Breakdown Voltage	I _T =1mA	V _{BR}	6.2	6.8	7.2	V
Reverse Leakage Current	V _{RWM} =3V	I _R	-	0.01	0.5	μA
Capacitance	V _R =0V,f=1MHz	C _J	-	54	70	pF

Device Marking

Item	Marking	Equivalent Circuit diagram
WGSMF05C	EC	

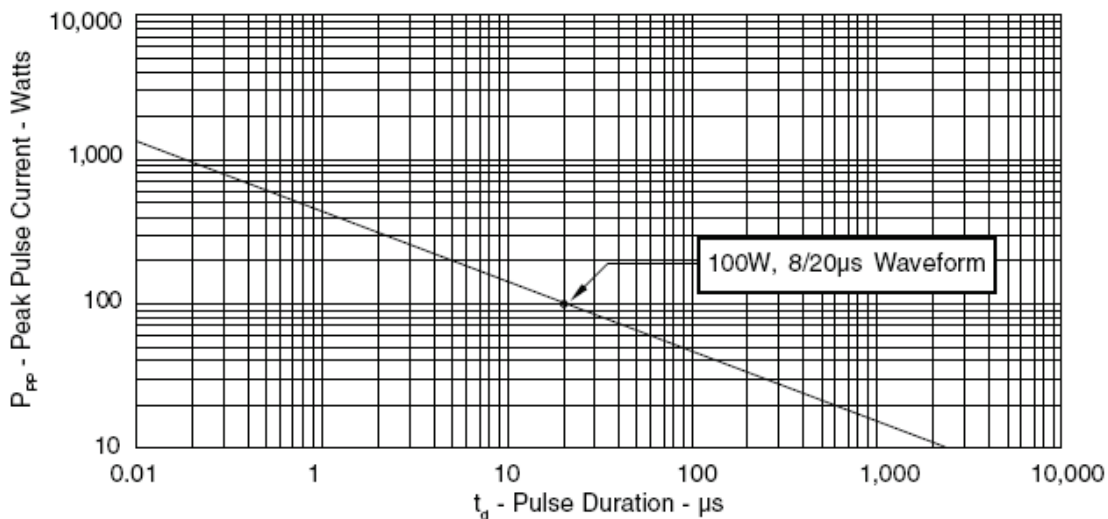


Figure 1 Peak Pulse Power VS Pulse Se Time

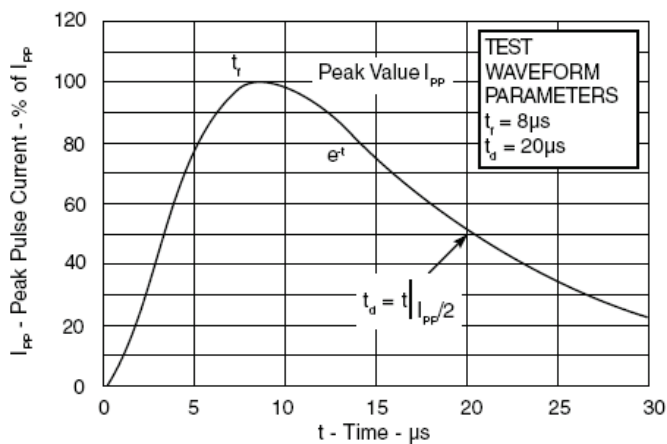


Figure 2 Pulse Wave Form

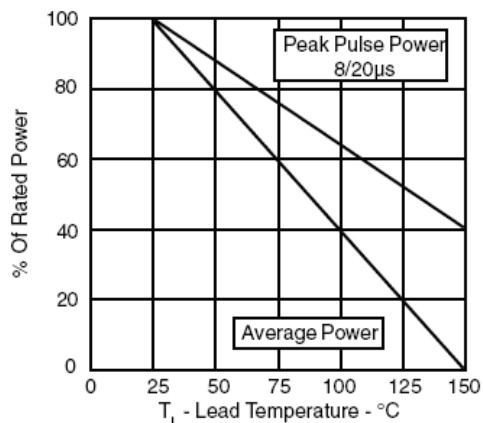


Figure 3 Power Derating Curve

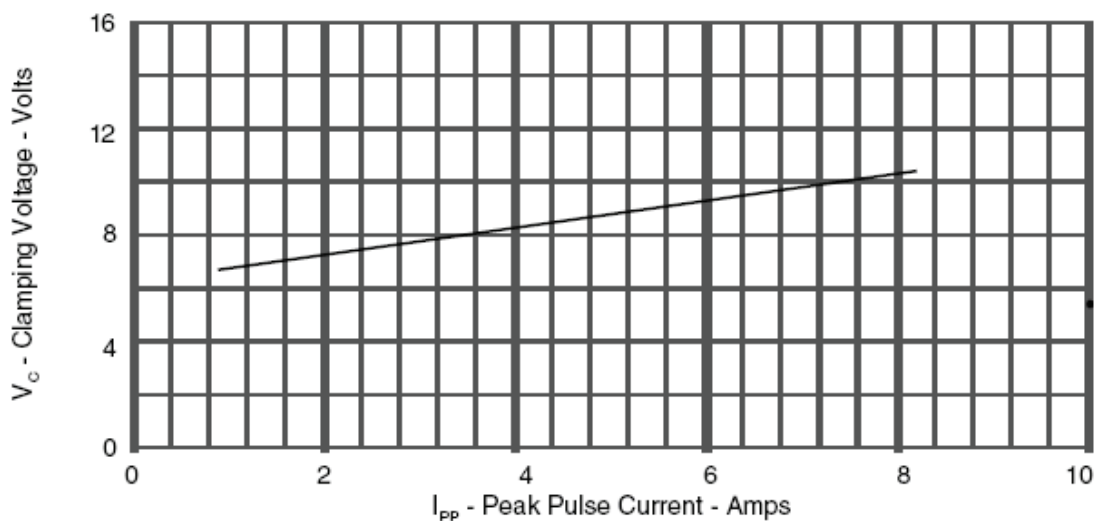


Figure 4 Typical Clamping Voltage VS Peak Pulse Current