

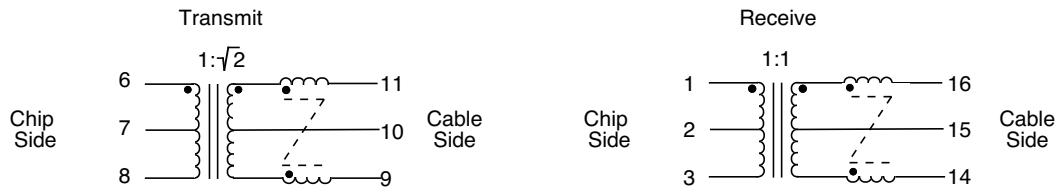
EPE6303S

- Optimized for Level One LXT901 IC •
- For application requiring enhanced common mode attenuation •
- Robust design allows for toughest soldering processes •
- Small outline package on 0.050" pitch •
- Complies with or exceeds IEEE 802.3, 10 Base-T Requirements •

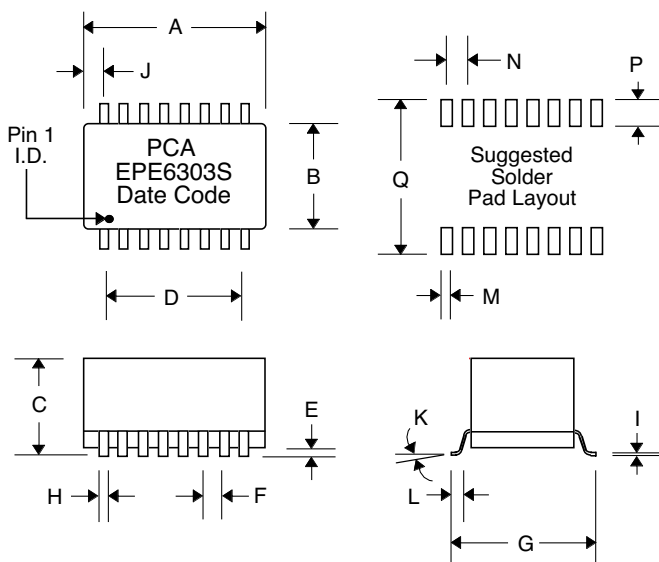
Electrical Parameters @ 25° C

Cable Side Inductance ($\mu\text{H} \pm 30\%$)		Interwinding Capacitance (pF Max.)		Leakage Inductance (μH Max.)		CMRR (dB Min.)				Hipot (Vrms)	DCR (Ω Max.)	
@ 100 KHz, 0.1 Vrms		@ 100 KHz, 0.1 Vrms		@ 100 KHz, 0.1 Vrms		1-50 Mhz	100 Mhz	200 Mhz	300 Mhz			
Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit Rcv	Xmit Rcv	Xmit Rcv	Xmit Rcv		Xmit	Rcv
200		15		0.5		-55	-45	-35	-25	2000	0.6	0.6

Schematic



Package



Dimensions

Dim.	(Inches)			(millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.440	.460		11.18	11.68	
B	.240	.260		6.09	6.60	
C	.215	.225		5.46	5.72	
D	.350	Typ.		8.89	Typ.	
E	.005	.015		.127	.381	
F	.050	Typ.		1.27	Typ.	
G	.345	.375		8.76	9.53	
H	.016	.022		.406	.559	
I	.010	Typ.		.254	Typ.	
J	.050	Typ.		1.27	Typ.	
K	0°	8°		0°	8°	
L	.020	.045		.508	1.14	
M			.030			.762
N			.050			1.27
P			.070			1.78
Q			.400			10.16