

### Low Power Peak EMI Reducing Solution

#### **Features**

- Generates a 1X low EMI optimized clock signal at the output.
- Integrated loop filter components.
- Operates with a 3.3 / 2.5V Supply.
- Operating current less than 5mA.
- · Low power CMOS design.
- Input frequency range 60MHz to 120MHz for 2.5V 60MHz to 120MHz for 3.3V
- Frequency deviation: ±1.5(Typ) @85MHz Output frequency.
- Available in 6-pin TSOT-23 Package.

#### **Product Description**

The ASM3P2598A is a versatile spread spectrum frequency modulator designed specifically for a wide range of clock frequencies. The ASM3P2598A reduces electromagnetic interference (EMI) at the clock source, allowing system wide reduction of EMI of all clock dependent signals. The ASM3P2598A allows significant system cost savings by reducing the number of circuit board layers ferrite beads, shielding that are traditionally required to pass EMI regulations.

The ASM3P2598A uses the most efficient and optimized modulation profile approved by the FCC and is implemented by using a proprietary all digital method.

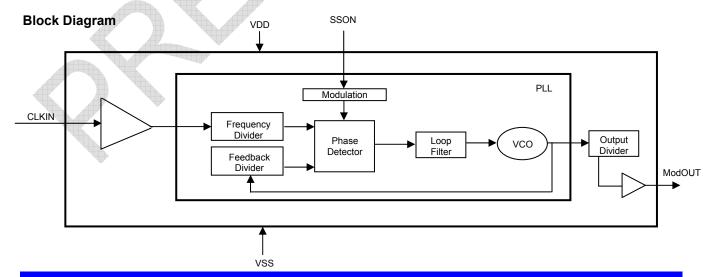
The ASM3P2598A modulates the output of a single PLL in order to "spread" the bandwidth of a synthesized clock, and more importantly, decreases the peak amplitudes of its harmonics. This results in significantly lower system EMI compared to the typical narrow band signal produced by oscillators and most frequency generators. Lowering EMI by increasing a signal's bandwidth is called 'spread spectrum clock generation'.

#### **Applications**

The ASM3P2598A is targeted towards all portable devices with very low power requirements like MP3 players,MFP, LCD Panel Module and digital still cameras.

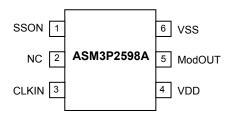
#### **Key Specifications**

Description	Specification
Supply voltages	VDD = 3.3V / 2.5V
Cycle-to-Cycle Jitter	360pS (Typ)
Output Duty Cycle	45/55%
Modulation Rate Equation	F <sub>IN</sub> /2560
Frequency Deviation	±1.5% (Typ) @85MHz Output





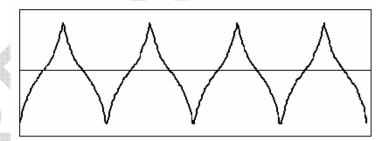
# Pin Configuration (6-pin TSOT- 23 Package)



## **Pin Description**

Pin#	Pin Name	Туре	Description
1	SSON	I	When SSON is HIGH, the spread spectrum is enabled and when LOW, it turns off the spread spectrum. Connect the pin to ground When Spread Spectrum feature is not required.
2	NC	-	No Connect
3	CLKIN	I	Clock Input
4	VDD	Р	Power supply for the entire chip.
5	ModOUT	0	Spread spectrum clock output.
6	VSS	Р	Ground connection.

### **Modulation Profile**



# **Specifications**

Description		Specification	
Frequency Range	For 2.5V Supply	60MHz < CLKIN < 120MHz	
Frequency Range	For 3.3V Supply	60MHz < CLKIN < 120MHz	
Modulation Equatio	n	F <sub>IN</sub> /2560	
Frequency Deviation	on	±1.5% (Typ) @ 85MHz Output	



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit
VDD, V <sub>IN</sub>	Voltage on any pin with respect to Ground	-0.5 to +4.6	V
T <sub>STG</sub>	Storage temperature	-65 to +125	°C
T <sub>A</sub>	Operating temperature	-40 to +85	°C
Ts	Max. Soldering Temperature (10 sec)	260	°C
TJ	Junction Temperature	150	°C
$T_DV$	Static Discharge Voltage	2	KV
ΙDV	(As per JEDEC STD22- A114-B)		

Note: These are stress ratings only and are not implied for functional use. Exposure to absolute maximum ratings for prolonged periods of time may affect device reliability.

## **DC Electrical Characteristics for 2.5V Supply**

Symbol	Parameter	Min	Тур	Max	Unit			
V <sub>IL</sub>	Input low voltage	VSS - 0.3	-	0.8	V			
$V_{IH}$	Input high voltage	2.0	₩ -	VDD + 0.3	V			
I <sub>IL</sub>	Input low current	_	-	-35	μA			
I <sub>IH</sub>	Input high current	-	-	35	μA			
$V_{OL}$	Output low voltage (VDD = 2.5V, I <sub>OL</sub> = 8 mA)	<b>*</b>	-	0.6	V			
V <sub>OH</sub>	Output high voltage (VDD = 2.5V, I <sub>OH</sub> = 8 mA)	1.8	_	_	V			
IDD	Static supply current*	_	1.8	_	mA			
Icc	Dynamic supply current (2.5V, 85MHz and no load)	_	4.0	_	mA			
VDD	Operating voltage	2.375	2.5	2.625	V			
ton	Power-up time (first locked cycle after power-up)	_	_	5	mS			
Z <sub>OUT</sub>	Output impedance	_	50	_	Ω			
* CLKIN pin is	* CLKIN pin is pulled low							

## **AC Electrical Characteristics for 2.5V Supply**

Symbol	Parameter			Тур	Max	Unit
CLKIN	Input frequency		60	-	120	MHz
ModOUT	Output frequency		60	-	120	MHz
f <sub>d</sub>	Frequency Deviation	Input Frequency = 60MHz	_	±1.6	-	- %
Id	Inp	Input Frequency = 120MHz	-	±1.1	-	
t <sub>LH</sub> *	Output rise time (measured fro	Output rise time (measured from 0.7V to 1.7V)		1.8	2.6	nS
t <sub>HL</sub> *	Output fall time (measured from	Output fall time (measured from 1.7V to 0.7V)		0.9	1.1	nS
t <sub>JC</sub>	Jitter (Cycle to cycle)	Jitter (Cycle to cycle)		360	-	pS
t <sub>D</sub>	Output duty cycle		45	50	55	%
* t <sub>i H</sub> and t <sub>H</sub> are measured into a capacitive load of 15pF						



# **DC Electrical Characteristics for 3.3V Supply**

Symbol	Parameter	Min	Тур	Max	Unit		
$V_{IL}$	Input low voltage	VSS - 0.3	-	0.8	V		
$V_{IH}$	Input high voltage	2.0	-	VDD + 0.3	V		
I <sub>IL</sub>	Input low current	_	-	-35	μA		
I <sub>IH</sub>	Input high current	_	-	35	μA		
V <sub>OL</sub>	Output low voltage (VDD = 3.3V, I <sub>OL</sub> = 8 mA)	_		0.4	V		
V <sub>OH</sub>	Output high voltage (VDD = 3.3V, I <sub>OH</sub> = 8 mA)	2.5	-	-	V		
IDD	Static supply current*	_	2.2	-	mA		
Icc	Dynamic supply current (3.3V, 85MHz and no load)	_	4.5		mA		
VDD	Operating voltage	3.0	3.3	3.6	V		
ton	Power-up time (first locked cycle after power-up)	-	-	5	mS		
Z <sub>OUT</sub>	Output impedance	- 1	45	_	Ω		
* CLKIN pin is	* CLKIN pin is pulled low						

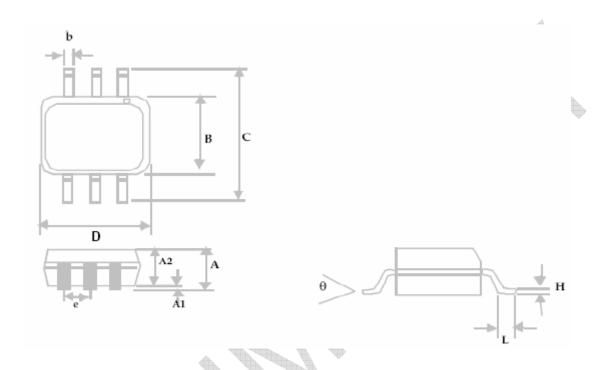
# **AC Electrical Characteristics for 3.3V Supply**

Symbol	Pa	Parameter			Max	Unit
CLKIN	Input frequency		60	_	120	MHz
ModOUT	Output frequency		60	_	120	MHz
f.	Fraguancy Dovistion	Input Frequency = 60MHz	-	±1.6	-	%
f <sub>d</sub> Frequency Deviation		Input Frequency = 120MHz	_	±1.1	-	70
t <sub>LH</sub> *	Output rise time (measure	Output rise time (measured from 0.8 to 2.0V)		1.2	1.8	nS
t <sub>HL</sub> *	Output fall time (measured	Output fall time (measured at 2.0V to 0.8V)		0.8	1.1	nS
t <sub>JC</sub>	Jitter (cycle to cycle)	7	_	360	_	pS
t <sub>D</sub>	Output duty cycle		45	50	55	%
*t <sub>LH</sub> and t <sub>HL</sub> are measured into a capacitive load of 15pF						



# **Package Information**

# 6-pin TSOT-23 Package



	1010000	Dim	ensions	
Symbol	Inches		Millim	neters
1	Min	Max	Min	Max
A		0.04		1.00
A1	0.00	0.004	0.00	0.10
A2	0.033	0.036	0.84	0.90
b	0.012	0.02	0.30	0.50
Н	0.005	BSC	0.127	BSC
D	0.114	BSC	2.90	BSC
В	0.06 BSC		1.60	BSC
е	0.0374 BSC		0.950 BSC	
С	0.11 BSC		2.80	BSC
L	0.0118	0.02	0.30	0.50
θ	0°	4°	0°	4°



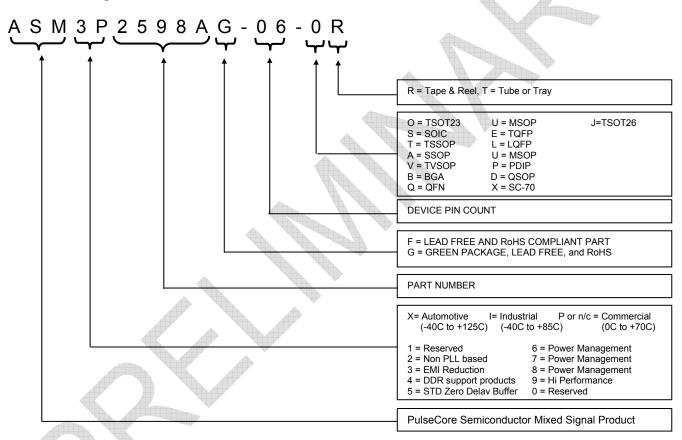




### **Ordering Information**

Part Number	Marking	Package Type	Temperature
ASM3P2598AF-06-OR	A11	6-Pin TSOT-23, TAPE & REEL, Pb Free	Commercial
ASM3P2598AG-06-OR	A12	6-Pin TSOT-23, TAPE & REEL, Green	Commercial
ASM3I2598AF-06-OR	A14	6-Pin TSOT-23, TAPE & REEL, Pb Free	Industrial
ASM3I2598AG-06-OR	A15	6-Pin TSOT-23, TAPE & REEL, Green	Industrial

#### **Device Ordering Information**



Licensed under U.S Patent Nos 5,488,627 and 5,631,921



PulseCore

Giving you the edge

October 2006

rev 0.3



PulseCore Semiconductor Corporation 1715 S. Bascom Ave Suite 200 Campbell, CA 95008 Tel: 408-879-9077

Fax: 408-879-9018 www.pulsecoresemi.com

Copyright © PulseCore Semiconductor All Rights Reserved Preliminary Information Part Number: ASM3P2598A Document Version: 0.3

Note: This product utilizes US Patent #6,646,463 Impedance Emulator Patent issued to PulseCore Semiconductor, dated 11-11-2003

© Copyright 2006 PulseCore Semiconductor Corporation. All rights reserved. Our logo and name are trademarks or registered trademarks of PulseCore Semiconductor. All other brand and product names may be the trademarks of their respective companies. PulseCore reserves the right to make changes to this document and its products at any time without notice. PulseCore assumes no responsibility for any errors that may appear in this document. The data contained herein represents PulseCore's best data and/or estimates at the time of issuance. PulseCore reserves the right to change or correct this data at any time, without notice. If the product described herein is under development, significant changes to these specifications are possible. The information in this product data sheet is intended to be general descriptive information for potential customers and users, and is not intended to operate as, or provide, any guarantee or warrantee to any user or customer. PulseCore does not assume any responsibility or liability arising out of the application or use of any product described herein, and disclaims any express or implied warranties related to the sale and/or use of PulseCore products including liability or warranties related to fitness for a particular purpose, merchantability, or infringement of any intellectual property rights, except as express agreed to in PulseCore's Terms and Conditions of Sale (which are available from PulseCore). All sales of PulseCore products are made exclusively according to PulseCore's Terms and Conditions of Sale. The purchase of products from PulseCore does not convey a license under any patent rights, copyrights; mask works rights, trademarks, or any other intellectual property rights of PulseCore or third parties. PulseCore does not authorize its products for use as critical components in life-supporting systems where a malfunction or failure may reasonably be expected to result in significant injury to the user, and the inclusion of PulseCore products in such life-supporting systems implies that the manufacturer assumes all risk of such use and agrees to indemnify PulseCore against all claims arising from such use.