

**Micro Commercial Components** 

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# MMSTA42

# **Features**

- **Epitaxial Planar Die Construction**
- Ideal for Medium Power Amplification and Switching
- Ultra-small surface mount package
- Marking: K3M
- Case Material: Molded Plastic. UL Flammability Classificatio Rating 94-0 and MSL Rating 1

#### **Maximum Ratings**

Symbol	Rating	Rating	Unit
$V_{CEO}$	Collector-Emitter Voltage	300	V
$V_{CBO}$	Collector-Base Voltage	300	V
$V_{EBO}$	Emitter-Base Voltage	6.0	V
l <sub>c</sub>	Collector Current-Continuous (1) (3)	200	mA
Pc	Power dissipation (1)	200	mW
$T_J$	Junction Temperature	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature	-55 to +150	°C

### **Electrical Characteristics @ 25°C Unless Otherwise Specified**

Symbol	Parameter	Min	Max	Units
OFF CHARACTERISTICS				
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage 300 (L=1.0mAdc, L <sub>B</sub> =0)			
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage (L=100uAdc, L=0)	300		Vdc
$V_{(BR)EBO}$	Collector-Emitter Breakdown Voltage ( $\xi$ =100uAdc, $\xi$ 0)	6.0		Vdc
Ісво	Collector-Base Cutoff Current (V <sub>CB</sub> =200Vdc,I <sub>E</sub> =0)		100	nAdc
l <sub>EBO</sub>	Emitter-Base Cutoff Current (V <sub>CE</sub> =6.0Vdc, I <sub>C</sub> =0)		100	nAdc
NN CHARACTERISTICS(2)				

### UN CHARACTERISTICS

h <sub>FE</sub>	DC Current Gain			
	(I <sub>C</sub> =1.0mAdc, V <sub>CE</sub> =10Vdc)	25		
	(I <sub>C</sub> =10mAdc, V <sub>CE</sub> =10Vdc)	40		
	(I <sub>C</sub> =30mAdc, V <sub>CE</sub> =10Vdc)	40		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage		0.5	Vdc
	$(l_c=20 \text{mAdc}, l_B=2.0 \text{mAdc})$			
$V_{BE(sat)}$	Base-Emitter Saturation Voltage		0.9	Vdc
	$(l_c=20\text{mAdc}, l_B=2.0\text{mAdc})$			

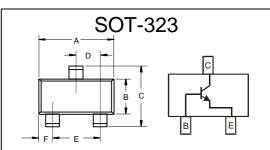
#### SMALL SIGNAL CHARACTERISTICS

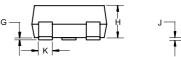
f⊤	Current-Gain-Bandwidth Product	50		MHz
	(V <sub>CE</sub> =20V, f=100MHz, l <sub>C</sub> =10mA)			
C <sub>CB</sub>	Collector-Base Capacitance		3.0	pF
	(V <sub>CP</sub> =20V f=1 0MHz k=0)			

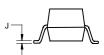
Note: 1. Valid provided that terminals are kept at ambient temperature.

- 2. Pulse test: Pulse width<300us, duty cycle<2%
- 3. When operated within safe operating area constraints.

# **NPN Small Signal Transistors**

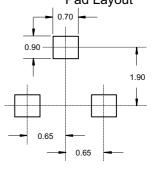






DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.071	.087	1.80	2.20	
В	.045	.053	1.15	1.35	
С	.079	.087	2.00	2.20	
D	.026 N	ominal	0.65Nom	inal	
Е	.047	.055	1.20	1.40	
F	.012	.016	.30	.40	
G	.000	.004	.000	.100	
Н	.035	.039	.90	1.00	
J	.004	.010	.100	.250	
K	.012	.016	.30	.40	

### Suggested Solder Pad Layout





## **Ordering Information**

Device	Packing
(Part Number)-TP	Tape&Reel3Kpcs/Reel

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