

RoHS Compliant Product

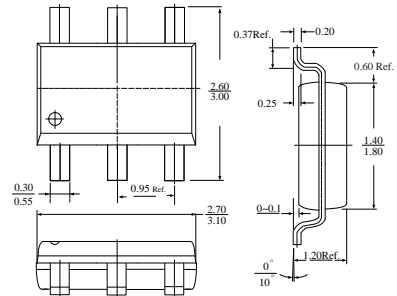
**Description**

The SST2623 utilized advance processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device. The SST2623 is universally used for all commercial-industrial applications.

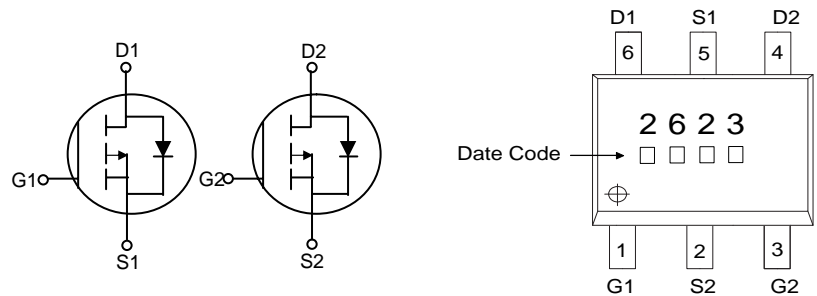
**Features**

- \* Low On-Resistance
- \* Low Gate Charge

**SOT-26**



Dimensions in millimeters



**Absolute Maximum Ratings**

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current <sup>3</sup>	I <sub>D</sub> @T <sub>A</sub> =25°C	-2	A
Continuous Drain Current <sup>3</sup>	I <sub>D</sub> @T <sub>A</sub> =70°C	-1.6	A
Pulsed Drain Current <sup>1</sup>	I <sub>DM</sub>	-20	A
Total Power Dissipation	P <sub>D</sub> @T <sub>A</sub> =25°C	1.2	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55~+150	°C

**Thermal Data**

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient <sup>3</sup> (Max)	R <sub>thj-a</sub>	110	°C/W

## Electrical Characteristics( T<sub>j</sub>=25°C Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA
Breakdown Voltage Temp. Coefficient	ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	-0.02	-	V/°C	Reference to 25°C, I <sub>D</sub> =-1mA
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.0	-	-3.0	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA
Gate-Source Leakage Current	I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V
Drain-Source Leakage Current (T <sub>j</sub> =25°C)	I <sub>DSS</sub>	-	-	-1	uA	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0
Drain-Source Leakage Current (T <sub>j</sub> =70°C)		-	-	-25	uA	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	-	-	170	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2A
		-	-	280		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.6A
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	-	2.8	4.5	nC	I <sub>D</sub> =-2A V <sub>DS</sub> =-24V V <sub>GS</sub> =-4.5V
Gate-Source Charge	Q <sub>gs</sub>	-	0.5	-		
Gate-Drain ("Miller") Charge	Q <sub>gd</sub>	-	1.4	-		
Turn-on Delay Time <sup>2</sup>	T <sub>d(ON)</sub>	-	5	-	nS	V <sub>DD</sub> =-15V I <sub>D</sub> =-1A V <sub>GS</sub> =-10V R <sub>G</sub> =3.3Ω R <sub>D</sub> =15Ω
Rise Time	T <sub>r</sub>	-	6	-		
Turn-off Delay Time	T <sub>d(OFF)</sub>	-	15	-		
Fall Time	T <sub>f</sub>	-	3	-		
Input Capacitance	C <sub>iss</sub>	-	150	240	pF	V <sub>GS</sub> =0V V <sub>DS</sub> =-25V f=1.0MHz
Output Capacitance	C <sub>oss</sub>	-	42	-		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	32	-		
Forward Transconductance	G <sub>fs</sub>	-	2	-	S	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2A

## Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Forward On Voltage <sup>2</sup>	V <sub>DS</sub>	-	-	-1.2	V	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V.
Reverse Recovery Time <sup>2</sup>	T <sub>rr</sub>	-	20	-	nS	I <sub>S</sub> =-2A, V <sub>GS</sub> =0V di/dt=100A/uS
Reverse Recovery Charge	Q <sub>rr</sub>	-	13	-	nC	

Notes: 1.Pulse width limited by safe operating area.

2.Pulse width ≤300us, dutycycle ≤2%.

3.Surface mounted on 1 in<sup>2</sup>copper pad of FR4 board; 135°C/W when mounted on min. copper pad.

### Characteristics Curve

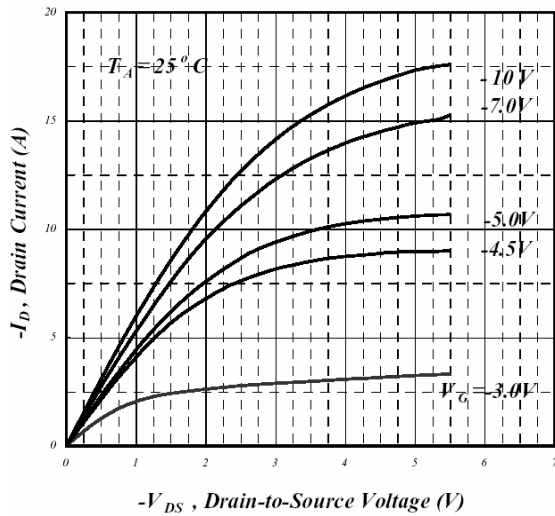


Fig 1. Typical Output Characteristics

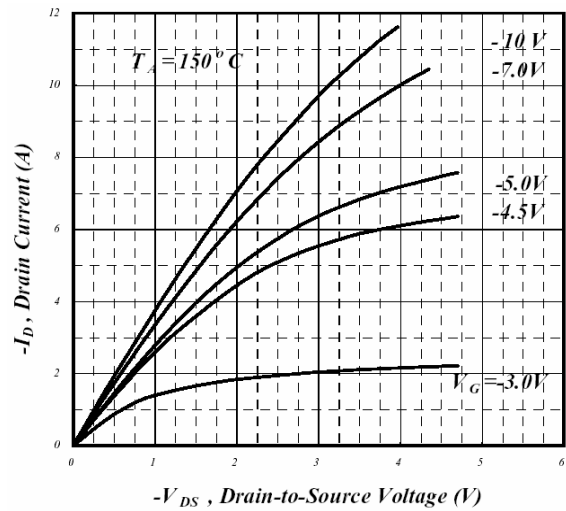


Fig 2. Typical Output Characteristics

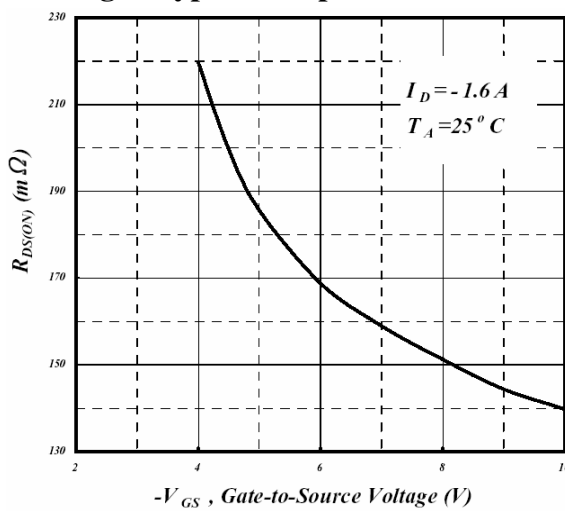


Fig 3. On-Resistance v.s. Gate Voltage

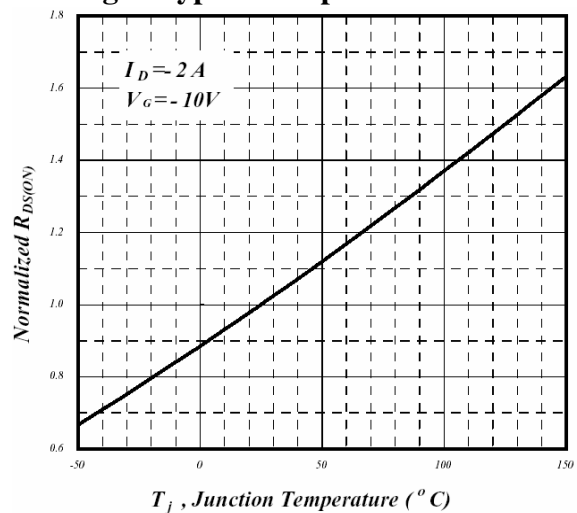


Fig 4. Normalized On-Resistance v.s. Junction Temperature

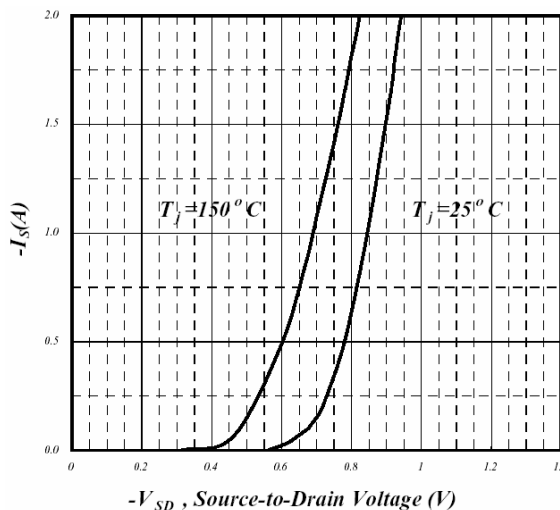


Fig 5. Forward Characteristics of Reverse Diode

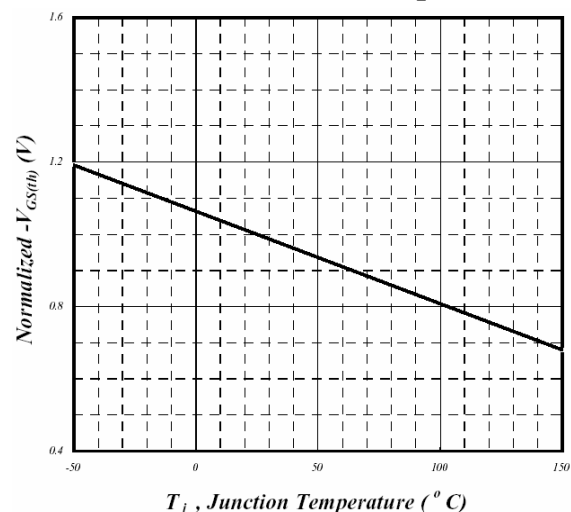


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

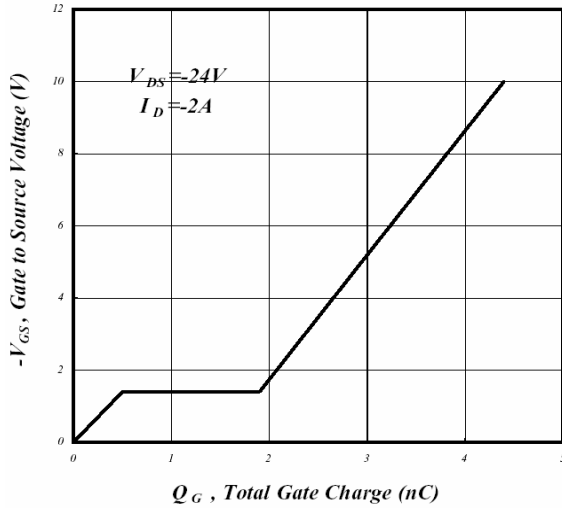


Fig 7. Gate Charge Characteristics

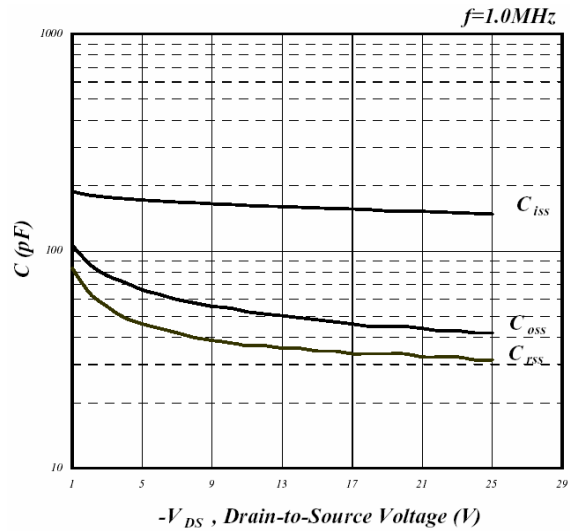


Fig 8. Typical Capacitance Characteristics

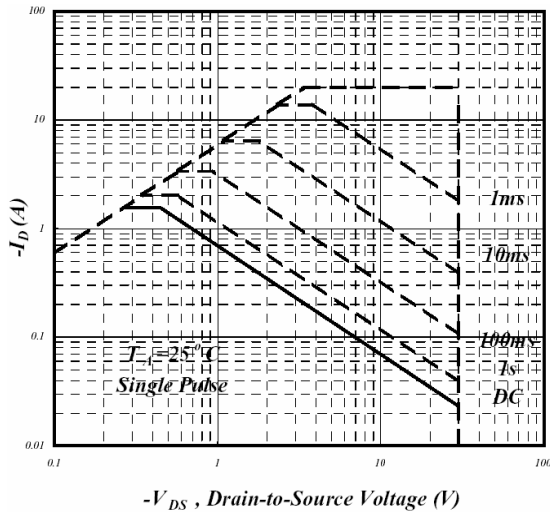


Fig 9. Maximum Safe Operating Area

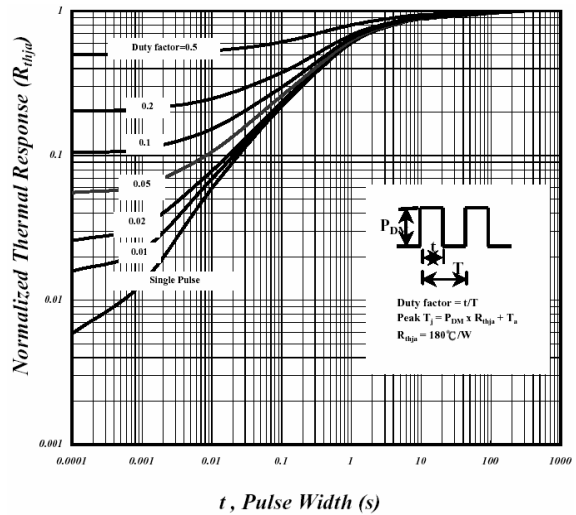


Fig 10. Effective Transient Thermal Impedance

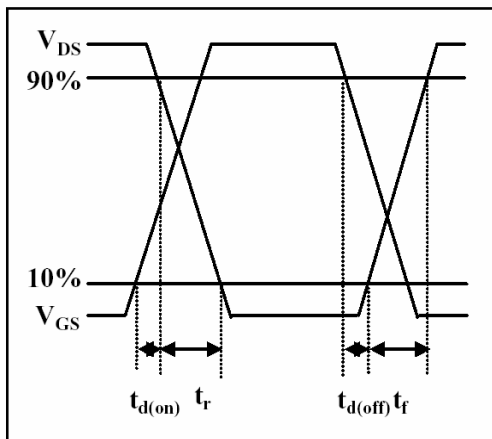


Fig 11. Switching Time Waveform

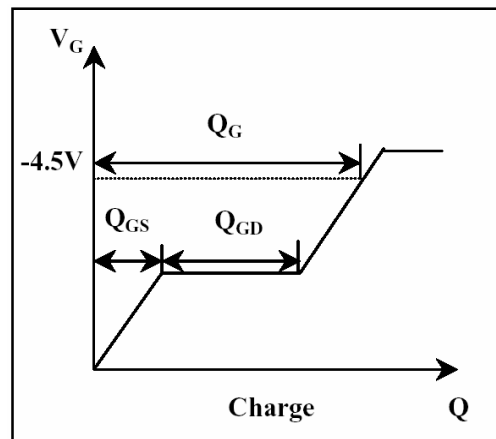


Fig 12. Gate Charge Waveform