Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ($L^2-\pi$ -MOSV)

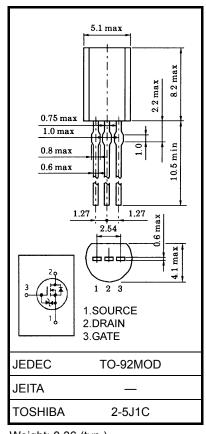
2SK2962

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance $: R_{DS (ON)} = 0.5 \Omega (typ.)$
- High forward transfer admittance $|Y_{fs}| = 1.2 \text{ S (typ.)}$
- Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 100 V)
- Enhancement mode : V_{th} = 0.8 to 2.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	100	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	100	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	1	А	
	Pulse (Note 1)	I _{DP}	3	А	
Drain power dissipation	1	PD	0.9	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	137	mJ	
Avalanche current		I _{AR}	1	А	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	0.09	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55 to 150	°C	



Weight: 0.36 (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch−a)}	138	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 221 mH, R_G = 25 Ω , I_{AR} = 1 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

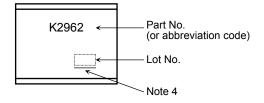
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V		_	±10	μA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = 4 V, I _D = 0.5 A		0.65	0.95	Ω
			V _{GS} = 10 V, I _D = 0.5 A — 0		0.5	0.7	32
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 0.5 A	0.6	1.2	—	S
Input capacitance	ce	C _{iss}		_	140	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	20	_	
Output capacitance		C _{oss}			45	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \int_{\mathcal{A}} \stackrel{I_{D}=0.5A}{}_{\mathcal{A}} V_{OUT}$	_	8	_	
	Turn-on time	t _{on}		_	13	_	20
	Fall time	t _f		_	45	_	- ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w =10 μ s	_	175	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	6.3	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 80 V, V _{GS} = 10 V, I _D = 1 A		4.3	_	nC
Gate−drain ("miller") Charge		Q _{gd}			2	_	

Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	Ι	_	1	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	Ι		3	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 1 A, V _{GS} = 0 V		-	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 1 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs		80		ns
Reverse recovery charge	Q _{rr}	$1DR = 1A$, $VGS = 0V$, $0DR / 01 = 50A / \mu s$	_	140		nC

Marking

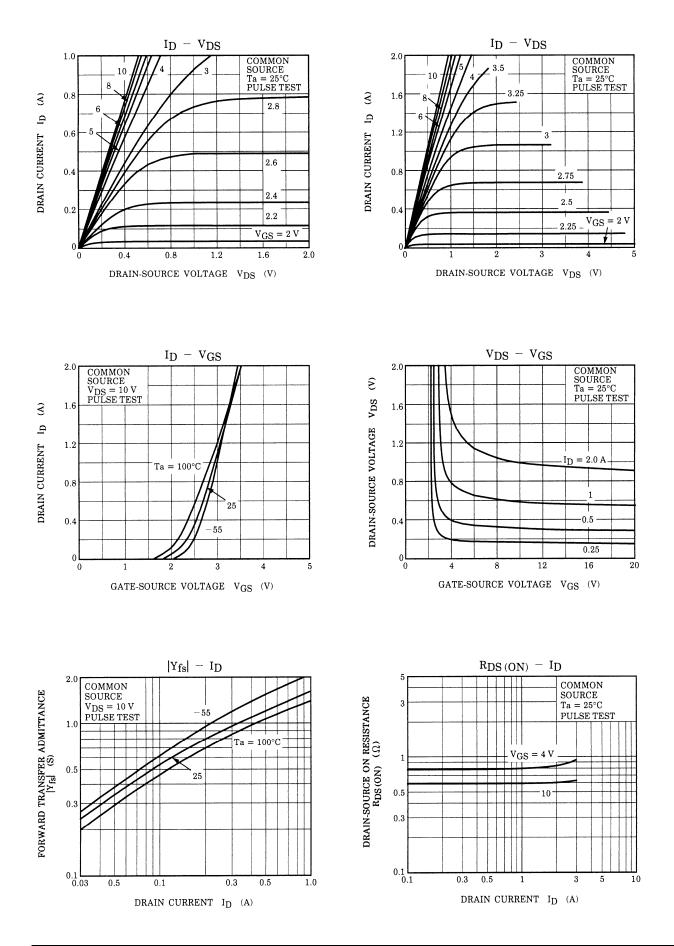


Note 4: A line under a Lot No. identifies the indication of product Labels.

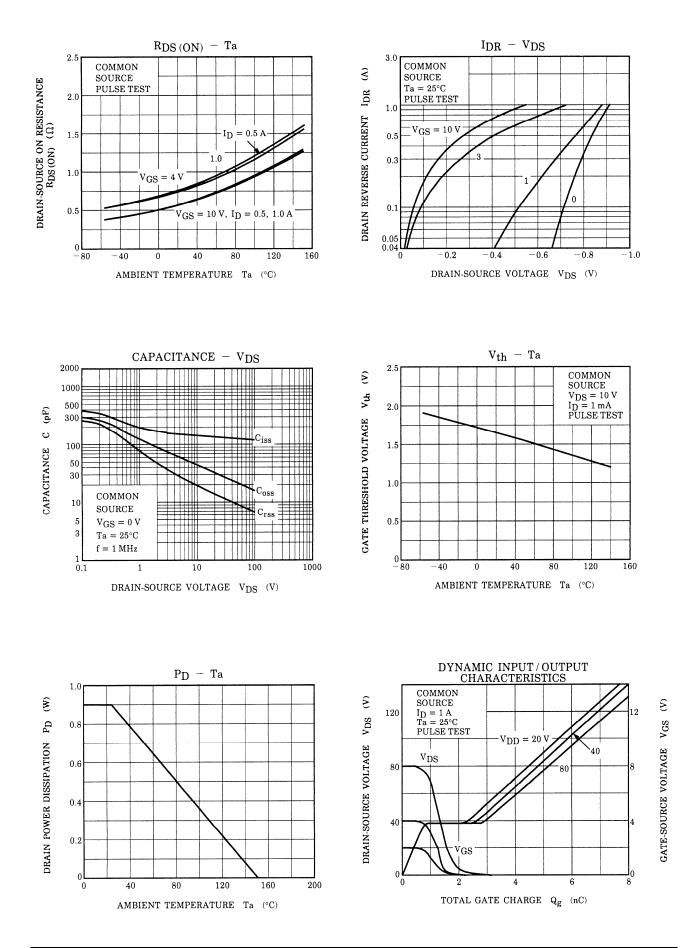
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

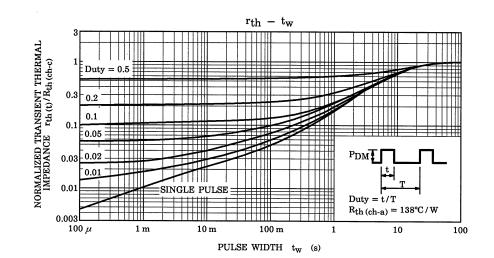
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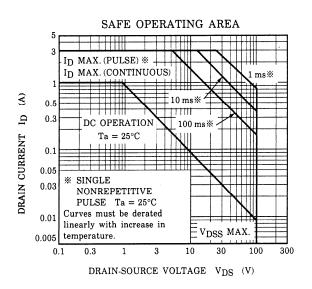
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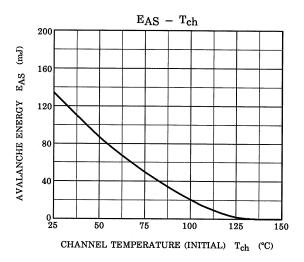


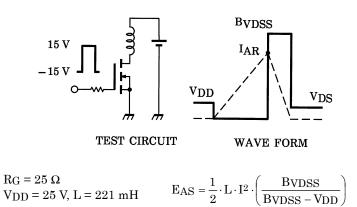
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