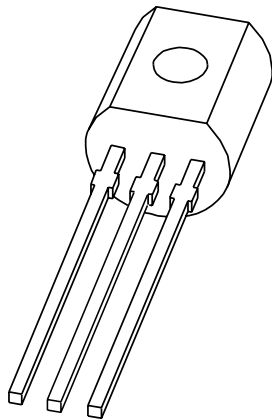


DATA SHEET



BC368

NPN medium power transistor;
20 V, 1 A

Product data sheet
Supersedes data of 2003 Dec 01

2004 Nov 05

NPN medium power transistor; 20 V, 1 A

BC368

FEATURES

- High current.

APPLICATIONS

- Linear voltage regulators
- Low side switch
- Supply line switch for negative voltages
- MOSFET driver
- Audio (pre-) amplifier.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V_{CEO}	collector-emitter voltage	–	20	V
I_C	collector current (DC)	–	1	A
I_{CM}	peak collector current	–	2	A
h_{FE}	DC current gain	85	375	–

DESCRIPTION

NPN medium power transistor (see “Simplified outline, symbol and pinning” for package details).

PRODUCT OVERVIEW

TYPE NUMBER	PACKAGE		MARKING CODE	PNP COMPLEMENT
	PHILIPS	EIAJ		
BC368	SOT54	SC-43A	C368	BC369

SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PINNING	
		PIN	DESCRIPTION
BC368		1 2 3	base collector emitter

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
BC368	SC-43A	plastic single-ended (through hole) package; 3 leads	SOT54

**NPN medium power transistor;
20 V, 1 A**

BC368

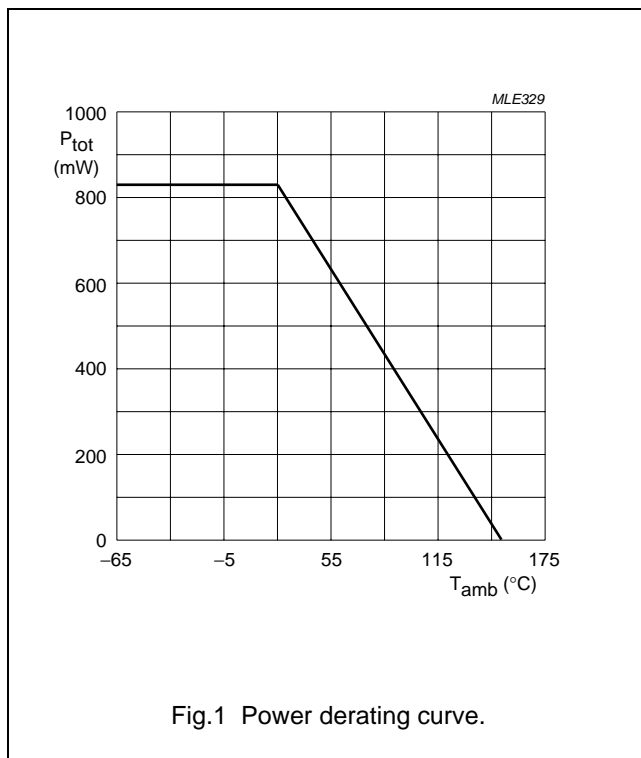
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CB0}	collector-base voltage	open emitter	–	32	V
V _{CEO}	collector-emitter voltage	open base	–	20	V
V _{EBO}	emitter-base voltage	open collector	–	5	V
I _C	output current (DC)		–	1	mA
I _{CM}	peak collector current		–	2	mA
I _{BM}	peak collector current		–	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; notes 1 and 2	–	0.83	W
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	ambient temperature		–65	+150	°C

Notes

1. Refer to SOT54 (SC-43A) standard mounting conditions.
2. Device mounted on an FR4 printed-circuit board, single-sided copper, tin-plated footprint.



NPN medium power transistor;
20 V, 1 A

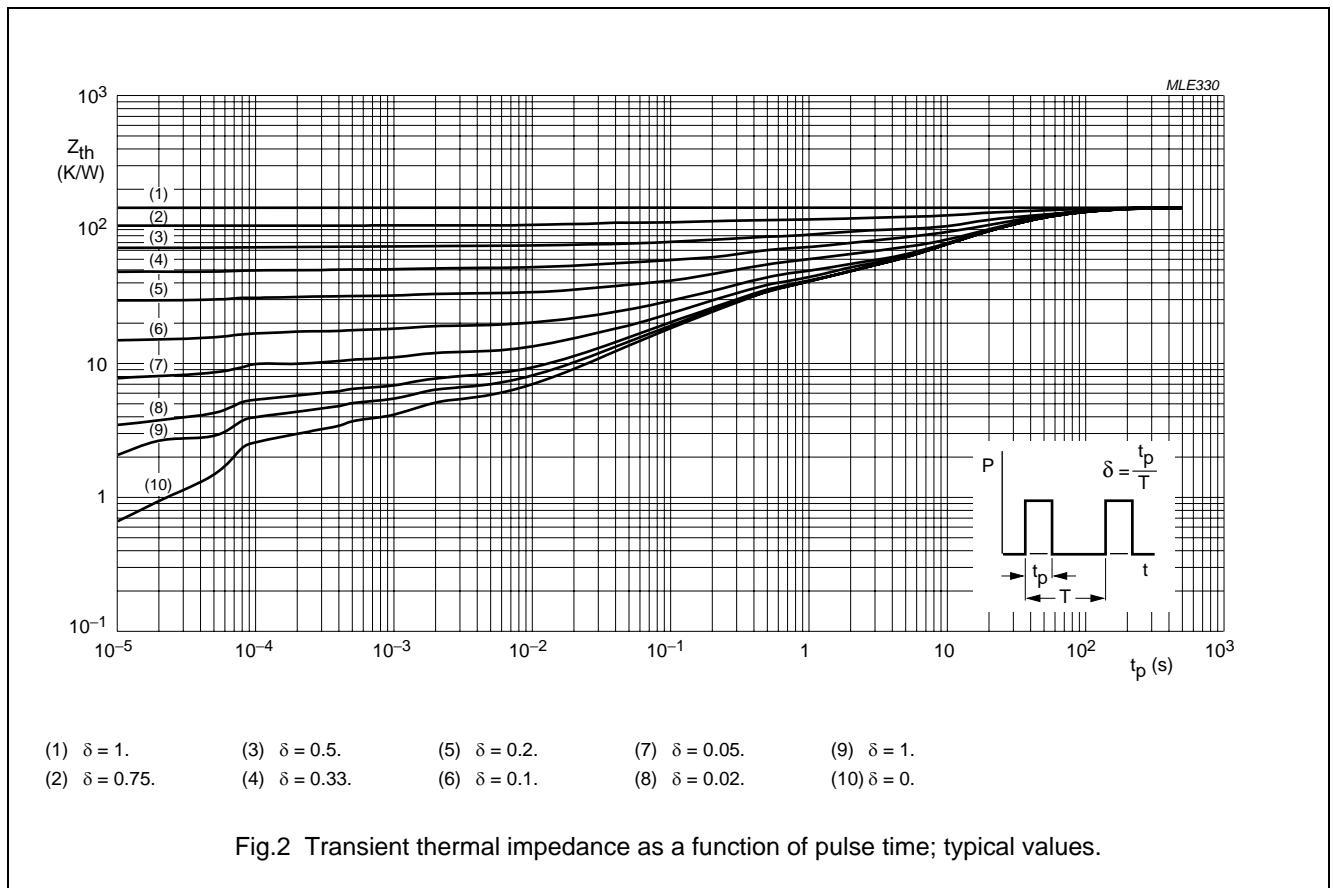
BC368

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	$T_{amb} \leq 25\text{ }^\circ\text{C}$; notes 1 and 2	150	K/W

Notes

1. Refer to SOT54 (SC-43A) standard mounting conditions.
2. Device mounted on an FR4 printed-circuit board, single-sided copper, tin-plated footprint.



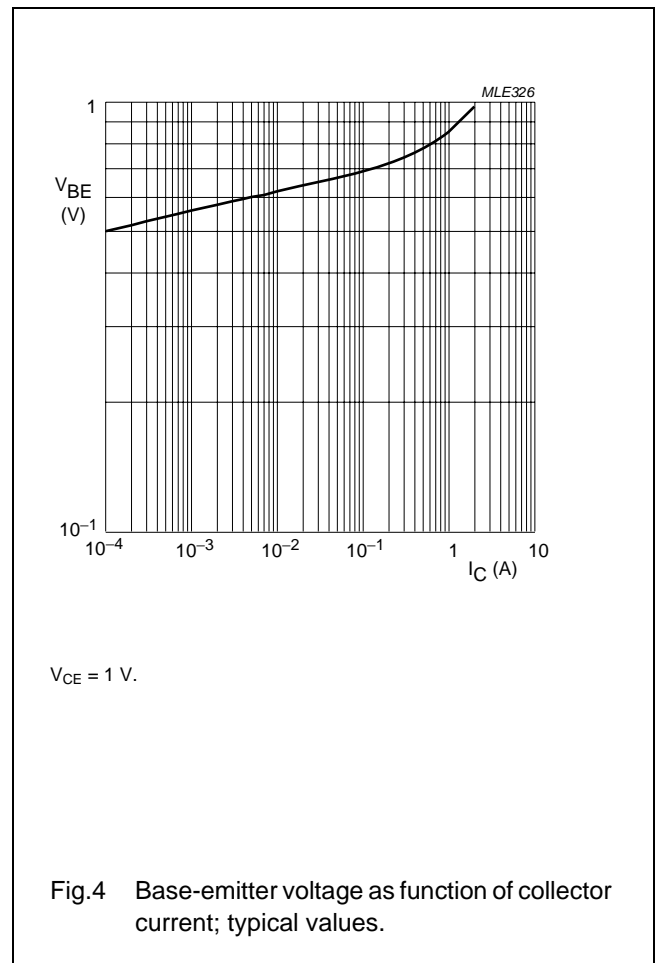
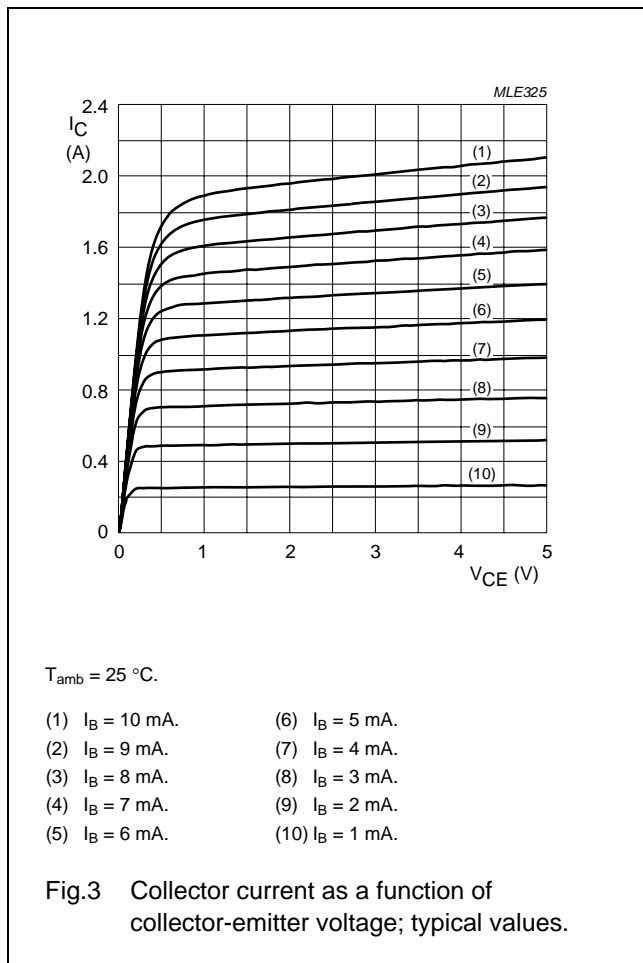
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20 V, 1 A**

BC368

CHARACTERISTICS

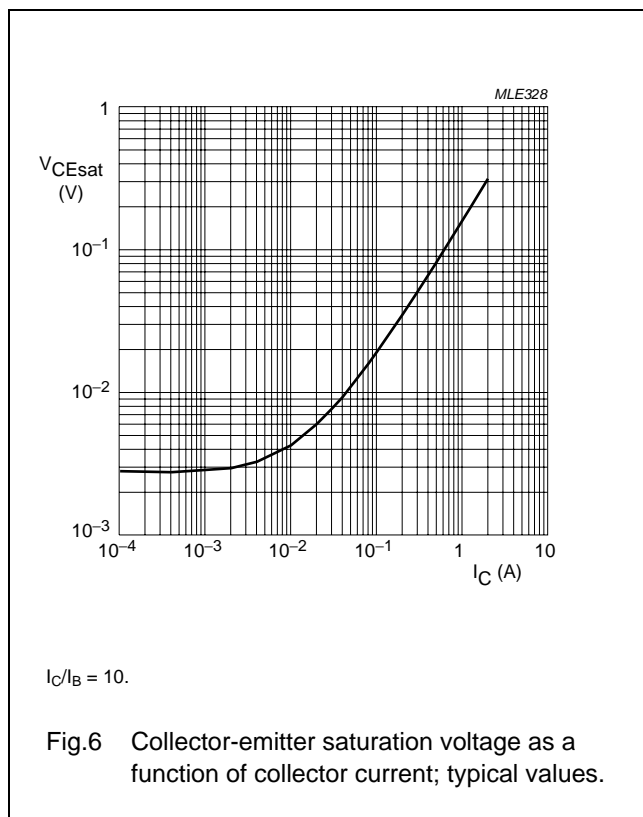
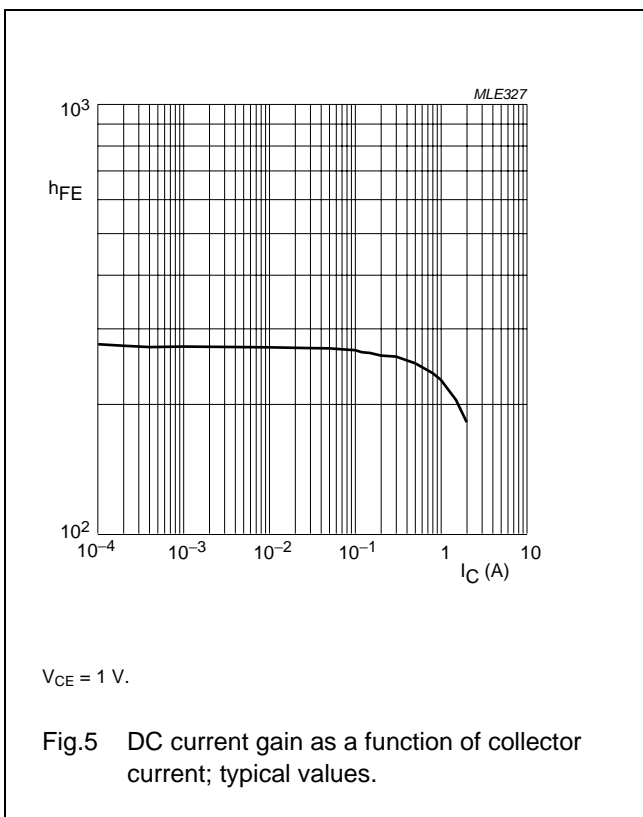
T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = 25 V; I _E = 0 A	–	–	100	nA
		V _{CB} = 25 V; I _E = 0 A; T _{amb} = 150 °C	–	–	10	µA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	–	–	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 5 mA	50	–	–	
		V _{CE} = 1 V; I _C = 500 mA	85	–	375	
		V _{CE} = 1 V; I _C = 1 mA	60	–	–	
V _{CEsat}	collector-emitter saturation voltage	I _C = 1 A; I _B = 100 mA	–	–	500	mV
V _{BE}	base-emitter voltage	V _{CE} = 10 V; I _C = 5 mA	–	–	700	mV
		V _{CE} = 1 V; I _C = 1 A	–	–	1	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz	–	22	–	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz	40	170	–	MHz



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BC368



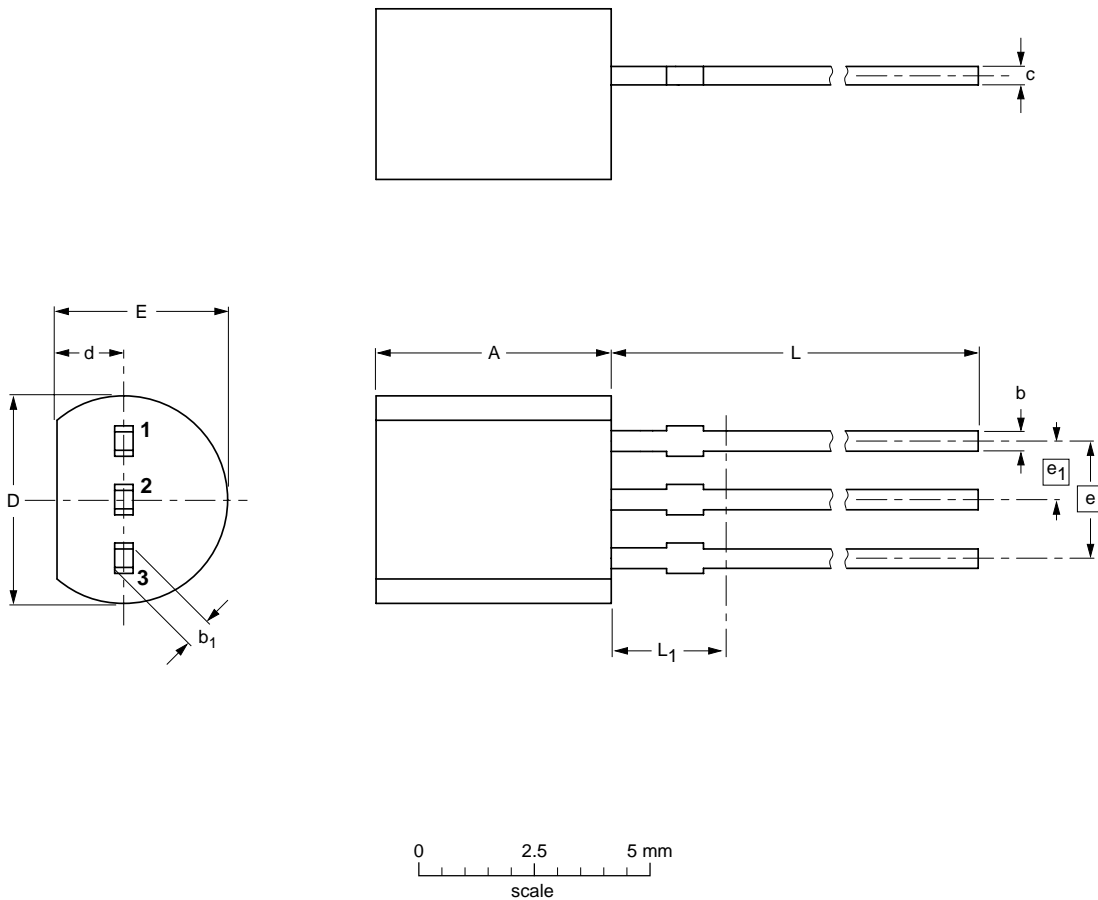
NPN medium power transistor;
20 V, 1 A

BC368

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b ₁	c	D	d	E	e	e ₁	L	L ₁ ⁽¹⁾ max.
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA		
SOT54		TO-92	SC-43A		04-06-28 04-11-16

NPN medium power transistor; 20 V, 1 A

BC368

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

1. Please consult the most recently issued document before initiating or completing a design.
2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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