

M52757FP

Wide Band Analog Switch

REJ03F0195-0201 Rev.2.01 Mar 31, 2008

Description

The M52757FP is a semiconductor integrated circuit for the RGB interface. The device features switching signals input from two types of image sources and outputting the signals to the CRT display, etc. The frequency band of video signals is 250 MHz, acquiring high-resolution images, and are optimum as an interface IC with high-resolution CRT display and various new media.

It includes Sync-separator, Video-signal-detector, and Sync-on G detector (SOG-DET.).

Features

Frequency band width: R.G.B
 Input level: R.G.B
 250 MHz
 0.7 V_{P-P} (Typ.)

Video signal-detector

 $\begin{tabular}{ll} Responsive frequency: & to 50 MHz \\ Input level: & 0.7 V_{P-P} (Typ.) \\ Detected level: & 150 mV (Typ.) \\ \end{tabular}$

(Measure bottom to DET. level)

- Only the G channel is proved with buffer video output.
- It is possible to save the consumption current by stopping current supply to Pin 2, 4, 6, 20, 24, 30, 33, 35 because SOG-DET can be operated with only V_{CC5} (Pin 15) as power save mode.
- Include Sync Separation, Video signal detector, and Sync-on G detector.

Application

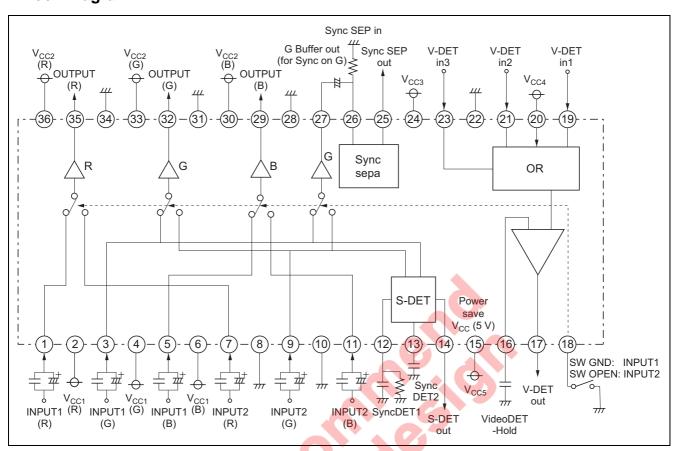
Display monitor

Recommended Operating Condition

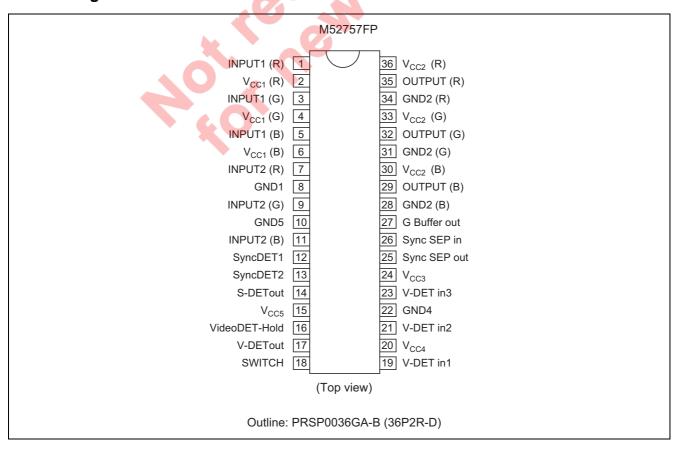
Supply voltage range: 4.75 to 5.5 V

Rated voltage: 5.0 V

Block Diagram



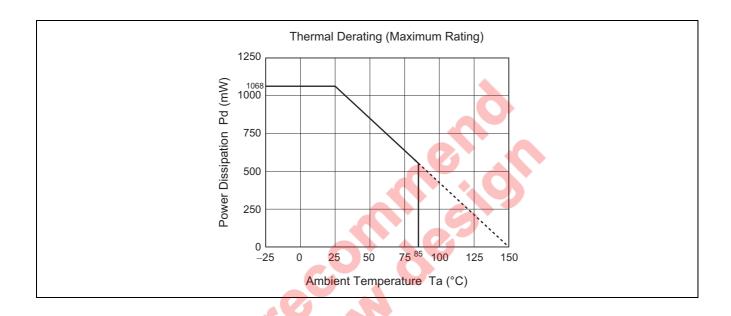
Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7.0	V
Power dissipation	Pd	1068	mW
Operating temperature	Topr	-20 to +85	°C
Storage temperature	Tstg	-40 to +150	°C
Recommended operating supply voltage	Vopr	5.0	V
Recommended operating supply voltage range	Vopr'	4.75 to 5.5	V
Electrostatic discharge	Sarge	±200	V



Electrical Characteristics

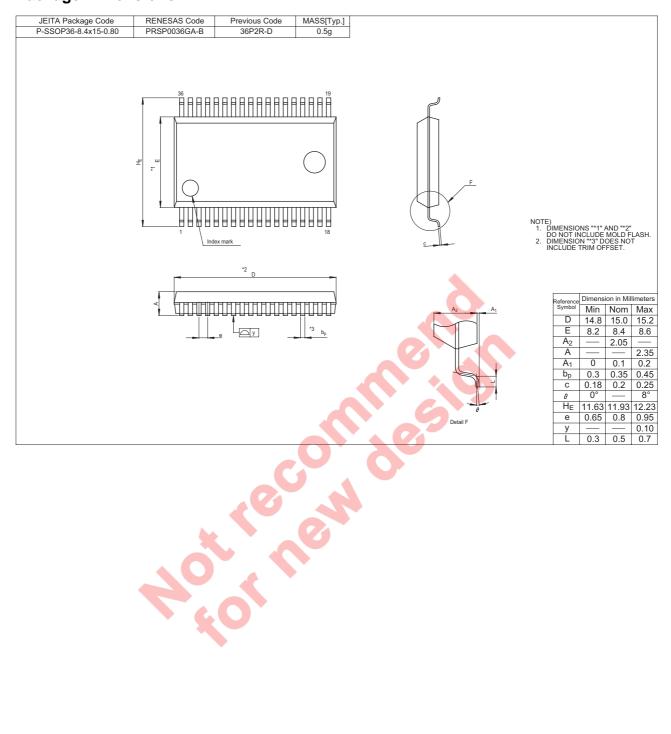
 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

		Limits			Test	() ($\begin{bmatrix} CC - 3 & V, & Ta = 23 & C \end{bmatrix}$	
Item	Symbol	Min	Тур	Max	Unit	Point (s)	Input	SW18
Circuit current 1	I _{CC} 1	45	65	85	mA	Α	_	OPN
Circuit current 2	I _{CC} 5	3	5	7	mA	15		OPN
<rgb sw=""></rgb>		l	I	<u>I</u>	<u>I</u>			
Output DC voltage 1	Vdc1	1.1	1.5	1.9	V	29, 32, 35		OPN
Output DC voltage 2	Vdc2	1.1	1.5	1.9	V	29, 32, 35	_	GND
Output DC voltage 3	Vdc3	0.5	0.9	1.3	V	27		OPN
Output DC voltage 4	Vdc4	0.5	0.9	1.3	V	27	_	GND
Maximum allowable input 1	VImax1	1.4	1.6	_	V _{P-P}	1, 3, 5	1, 3, 5	GND
Maximum allowable input 2	VImax2	1.4	1.6		V _{P-P}	7, 9, 11	7, 9, 11	GND
Voltage gain 1	Gv1	-0.1	0.5	1.1	dB	29, 32, 35	1, 3, 5	GND
Relative voltage gain 1	∆Gv1	-0.6	0	0.6	dB			alues above
Voltage gain 2	Gv2	-0.1	0.5	1.1	dB	29, 32, 35	7, 9, 11	OPN
Relative voltage gain 2	∆Gv2	-0.6	0	0.6	dB	Relative to r		
Voltage gain 3	Gv3	-0.6	0	0.6	dB	27	3	GND
Voltage gain 4	Gv4	-0.6	0	0.6	dB	27	9	OPN
Freq. characteristic 1 (100 MHz)	Fc1	-1.0	0	1.0	dB	29, 32, 35	1, 3, 5	GND
Relative Freq. characteristic 1	ΔFc1	-1.0	0	1.0	dB			alues above
Freq. characteristic 2 (100 MHz)	Fc2	-1.0	0	1.0	dB	29, 32, 35	7, 9, 11	OPN
Relative Freq. characteristic 2	ΔFc2	-1.0	0	1.0	dB			alues above
Freq. characteristic 1 (250 MHz)	Fc3	-3.0	-1.5	1.0	dB	29, 32, 35	1, 3, 5	GND
Freq. characteristic 2 (250 MHz)	Fc4	-3.0	-1.5	1.0	dB	29, 32, 35	7, 9, 11	OPN
Crosstalk between 2 inputs 1	CTI1		-60	-50	dB	29, 32, 35	1, 3, 5	GD to OP
(10 MHz)			00	30		20, 02, 00	1, 0, 0	05 10 01
Crosstalk between 2 inputs 2	CTI2	-	-60	-50	dB	29, 32, 35	7, 9, 11	OP to GD
(10 MHz)	0112	(3)				20, 02, 00	., 0,	0. 10 02
Crosstalk between 2 inputs 3	CTI3	_	-40	-35	dB	29, 32, 35	1, 3, 5	GD to OP
(100 MHz)						, ,	, ,	
Crosstalk between 2 inputs 4	CTI4		-40	-35	dB	29, 32, 35	7, 9, 11	OP to GD
(100 MHz)								
Crosstalk between channel 1	CTC1	_	-50	-40	dB	29, 32, 35	1, 3, 5	GND
(10 MHz)								
Crosstalk between channel 2	CTC2	_	-50	-40	dB	29, 32, 35	7, 9, 11	OPN
(10 MHz)								
Crosstalk between channel 3	CTC3	_	-30	-25	dB	29, 32, 35	1, 3, 5	GND
(100 MHz)								
Crosstalk between channel 4	CTC4	_	-30	-25	dB	29, 32, 35	7, 9, 11	OPN
(100 MHz)								0.15
Pulse characteristic 1	Tr1		1.6	2.5	ns	29, 32, 35	1, 3, 5	GND
	Tf1		1.6	2.5	ns	29, 32, 35	1, 3, 5	GND
Pulse characteristic 2	Tr2	_	1.6	2.5	ns	29, 32, 35	7, 9, 11	OPN
0)410.055	Tf2	_	1.6	2.5	ns	29, 32, 35	7, 9, 11	OPN
<sync sep=""></sync>	0) (
Sync input min. level	SYrv	0.2	_	_	V _{P-P}	25	26	-
Sync-sep output Hi level	SYVH	4.5	4.9	_	V	25	26	
Sync-sep output Low level	SYVL	_	0.2	0.4	V	25	26	
Sync-sep output delay time 1	TdSf	_	60	_	ns	25	26	
Sync-sep output delay time 2	TdSb	_	60	_	ns	25	26	_

Electrical Characteristics (cont.)

		Limits			Test			
Item	Symbol	Min	Тур	Max	Unit	Point (s)	Input	SW18
<sync det=""></sync>								
Min. detectable Sync amplitude	SDETrv	0.2			V_{P-P}	14	3, 9	_
Max. detectable Sync width	SDETrt	_	4.0	_	μS	14	3, 9	_
Sync-DET output high level	SDVH	4.5	4.9	_	V	14	3, 9	_
Sync-DET output low level	SDVL	_	0.2	0.4	V	14	3, 9	_
Max. allowable input noise level	SDETnv	_	_	0.05	V_{P-P}	14	3, 9	_
<video det=""></video>								
Allowable input DC range	VDinV	2.0	2.4	2.8	V	19, 21, 23	19, 21, 23	_
Allowable input amplitude range	VDin	_	0.7	1.0	V_{P-P}	19, 21, 23	19, 21, 23	_
Min. detectable input level	VDETrv	0.15	_	_	V_{P-P}	17	19, 21, 23	_
Max. allowable input noise level	VDETnv	—	—	0.1	V _{P-P}	17	19, 21, 23	_
Max. input pulse width	VDETrt	_	10.0	_	ns	17	19, 21, 23	_
Video-DET output Hi level	SDVH	4.2	4.8	_	V	17	19, 21, 23	_
Video-DET output Low level	SDVL		0.7	1.0	V	17	19, 21, 23	_
	40							

Package Dimensions



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- Renesas lechnology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Notes:

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