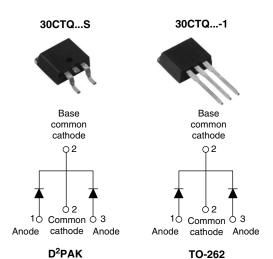


Vishay High Power Products

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 15 A			
V _R	50/60 V			

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- · Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for Q101 level

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	30	Α		
V _{RRM}		50/60	V		
I _{FSM}	t _p = 5 μs sine	1000	Α		
V _F	15 Apk, T _J = 125 °C (per leg)	0.56	V		
T _J	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	30CTQ050S 30CTQ050-1	30CTQ060S 30CTQ060-1	UNITS
Maximum DC reverse voltage	V_R	50	60	V
Maximum working peak reverse voltage	V_{RWM}	50	60	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	L TEST CONDITIONS VALUES U		UNITS	
Maximum average per device forward current	_	50 % duty cycle at T _C = 105 °C, rectangular waveform		30	
See fig. 5 per leg		$I_{F(AV)}$ 50 % duty cycle at T_C = 105 °C, re	eciangulai waveloiiii	15	Α
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1000	
surge current per leg See fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	260	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11.5 mH		mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		Α	

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30CTQ...S/30CTQ...-1

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	0.62	
		30 A		0.82	V
		15 A	T _J = 125 °C	0.56	
		30 A		0.71	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.80	m A
See fig. 2		T _J = 125 °C		45	mA
Threshold voltage	$V_{F(TO)}$	T _J = T _J maximum		0.39	V
Forward slope resistance	r _t			8.47	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		720	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 \		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		D	DC operation	3.25	°C/W
Maximum thermal resistance, junction to case per package		R_{thJC}		1.63	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	OZ.
minimum				6 (5)	kgf · cm
Mounting torque –	maximum			12 (10)	(lbf \cdot in)
Marking device			Occasional In D2DAI/	30CTQ050S	
			Case style D ² PAK	30CTQ060S	
			O	30CTQ050-1	
			Case style TO-262	30CTQ060-1	



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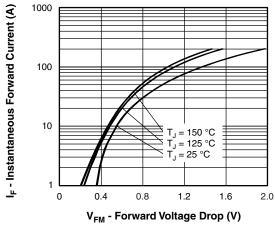


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

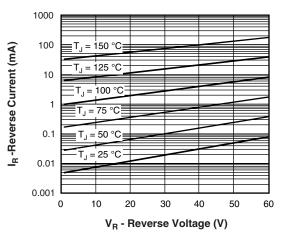


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

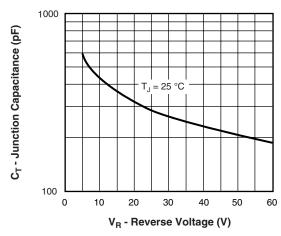


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

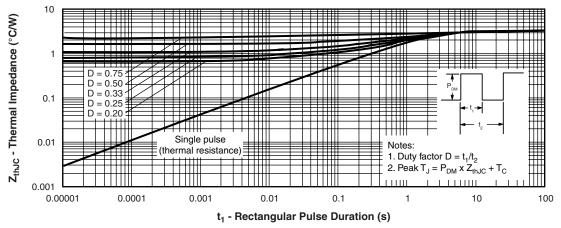
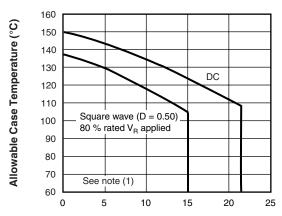


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

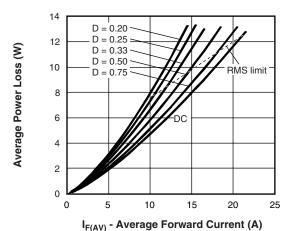


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

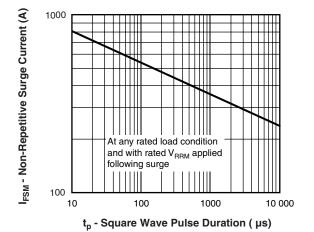


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

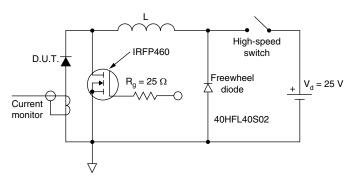


Fig. 8 - Unclamped Inductive Test Circuit

Note

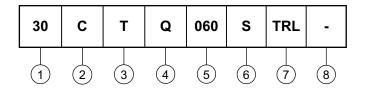
Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 10 \text{ V}$



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (30 A)

2 - Circuit configuration:

C = Common cathode

3 - T = TO-220

4 - Schottky "Q" series

5 - Voltage ratings - 050 = 50 V 060 = 60 V

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			



Vishay

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