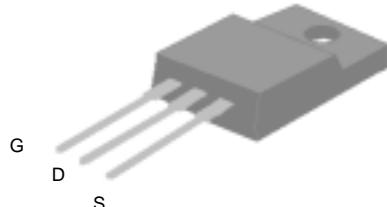


N-channel Enhancement-mode Power MOSFET

PRODUCT SUMMARY

BV _{DSS}	60V
R _{DS(ON)}	18mΩ
I _D	35A

 Pb-free; RoHS-compliant TO-220CFM



TO-220CFM (suffix I)

DESCRIPTION

The SSM9972GI achieves fast switching performance with low gate charge without a complex drive circuit. It is suitable for low voltage applications such as DC/DC converters and general switching circuits.

The SSM9972GI is in TO-220CFM for through-hole mounting where a small footprint is required on the board, and/or an external heatsink is to be attached.

These devices are manufactured with an advanced process, providing improved on-resistance and switching performance.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Units
V _{DS}	Drain-source voltage	60	V
V _{GS}	Gate-source voltage	±25	V
I _D	Continuous drain current, T _C = 25°C	35	A
	T _C = 100°C	22	A
I _{DM}	Pulsed drain current ¹	120	A
P _D	Total power dissipation, T _C = 25°C	31	W
	Linear derating factor	0.25	W/°C
T _{STG}	Storage temperature range	-55 to 150	°C
T _J	Operating junction temperature range	-55 to 150	°C

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Units
R _{θJC}	Maximum thermal resistance, junction-case	4	°C/W
R _{θJA}	Maximum thermal resistance, junction-ambient	62	°C/W

Notes:

1. Pulse width must be limited to avoid exceeding the safe operating area.
2. Pulse width <300us, duty cycle <2%.

ELECTRICAL CHARACTERISTICS (at $T_j = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-source breakdown voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	60	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown voltage temperature coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	-	0.06	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static drain-source on-resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=23\text{A}$	-	-	18	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=12\text{A}$	-	-	22	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate threshold voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1	-	3	V
g_{fs}	Forward transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=23\text{A}$	-	40	-	S
I_{DSS}	Drain-source leakage current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	10	uA
		$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_j = 150^\circ\text{C}$	-	-	25	uA
I_{GSS}	Gate-source leakage current	$V_{\text{GS}}=\pm 25\text{V}$	-	-	± 100	nA
Q_g	Total gate charge ²	$I_{\text{D}}=23\text{A}$	-	35	56	nC
Q_{gs}	Gate-source charge	$V_{\text{DS}}=48\text{V}$	-	9.5	-	nC
Q_{gd}	Gate-drain ("Miller") charge	$V_{\text{GS}}=4.5\text{V}$	-	20	-	nC
$t_{\text{d}(\text{on})}$	Turn-on delay time ²	$V_{\text{DS}}=30\text{V}$	-	12	-	ns
t_r	Rise time	$I_{\text{D}}=35\text{A}$	-	37	-	ns
$t_{\text{d}(\text{off})}$	Turn-off delay time	$R_{\text{G}}=3.3\Omega$, $V_{\text{GS}}=10\text{V}$	-	47	-	ns
t_f	Fall time	$R_{\text{D}}=0.86\Omega$	-	59	-	ns
C_{iss}	Input capacitance	$V_{\text{GS}}=0\text{V}$	-	3160	5060	pF
C_{oss}	Output capacitance	$V_{\text{DS}}=25\text{V}$	-	280	-	pF
C_{rss}	Reverse transfer capacitance	f=1.0MHz	-	230	-	pF
R_g	Gate resistance	f=1.0MHz	-	1.6	2.4	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward voltage ²	$I_{\text{S}}=23\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	1.2	V
t_{rr}	Reverse recovery time	$I_{\text{S}}=23\text{A}$, $V_{\text{GS}}=0\text{V}$,	-	36	-	ns
Q_{rr}	Reverse recovery charge	$dI/dt=100\text{A}/\mu\text{s}$	-	45	-	nC

Notes:

1. Pulse width must be limited to avoid exceeding the maximum junction temperature of 150°C .

2. Pulse width <300us, duty cycle <2%.

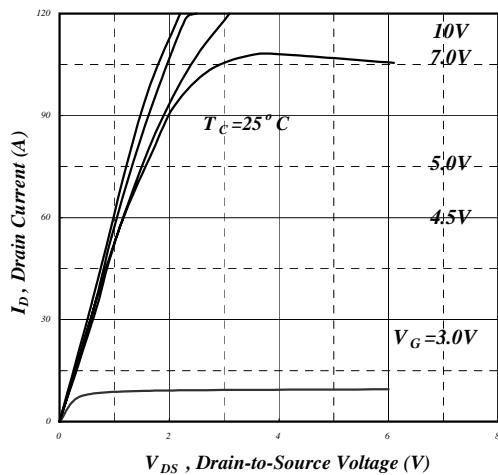


Fig 1. Typical Output Characteristics

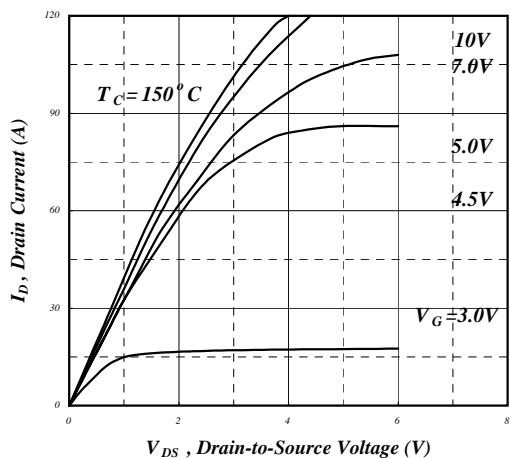


Fig 2. Typical Output Characteristics

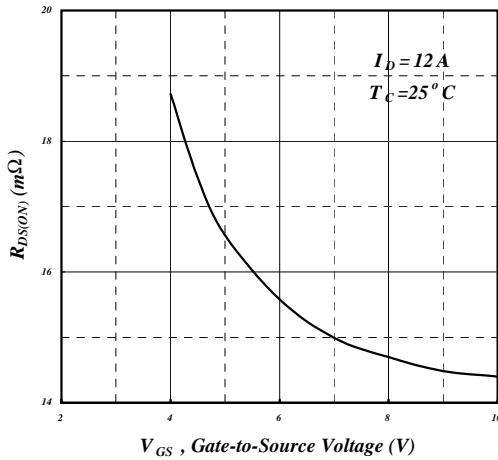


Fig 3. On-Resistance v.s. Gate Voltage

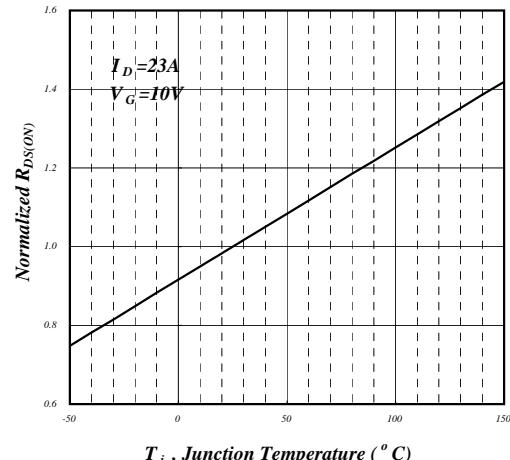


Fig 4. Normalized On-Resistance vs. Junction Temperature

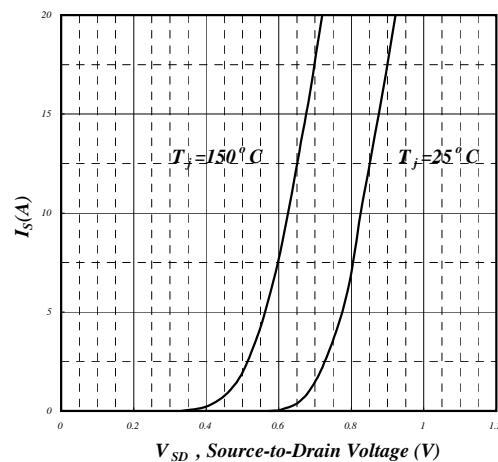


Fig 5. Forward Characteristic of Reverse Diode

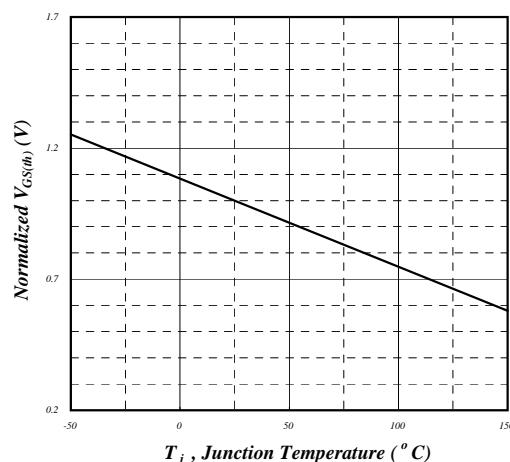


Fig 6. Gate Threshold Voltage vs. Junction Temperature

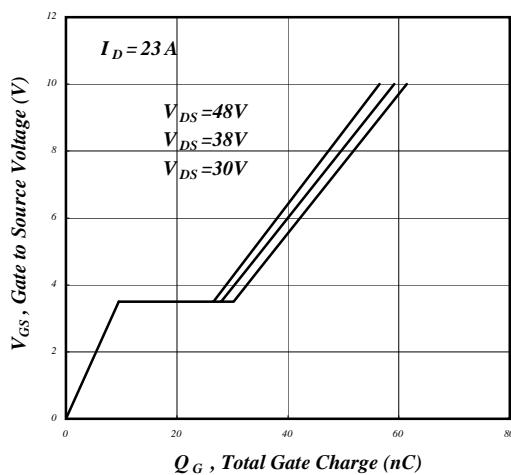


Fig 7. Gate Charge Characteristics

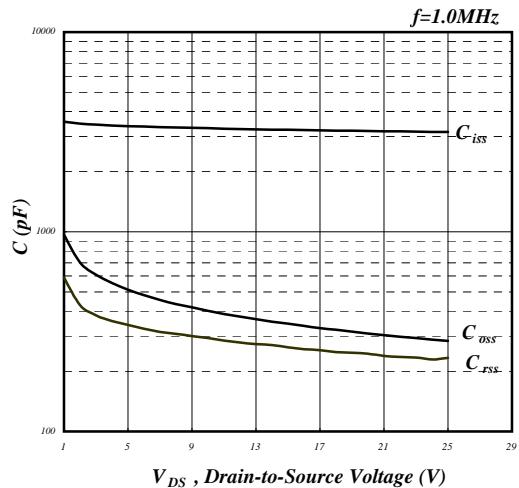


Fig 8. Typical Capacitance Characteristics

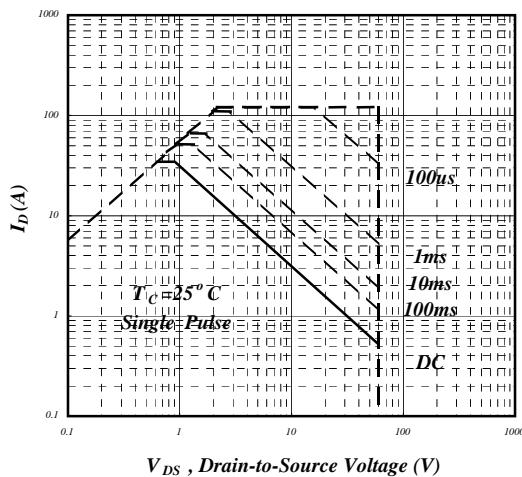


Fig 9. Maximum Safe Operating Area

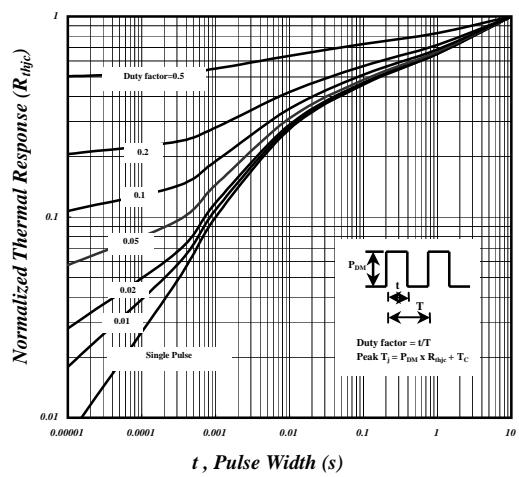


Fig 10. Effective Transient Thermal Impedance

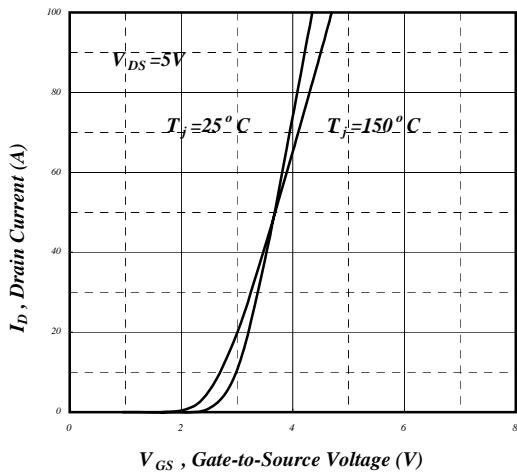


Fig 11. Transfer Characteristics

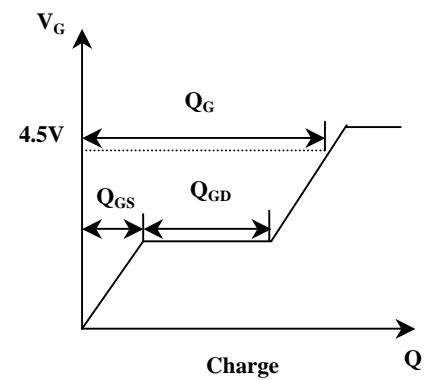
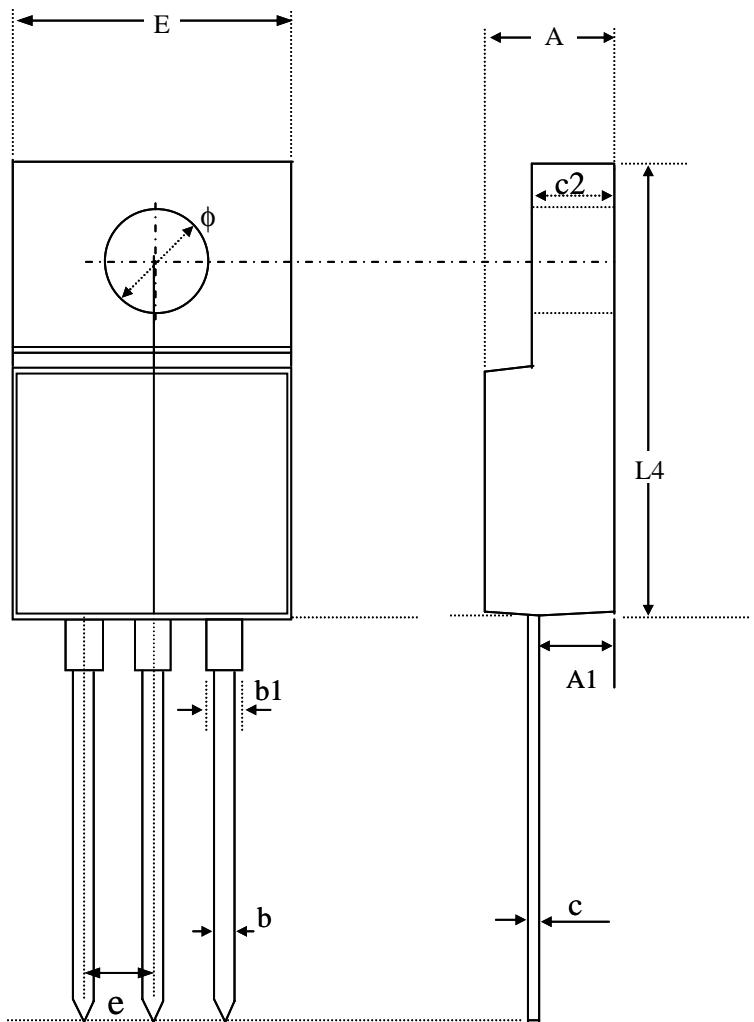


Fig 12. Gate Charge Waveform

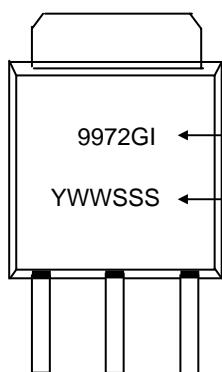
PHYSICAL DIMENSIONS - TO-220CFM



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	4.30	4.60	4.90
A1	2.30	2.65	3.00
b	0.50	0.70	0.90
b1	0.95	1.20	1.50
c	0.45	0.65	0.80
c2	2.30	2.60	2.90
E	9.70	10.00	10.40
L4	14.70	15.40	16.10
ϕ	----	3.20	----
e	----	2.54	----

1. All dimensions are in millimeters.
2. Dimension do not include mold protrusions.

PART MARKING - TO-220



PART NUMBER: 9972GI = SSM9972GI

DATE/LOT CODE:

Y = last digit of the year

WW = work week (01 -> 52)

SSS = lot code sequence

PACKING: Moisture sensitivity level MSL3

1000pcs in tubes packed inside a moisture barrier bag (MBB).

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