BLS6G2933S-130

LDMOS S-band radar power transistor Rev. 01 — 11 December 2008

Objective data sheet

Product profile

1.1 General description

130 W LDMOS power transistor intended for radar applications in the 2.9 GHz to 3.3 GHz range.

Table 1. **Typical performance**

Typical RF performance at T_{case} = 25 °C; t_p = 300 μ s; δ = 10 %; I_{Dq} = 100 mA; in a class-AB production test circuit.

Mode of operation	f	V _{DS}	PL	Gp	η_{D}	t _r	t _f
	(GHz)	(V)	(W)	(dB)	(%)	(ns)	(ns)
pulsed RF	2.9 to 3.3	32	130	12.5	47	20	6

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Typical pulsed RF performance at a frequency of 2.9 GHz to 3.3 GHz, a supply voltage of 32 V, an I_{Dq} of 100 mA, a t_p of 300 μ s with δ of 10 %:
 - Output power = 130 W
 - ◆ Power gain = 12.5 dB
 - ◆ Efficiency = 47 %
- Easy power control
- Integrated ESD protection
- High flexibility with respect to pulse formats
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (2.9 GHz to 3.3 GHz)
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding restriction of hazardous substances (RoHS)



1.3 Applications

 S-band power amplifiers for radar applications in the 2.9 GHz to 3.3 GHz frequency range

2. Pinning information

Table 2. Pinning

	3		
Pin	Description	Simplified outline	Graphic symbol
1	drain		,
2	gate	1 1	1
3	source	[1]	2 3 sym112

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLS6G2933S-130	-	ceramic earless flanged cavity package; 2 leads	SOT922-1

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Min	Max	Unit
V_{DS}	drain-source voltage	-	60	V
V_{GS}	gate-source voltage	-0.5	+13	V
I _D	drain current	-	33	Α
T _{stg}	storage temperature	-65	+150	°C
T _j	junction temperature	-	225	°C

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$Z_{\text{th(j-mb)}}$		$T_{case} = 85 ^{\circ}C; P_{L} = 130 W$		
	junction to mounting base	$t_p = 100 \ \mu s; \ \delta = 10 \ \%$	0.23	K/W
		$t_p = 200 \ \mu s; \ \delta = 10 \ \%$	0.28	K/W
		$t_p = 300 \ \mu s; \ \delta = 10 \ \%$	0.32	K/W
		$t_p = 100 \ \mu s; \ \delta = 20 \ \%$	0.33	K/W

6. Characteristics

Table 6. Characteristics

 $T_i = 25 \,^{\circ}C$ unless otherwise specified.

,	•					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 0.6 \text{ mA}$	60	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 10 \text{ V};$ $I_D = 180 \text{ mA}$	1.4	1.8	2.4	V
I _{DSS}	drain leakage current	$V_{GS} = 0 \text{ V}; V_{DS} = 28 \text{ V}$	-	-	4.2	μΑ
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	27	33	-	Α
I _{GSS}	gate leakage current	$V_{GS} = 11 \text{ V}; V_{DS} = 0 \text{ V}$	-	-	450	nΑ
9 _{fs}	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 9 \text{ A}$	8.1	13	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 6.3 \text{ A}$	-	0.085	0.135	Ω

7. Application information

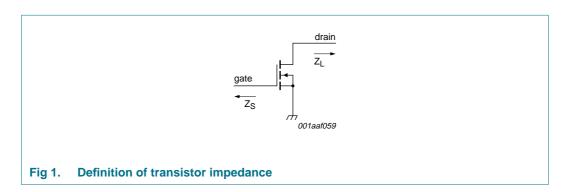
Table 7. Application information

Mode of operation: pulsed RF; t_p = 300 μ s; δ = 10 %; RF performance at V_{DS} = 32 V; I_{Dq} = 100 mA; T_{case} = 25 °C; unless otherwise specified, in a class-AB production circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
P_{L}	output power		-	130	-	W
V_{CC}	supply voltage	$P_{L} = 130 \text{ W}$	-	-	32	V
Gp	power gain	$P_{L} = 130 \text{ W}$	10	12.5	-	dB
RL_{in}	input return loss	$P_{L} = 130 \text{ W}$	-	10	-	dB
$P_{L(1dB)}$	output power at 1 dB gain compression		-	140	-	W
η_{D}	drain efficiency	$P_{L} = 130 \text{ W}$	40	47	-	%
P _{droop(pulse)}	pulse droop power	$P_{L} = 130 \text{ W}$	-	0	0.5	dB
t _r	rise time	P _L = 130 W	-	20	50	ns
t _f	fall time	P _L = 130 W	-	6	50	ns

Table 8. Typical impedance

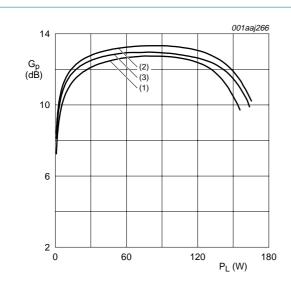
f	Z _S	Z _L
GHz	Ω	Ω
2.9	2.2 – j7.6	4.5 – j5.6
3.0	2.5 – j6.6	4.3 – j5.7
3.1	3.2 – j5.6	4.0 – j5.8
3.2	4.5 – j4.8	3.6 – j5.8
3.3	6.8 – j5.3	3.2 – j5.8



7.1 Ruggedness in class-AB operation

The BLS6G2933S-130 is capable of withstanding a load mismatch corresponding to VSWR = 5 : 1 through all phases under the following conditions: V_{DS} = 32 V; I_{Dq} = 100 mA; P_{L} = 130 W; t_{p} = 300 μ s; δ = 10 %.

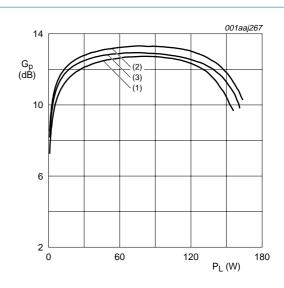
7.2 Graphs



 V_{DS} = 32 V; I_{Dq} = 100 mA; t_p = 300 μ s; δ = 10 %.

- (1) f = 2.9 GHz
- (2) f = 3.1 GHz
- (3) f = 3.3 GHz

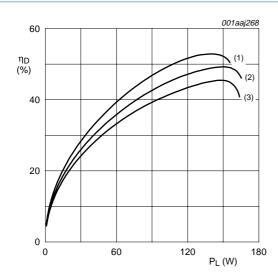
Fig 2. Power gain as a function of load power; typical values



 $V_{DS} = 32 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 100 \text{ } \mu\text{s}; \delta = 20 \text{ } \%.$

- (1) f = 2.9 GHz
- (2) f = 3.1 GHz
- (3) f = 3.3 GHz

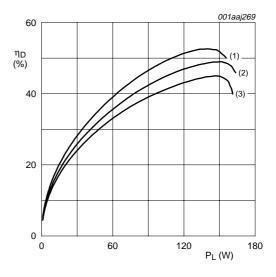
Fig 3. Power gain as a function of load power; typical values



 V_{DS} = 32 V; I_{Dq} = 100 mA; t_p = 300 $\mu s;~\delta$ = 10 %.

- (1) f = 2.9 GHz
- (2) f = 3.1 GHz
- (3) f = 3.3 GHz

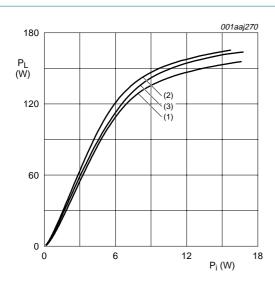
Fig 4. Drain efficiency as a function of load power; typical values



 $V_{DS} = 32 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 100 \text{ } \mu\text{s}; \delta = 20 \text{ } \%.$

- (1) f = 2.9 GHz
- (2) f = 3.1 GHz
- (3) f = 3.3 GHz

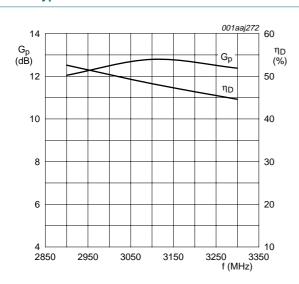
Fig 5. Drain efficiency as a function of load power; typical values



 V_{DS} = 32 V; I_{Dq} = 100 mA; t_p = 300 $\mu s; \, \delta$ = 10 %.

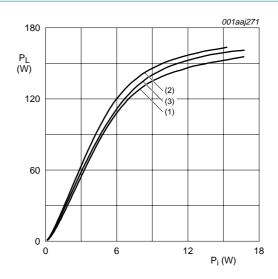
- (1) f = 2.9 GHz
- (2) f = 3.1 GHz
- (3) f = 3.3 GHz

Fig 6. Load power as a function of input power; typical values



 P_L = 130 W; V_{DS} = 32 V; I_{Dq} = 100 mA; t_p = 300 μs; δ = 10 %.

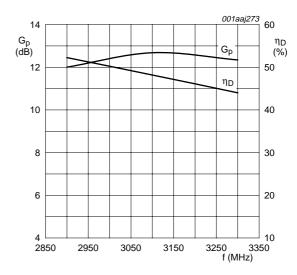
Fig 8. Power gain and drain efficiency as function of frequency; typical values



 $V_{DS} = 32 \text{ V}; I_{Dq} = 100 \text{ mA}; t_p = 100 \text{ } \mu\text{s}; \delta = 20 \text{ } \%.$

- (1) f = 2.9 GHz
- (2) f = 3.1 GHz
- (3) f = 3.3 GHz

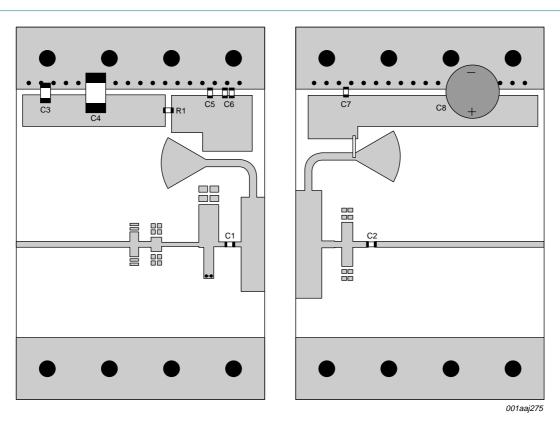
Fig 7. Load power as a function of input power; typical values



 P_L = 130 W; V_{DS} = 32 V; I_{Dq} = 100 mA; t_p = 100 μs; δ = 20 %.

Fig 9. Power gain and drain efficiency as function of frequency; typical values

8. Test information



Striplines are on a double copper-clad Duroid 6006 Printed-Circuit Board (PCB) with ϵ_r = 6.15 and thickness = 0.64 mm. See <u>Table 9</u> for list of components.

Fig 10. Component layout for 2700 MHz to 3100 MHz test circuit

Table 9. List of components See Figure 10.

Component	Description	Value	Quantity	Remarks
C1, C2, C5, C7	multilayer ceramic chip capacitor	33 pF	1	ATC 100A or equivalent
C3	multilayer ceramic chip capacitor	1 μF	1	ATC 900A or equivalent
C4	multilayer ceramic chip capacitor	$47~\mu F;63~V$	1	
C6	multilayer ceramic chip capacitor	1 nF	2	ATC 700A or equivalent
C8	electrolytic capacitor	68 μF; 63 V	1	
R1	SMD resistor	47 Ω	1	SMD 0603

9. Package outline

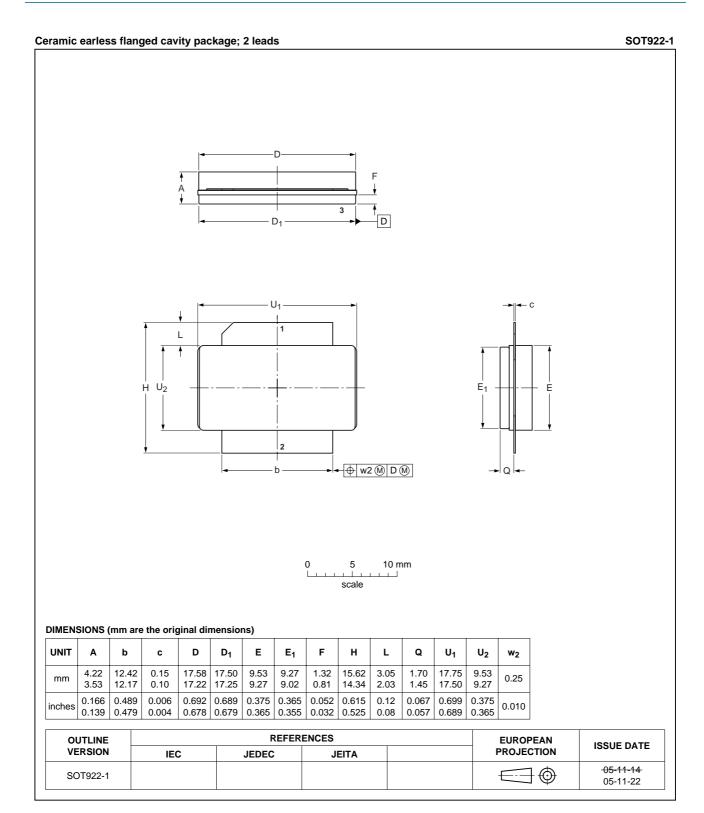


Fig 11. Package outline SOT922-1

10. Abbreviations

Table 10. Abbreviations

Acronym	Description
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
LDMOST	Laterally Diffused Metal-Oxide Semiconductor Transistor
RF	Radio Frequency
S-band	Short wave Band
SMD	Surface Mounted Device
VSWR	Voltage Standing-Wave Ratio

11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLS6G2933S-130_1	20081211	Objective data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

12.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

12.3 Disclaimers

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

13. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

14. Contents

1	Product profile
1.1	General description
1.2	Features
1.3	Applications
2	Pinning information 2
3	Ordering information
4	Limiting values
5	Thermal characteristics 2
6	Characteristics 3
7	Application information 3
7.1	Ruggedness in class-AB operation 4
7.2	Graphs
8	Test information
9	Package outline 8
10	Abbreviations 9
11	Revision history9
12	Legal information
12.1	Data sheet status
12.2	Definitions
12.3	Disclaimers
12.4	Trademarks10
13	Contact information 10
14	Contents 11

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

