

Midium Power Transistors (50V / 1A)

2SCR513P

Structure

NPN Silicon epitaxial planar transistor

Features

- 1) Low saturation voltage, typically $V_{CE \, (sat)} = 0.35 V \, (Max.) \, (I_C \, / \, I_B = 500 mA \, / \, 25 mA)$
- 2) High speed switching

Applications

Driver

Packaging specifications

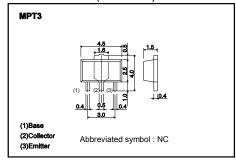
Type	Package	Taping
	Code	T100
	Basic ordering unit (pieces)	1000
2SCR513P		0

● Absolute maximum ratings (Ta = 25°C)

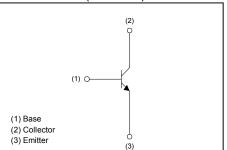
Para	Symbol	Limits	Unit	
Collector-base voltage		V_{CBO}	50	V
Collector-emitter voltage		V_{CEO}	50	V
Emitter-base voltage		V_{EBO}	6	V
Collector current	DC	I _C	1	Α
	Pulsed	I _{CP} *1	2	Α
Power dissipation		P _D *2	0.5	W
		P _D *3	2	W
Junction temperature		T_j	150	°C
Range of storage temperature		T _{stg}	-55 to 150	°C

^{*1} Pw=10ms, Single Pulse

Dimensions (Unit : mm)



• Inner circuit (Unit : mm)



^{*2} Each terminal mounted on a recommended land.

^{*3} Mounted on a ceramic board. (40x40x0.7mm³)

2SCR513P Data Sheet

●Electrical characteristic (Ta = 25°C)

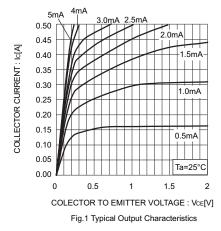
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-emitter breakdown voltage	BV_CEO	50	-	-	V	I _C = 1mA	
Collector-base breakdown voltage	BV _{CBO}	50	-	-	V	I _C = 100μA	
Emitter-base breakdown voltage	BV_EBO	6	-	-	V	I _E = 100μA	
Collector cut-off current	I _{CBO}	-	-	1	μA	V _{CB} = 50V	
Emitter cut-off current	I _{EBO}	-	-	1	μA	V _{EB} = 4V	
Collector-emitter staturation voltage	V _{CE(sat)} *1	-	130	350	mV	I_C = 500mA, I_B = 25mA	
DC current gain	h _{FE}	180	-	450	-	V_{CE} = 2V, I_{C} = 50mA	
Transition frequency	f _T *1	-	360	-	MHz	V _{CE} = 10V I _E =-200mA, f=100MHz	
Collector output capacitance	C _{ob}	-	7	-	pF	V _{CB} = 10V, I _E =0A f=1MH z	
Turn-on time	t _{on} * ₂	_	40	-	ns	- 0 5 \	
Storage time	t _{stg} * ₂	-	410	-	ns	I_{C} = 0.5A, I_{B1} = 50mA, I_{B2} =-50mA, V_{CC} ~10V	
Fall time	t _f *2	_	75	-	ns	1.82 33.11.1, V.C.C10V	

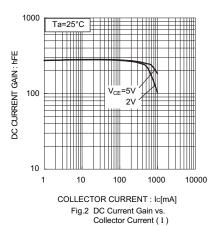
^{*1} Pulsed

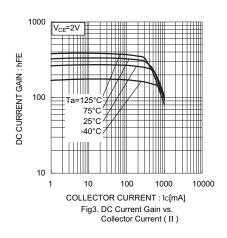
^{*2} See switching time test circuit

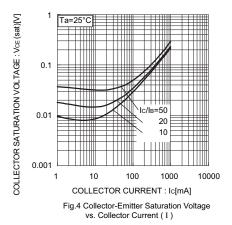
2SCR513P Data Sheet

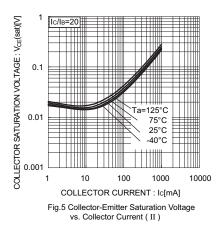
•Electrical characteristic curves

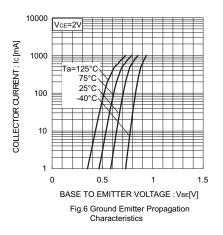


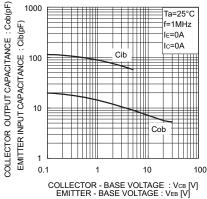


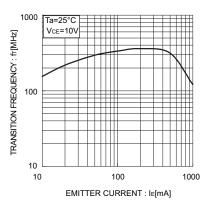












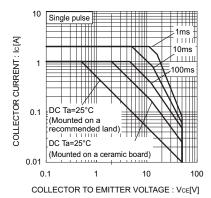


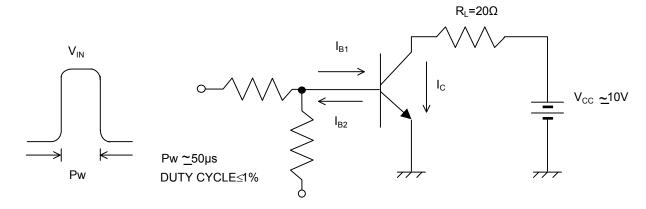
Fig.7 Emitter Input Capacitance vs.
Emitter-Base Voltage
Collector Output Capacitance vs.
Collector-Base Voltage

Fig.8 Gain BandwidthProduct vs. Emitter Current

Fig.9 Safe Operating Area

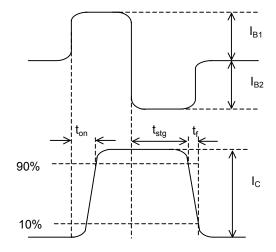
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•Switching time test circuit



BASE CURENT WAVEFORM

COLLECTOR CURRENT WAVEFORM



Notes

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