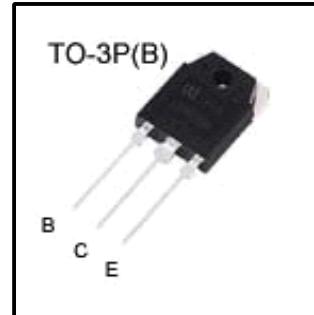


*High voltage Fast Switching NPN Power Transistor***Features**

- Very High Switching Speed
- High voltage Capability
- Wide Reverse Bias SOA

**General Description**

This Device is designed for high voltage, High speed switching characteristics required such as lighting system,switching mode power supply.

Absolute Maximum Ratings

Symbol	Parameter	Test Conditions	Value	Units
V_{CES}	Collector -Emitter Voltage	$V_{BE}=0$	700	V
V_{CEO}	Collector -Emitter Voltage	$I_B=0$	400	V
V_{EBO}	Emitter -Base Voltage	$I_C=0$	9.0	V
I_C	Collector Current		12	A
I_{CP}	Collector pulse Current		25	A
I_B	Base Current		6.0	A
I_{BM}	Base Peak Current	$t_P=5ms$	12	A
P_C	Total Dissipation at $T_c=25^\circ\text{C}$		110	W
T_J	Operation Junction Temperature		-40~150	°C
T_{STG}	Storage Temperature		-40~150	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
R_{eJC}	Thermal Resistance Junction to Case	1.13	°C/W
R_{eJA}	Thermal Resistance Junction to Ambient	62.5	°C/W

Electrical Characteristics

Symbol	Parameter	Test conditions	Value			Units
			Min	Typ	Max	
$V_{CEO(sus)}$	Collector-Emitter Breakdown Voltage	$I_c=10mA, I_b=0$	400	-	-	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_c=5.0A, I_b=1.0A$			1.0	
		$I_c=8.0A, I_b=1.6A$	-	-	1.5	V
		$I_c=12A, I_b=3.0A$			3.0	
		$I_c=8.0A, I_b=1.6A$	-	-	2.0	V
		$T_c=100^\circ C$				
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_c=5.0A, I_b=1.0A$	-	-	1.2	V
		$I_c=8.0A, I_b=1.6A$			1.6	
		$I_c=8.0A, I_b=1.6A$	-	-	1.5	V
		$T_c=100^\circ C$				
I_{EBO}	Emitter -Base Cutoff Current	$V_{eb}=9V, I_c=0V$	-	-	10	uA
hFE	DC Current Gain	$V_{ce}=5V, I_c=5.0A$	10	-	40	
		$V_{ce}=5V, I_c=8.0A$	6	-	30	
t_s	Storage Time	$V_{cc}=5.0V, I_c=0.5A$ (UI9600)	4	-	10	μs
t_f	Fall Time			-	0.8	
f_T	Current Gain Band width Product	$V_{ce}=10V, I_c=0.5A$	4			MHz

Note :

Pulse Test :Pulse width 300,Duty cycle 2%

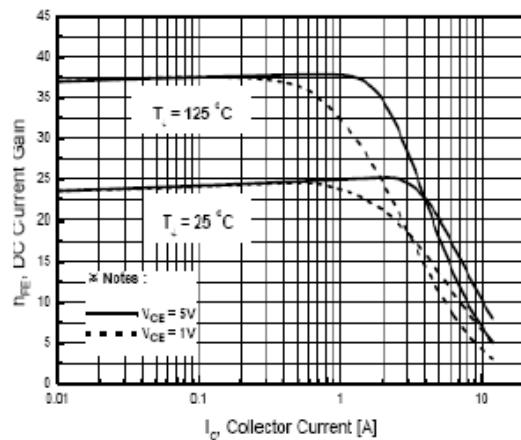


Fig.1 DC Current Gain

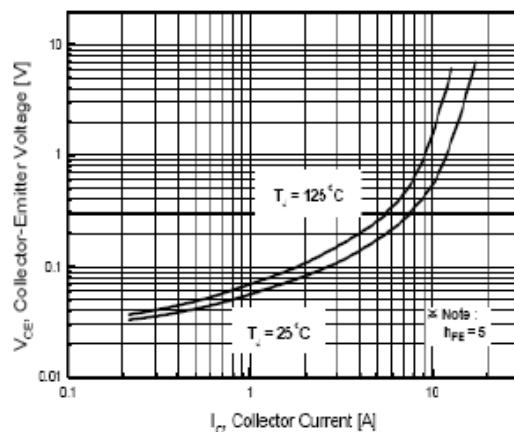


Fig.2 Collector -Emitter Saturation voltgae

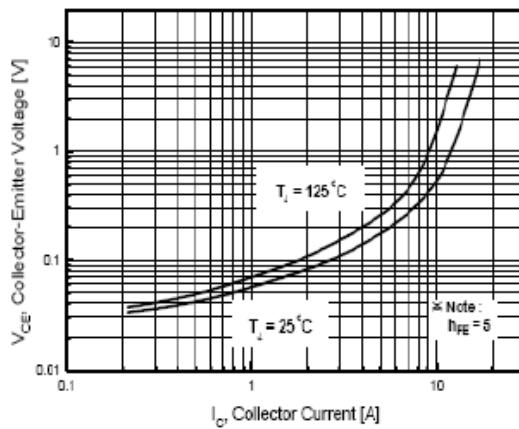


Fig.3 Base-Emitter Saturation Voltage

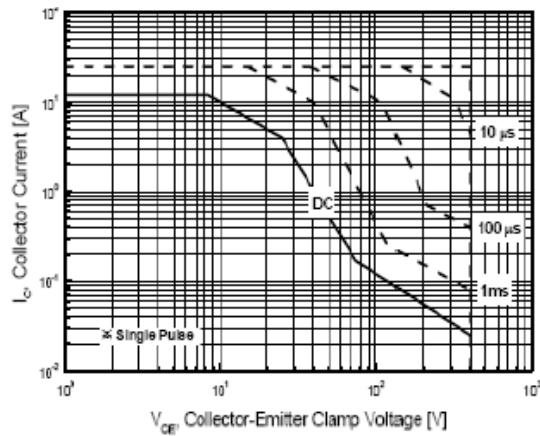


Fig.4 Safe Operation Area

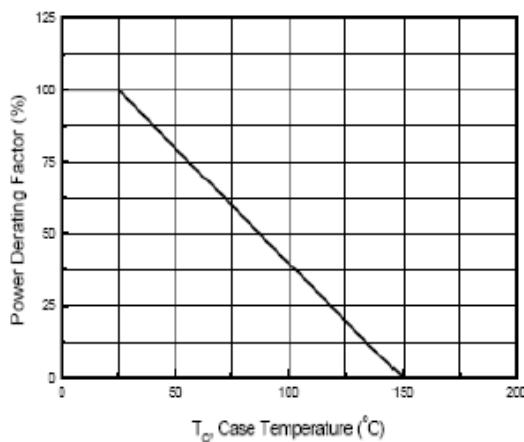


Fig.5 Power Derating

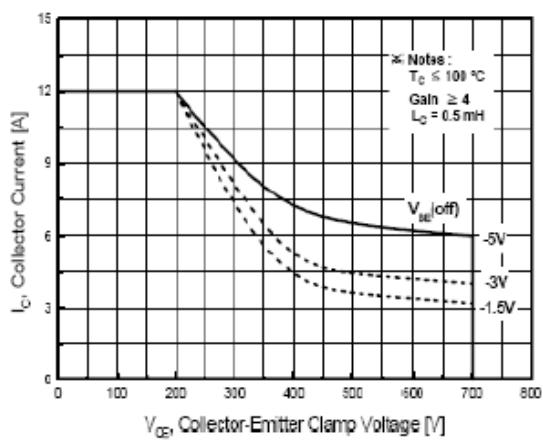
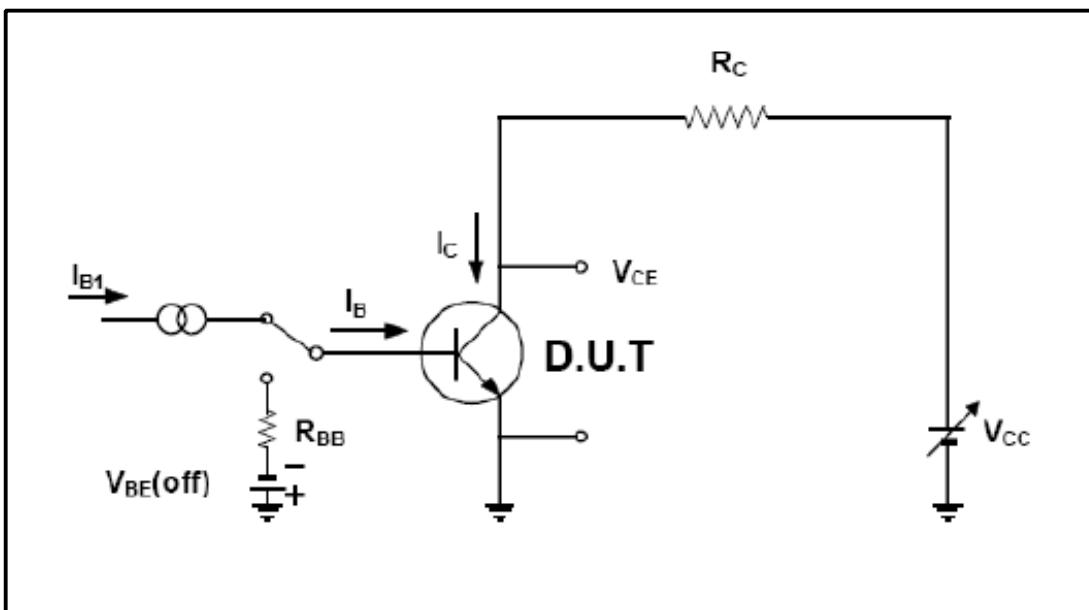
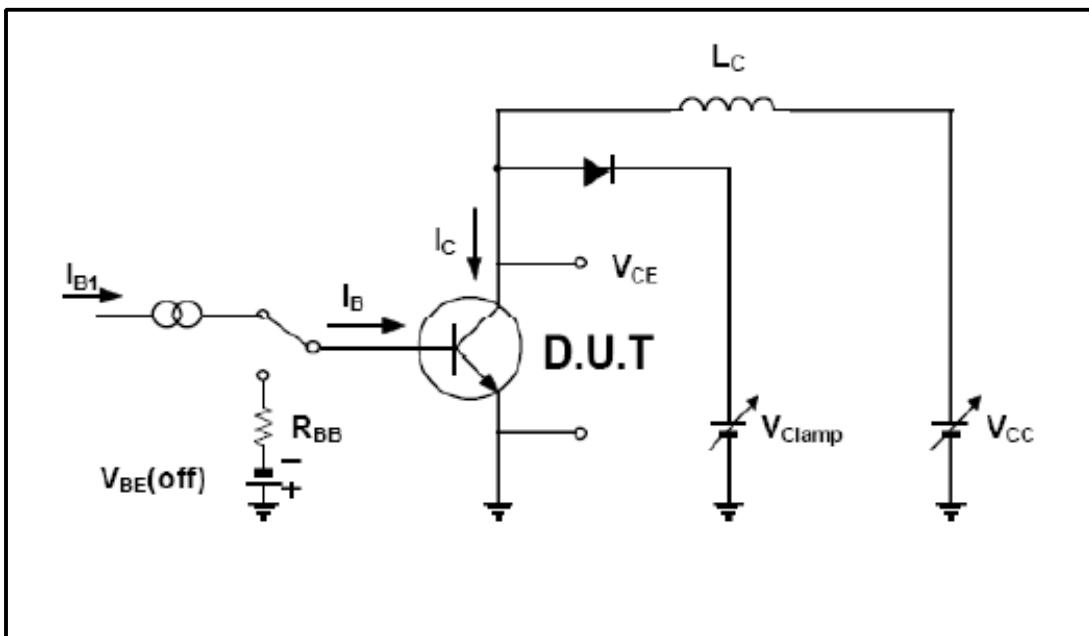


Fig.6 Reverse Biased Safe Operation Area



Resistive Load Switching Test Circuit



Inductive Load Switching & RBSOA Test Circuit

TO3P(B)Package Dimension