

Single P-channel MOSFET

ELM33403CA-S

■ General description

ELM33403CA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -20V$
- $I_d = -4A$
- $R_{ds(on)} < 64m\Omega$ ($V_{gs} = -4.5V$)
- $R_{ds(on)} < 79m\Omega$ ($V_{gs} = -2.5V$)
- $R_{ds(on)} < 120m\Omega$ ($V_{gs} = -1.8V$)

■ Maximum absolute ratings

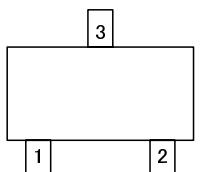
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-20	V	
Gate-source voltage	V_{gs}	± 12	V	
Continuous drain current	I_d	-4.0	A	
		-3.0		
Pulsed drain current	I_{dm}	-20	A	3
Power dissipation	P_d	1.25	W	
		0.80		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	Steady-state	$R_{\theta ja}$	75	100	°C/W	

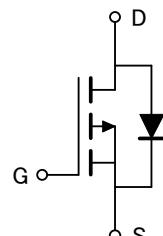
■ Pin configuration

SOT-23 (TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

■ Circuit



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■ Electrical characteristics

$T_a=25^\circ C$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVDSS	V _{GS} =0V, I _D =-250 μA	-20			V	
Zero gate voltage drain current	Idss	V _{DS} =-16V, V _{GS} =0V V _{DS} =-16V, V _{GS} =0V, T _J =125°C			-1 -10	μA	
Gate-body leakage current	Igss	V _{DS} =0V, V _{GS} =±12V			±100	nA	
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250 μA	-0.45	-0.80	-1.20	V	
On state drain current	I _{D(on)}	V _{GS} =-4.5V, V _{DS} =-5V	-20			A	1
Static drain-source on-resistance	R _{Ds(on)}	V _{GS} =-4.5V, I _D =-4A		55	64	mΩ	1
		V _{GS} =-2.5V, I _D =-3A		62	79	mΩ	
		V _{GS} =-1.8V, I _D =-2A		90	120	mΩ	
Forward transconductance	G _F	V _{DS} =-5V, I _D =-4A		12		S	1
Diode forward voltage	V _{SD}	I _S =-1A, V _{GS} =0V			-1.2	V	1
Max. body-diode continuous current	I _S				-1.6	A	
Pulsed body-diode current	I _{SM}				-3	A	3
DYNAMIC PARAMETERS							
Input capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =-15V, f=1MHz		950		pF	
Output capacitance	C _{OSS}			115		pF	
Reverse transfer capacitance	C _{RSS}			75		pF	
SWITCHING PARAMETERS							
Total gate charge	Q _G	V _{GS} =-4.5V, V _{DS} =-10V I _D =-4A		9.4		nC	2
Gate-source charge	Q _{GS}			2.0		nC	2
Gate-drain charge	Q _{GD}			3.0		nC	2
Turn-on delay time	t _{D(on)}	V _{GS} =-4.5V, V _{DS} =-10V I _D ≈ -1A, R _{GEN} =6Ω		6.3		ns	2
Turn-on rise time	t _R			3.2		ns	2
Turn-off delay time	t _{D(off)}			38.0		ns	2
Turn-off fall time	t _F			12.0		ns	2

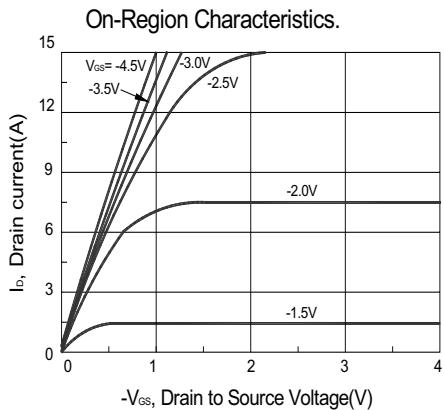
NOTE :

1. Pulsed width $\leq 300 \mu\text{sec}$ and Duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle $\leq 1\%$.

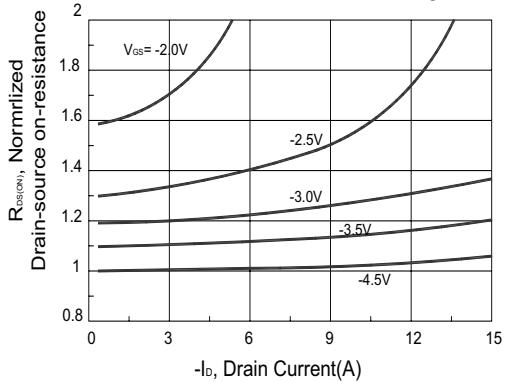
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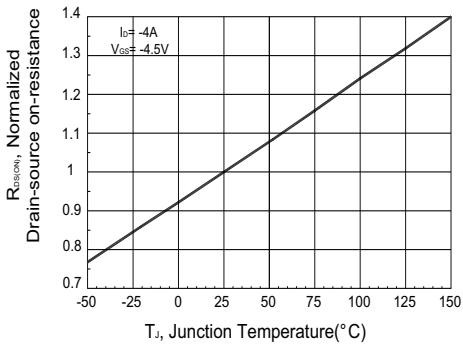
■ Typical electrical and thermal characteristics



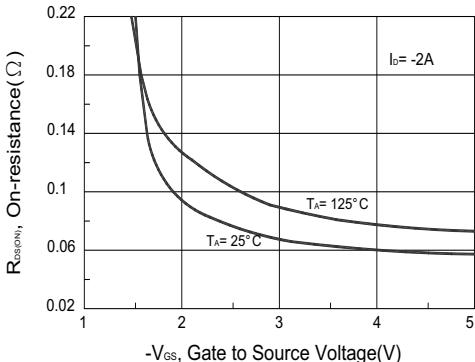
On-Resistance Variation with Drain Current and Gate Voltage.



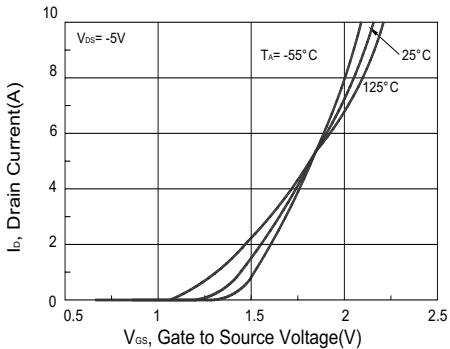
On-Resistance Variation with Temperature.



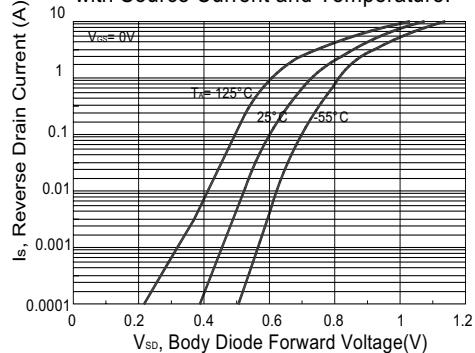
On-Resistance Variation with Gate-to-Source Voltage.



Transfer Characteristics.



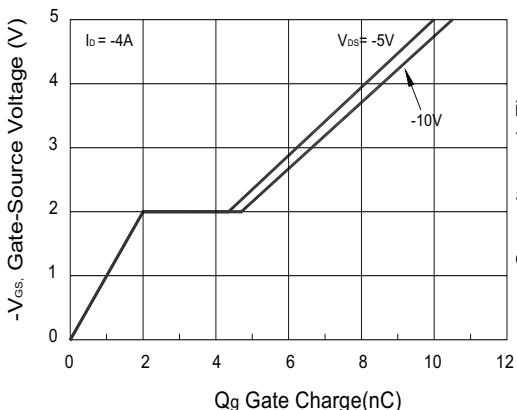
Body Diode Forward Voltage Variation with Source Current and Temperature.



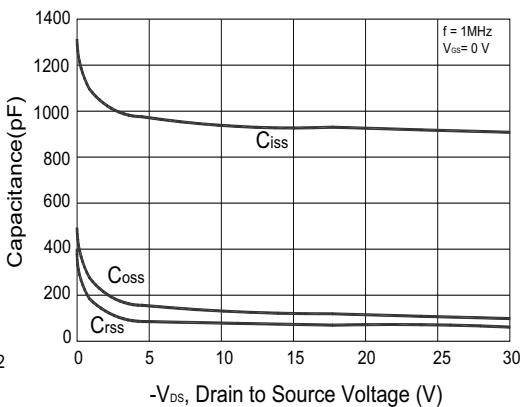
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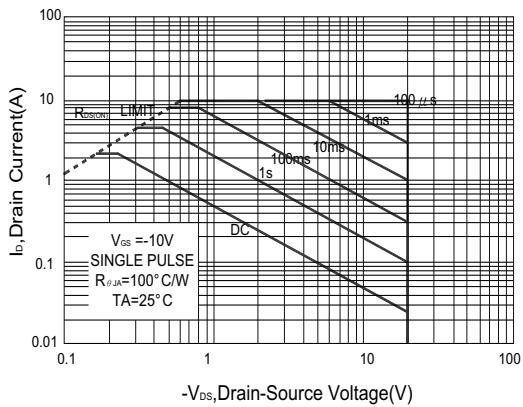
Gate-Charge Characteristics



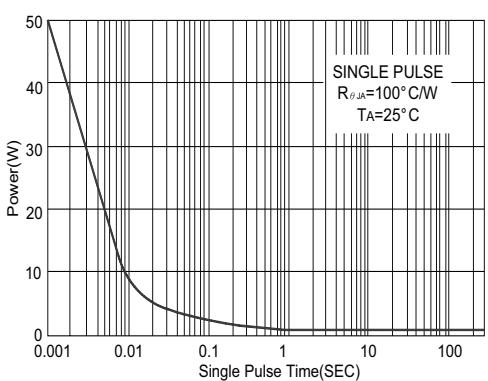
Capacitance Characteristics



Maximum Safe Operating Area.



Single Pulse Maximum Power Dissipation.



Transient Thermal Response Curve.

