| PRINCIPAL SPECIFICATIONS |  |  |  |
| :---: | :---: | :---: | :---: |

For complete model number replace ** with desired calibration frequency, $\mathrm{f}_{\mathrm{c}}$ in MHz .

## General Notes:

1. PTB- \& PTM-84A series phase shifters provide up to $360^{\circ}$ of phase shift at a selected calibration frequency in 8 binary increments ( 255 steps) resulting in $1.4^{\circ}$ resolution. Each step is generated with different cable lengths switched with PIN diodes. Advantages of the switched-line phase shifter over the digitally controlled analog type include potential for higher accuracy and smaller value of the Least Significant Bit (LSB).
2. Phase shifters based on cable switching are inherently stable and well matched. Phase shift can be set and held very closely in binary increments from the LSB to the MSB (Most Significant Bit).
3. This series can be calibrated up to 100 MHz and used to 180 MHz while the related PTM-84B series extends coverage to 500 MHz .

## GENERAL SPECIFICATIONS

| Phase Shift Range: <br> Least Significant Bit: | $0^{\circ}$ to $360^{\circ}$ nom. @ $f_{c}$ $1.4^{\circ}$ |
| :---: | :---: |
| Most Significant Bit : | $180^{\circ}$ |
| Accuracy at, $\mathrm{f}_{\mathrm{c}}$ : | $1 / 2$ of LSB typ. guaranteed monotonic |
| Impedance: | $50 \Omega$ nom. |
| VSWR: | 1.3:1 max. |
| Insertion Loss, lı: | 2.5 dB nom., 4 dB m |
| IL, Variation vs. Cont: | : $\pm 0.2 \mathrm{~dB}$ @ mid band |
| Max. Input Power: | +10 dBm |
| Control Input: <br> @ 2 loads max. per Bit | 8 Bit TTL |
| Logic Sense: | Positive |
| Supply Power: | +5 VDC@250 mA max. |
| Settling Time: | 4 us max. |
| Weight, nominal: | 10 oz (285 g) |
| Operating Temp: | $-55^{\circ}$ to $+85^{\circ} \mathrm{C}$ |

Least Significant Bit: $1.4^{\circ}$
Most Significant Bit : $180^{\circ}$
Accuracy at, $f_{c}$ : $\quad 1 / 2$ of LSB typ. guaranteed monotonic
Impedance: $\quad 50 \Omega$ nom.
VSWR: 1.3:1 max.
Insertion Loss, lL: $\quad 2.5 \mathrm{~dB}$ nom., 4 dB max
LL, Variation vs. Cont: $\pm 0.2 \mathrm{~dB} @$ mid band
Max. Input Power: +10 dBm
8 Bit TiL
Logic Sense: Positive
Supply Power: +5 VDC@250 mA max.
Settling Time: $\quad 4 \mu \mathrm{~s}$ max.
Weight, nominal: $10 \mathrm{oz}(285 \mathrm{~g})$
Operating Temp: $\quad-55^{\circ}$ to $+85^{\circ} \mathrm{C}$



