

**SPS-4170WG**

**(RoHS Compliant)**

**3.3V / 1310 nm / 8.5 Gbps Digital Diagnostic SFP+ LC SINGLE-MODE TRANSCEIVER**

**FEATURES**

- | Up to 8.5 Gbps Bi-directional Data Links
- | Complaint with SFP+ MSA
- | Compliance with Fibre Channel FC-PI-4 800-SM-LC-L
- | Compliant with 4G and 2G Fibre Channel
- | **SFF-8472 Digital Diagnostic Function**
- | 1310 nm DFB LD Transmitter
- | AC/AC Coupling according to MSA
- | **Up to 70 km at 8.5 Gbps**
- | Single +3.3 V Power Supply
- | RoHS 6/6 Compliant
- | 0 to 70°C Operating
- | Class 1 Laser International Safety Standard IEC-60825 Compliant

**DESCRIPTION**

The SPS-4170WG series single mode transceiver is small form factor pluggable module for bi-directional serial optical data communications such as 8x/4x/2x/1x Fibre Channel. It is with the SFP+ 20-pin connector to allow hot plug capability. Digital diagnostic functions are available via an I<sup>2</sup>C. This module is designed for single mode fiber and operates at a nominal wavelength of 1310 nm. The transmitter section uses a 1310 nm multiple quantum well DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

**APPLICATIONS**

- | Multi-rate 8x / 4x / 2x Fibre Channel

**LASER SAFETY**

This single mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

**ORDER INFORMATION**

P/No.	Bit Rate (Gb/s)	FC-PI	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	RoHS Compliant
<b>SPS-4170WG</b>	8 / 4 / 2	FC-PI-4	<b>70</b>	1310 DFB	SFP+ with DMI	0 to 70	Yes

**Absolute Maximum Ratings**

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Case Temperature	Topr	0	70	°C	
Power Supply Voltage	Vcc	-0.5	3.6	V	

**Recommended Operating Conditions**

Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Operating Case Temperature	Topr	0		70	°C
Power Supply Current	I <sub>CC(TX+RX)</sub>		300	350	mA
Data Rate			8.5		Gb/s

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<b>Transmitter Optical Specifications (0°C &lt; Topr &lt; 70°C, 3.13V &lt; Vcc &lt; 3.47V)</b>						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Average Launch Power	P <sub>O, Avg</sub>	4		7	dBm	1
Output Center Wavelength	λ <sub>c</sub>	1290	1310	1330	nm	
Output Spectrum Width	σ <sub>λ</sub>			1	nm	-20 dB width
Side Mode Suppression Ratio	SMSR	30			dB	
Relative Intensity Noise	RIN			-130	dB/Hz	
Average Launch Power of OFF Transmitter				-30	dBm	

1. Output power is power coupled into a 9/125 μm single-mode fiber.

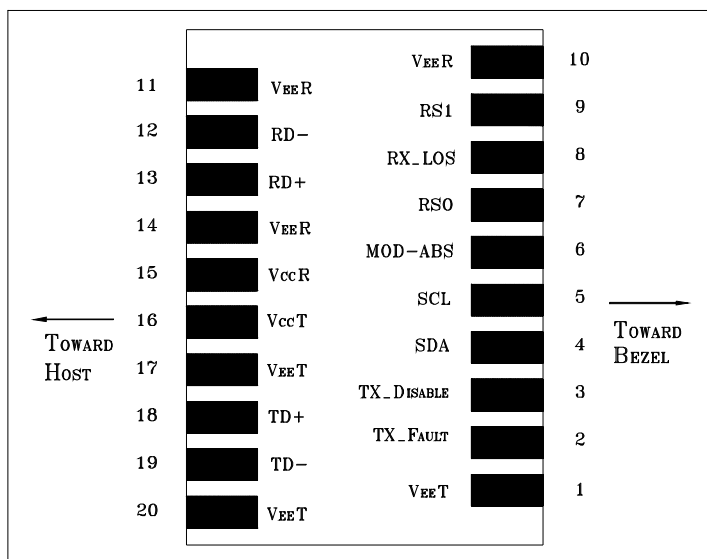
<b>Receiver Optical Specifications (0°C &lt; Topr &lt; 70°C, 3.13V &lt; Vcc &lt; 3.47V)</b>						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Unstressed Sensitivity at 8.5 Gb/s	Sens			-23	dBm	2, Average Power
Receiver Overload	P <sub>MAX</sub>	-7	---		dBm	
LOS -- Deasserted	LOS <sub>D</sub>	---	---	-28	dBm	Transition: low to high
LOS -- Asserted	LOS <sub>A</sub>	-37	---	---	dBm	Transition: high to low
Wavelength of Operation	λ <sub>c</sub>	1290		1330	nm	

2. Measured with worst ER; BER < 10<sup>-12</sup> and PRBS 2<sup>7</sup>-1.

<b>Electrical Characteristics</b>						
Parameter	Symbol	Min	Typ	Max	Units	Notes
<b>High-Speed Signal (CML) Interface Specification</b>						
Input Data Rate			8.5		Gb/s	
TX Clock Tolerance				±100	ppm	3
Differential Input Impedance	R <sub>in</sub>		100		Ω	
Differential Data Input Amplitude		150		1200	mV <sub>pp</sub>	Internally AC coupled
Output Data Rate			8.5		Gb/s	
RX Clock Tolerance				±100	ppm	3
Differential Output Impedance	R <sub>out</sub>		100		Ω	
Differential Data Output Amplitude		350	600	700	mV <sub>pp</sub>	Internally AC coupled
<b>Low-Speed Signal (LVTTTL) Interface Specification</b>						
Input High Voltage		2.0		V <sub>cc</sub> +0.3	V	
Input Low Voltage		GND		0.8	V	
Output High Voltage		2.4		V <sub>cc</sub>	V	
Output Low Voltage		GND		0.5	V	

3. Clock tolerance for 8.5Gb/s, 4.25Gb/s, 2.12Gb/s, 1.063Gb/s and 1.25 Gb/s.

## CONNECTION DIAGRAM



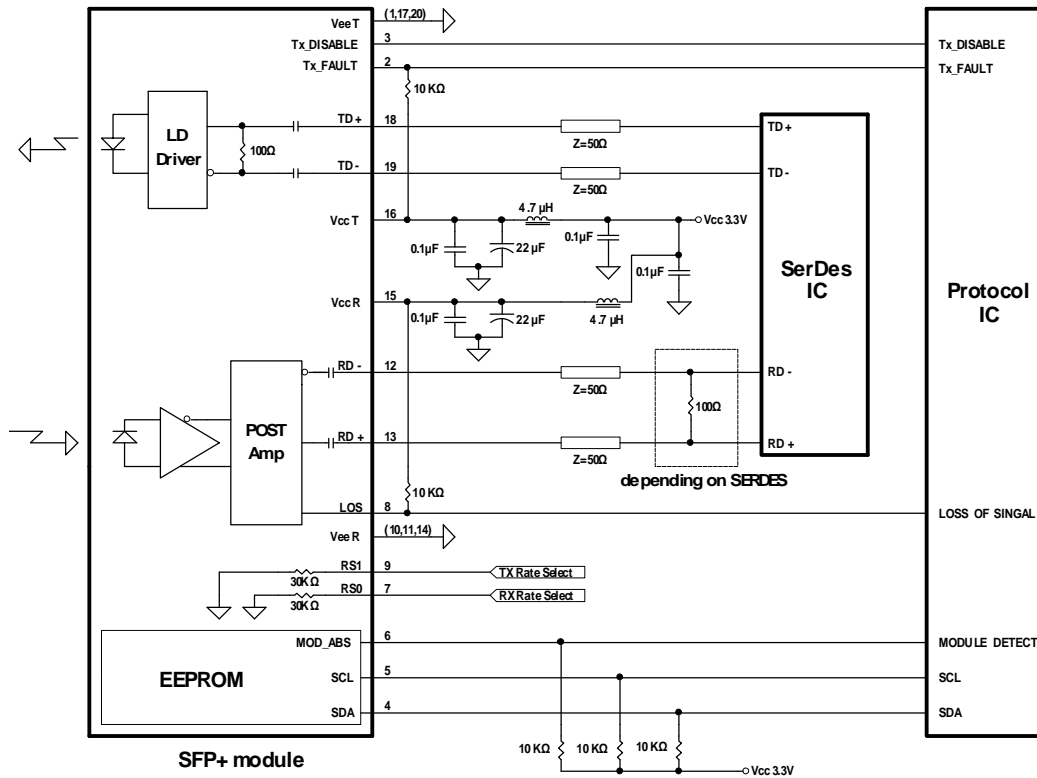
PIN	Signal Name	Description	PIN	Signal Name	Description
1	V <sub>EE</sub> T	Transmitter Signal Ground	11	V <sub>EE</sub> R	Receiver Signal Ground
2	TX_Fault	Transmitter Fault Indication. Logic "1" Output = Laser Fault. Logic "0" Output = Normal Operation	12	RD-	Inverse Receiver Data Out
3	TX_Disable	Logic "1" Input (or no connection) = Laser off, Logic "0" = Laser on.	13	RD+	Receiver Data Out
4	SDA	Modulation Definition 2 – Two wires serial ID Interface	14	V <sub>EE</sub> R	Receiver Signal Ground
5	SDL	Modulation Definition 1 – Two wires serial ID Interface	15	V <sub>CC</sub> R	Receiver Power – 3.3V±5%
6	MOD-ABS	Modulation Definition 0 – Ground in Module	16	V <sub>CC</sub> T	Transmitter Power – 3.3V±5%
7	RS0	RX Rate Select (LVTTTL). This pin has an internal 30k pulldown to ground. A signal on this pin will not affect module performance.	17	V <sub>EE</sub> T	Transmitter Signal Ground
8	RX_LOS	Loss of Signal Out (OC).	18	TD+	Transmitter Data In
9	RS1	TX Rate Select (LVTTTL). This pin has an internal 30k pulldown to ground. A signal on this pin will not affect module performance.	19	TD-	Inverse Transmitter Data In
10	V <sub>EE</sub> R	Receiver Signal Ground	20	V <sub>EE</sub> T	Transmitter Signal Ground

### Module Definition

Module Definition	PIN 4	PIN 5	PIN 6	Interpretation by Host
4	SDA	SCL	MOD-ABS	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, SDA and SDL appear as no connector (NC) and MOD-ABS is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E<sup>2</sup>PROM protocol of the ATMEL AT24C01A/02/04 family of components.

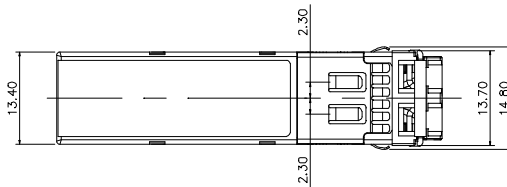
## RECOMMENDED CIRCUIT SCHEMATIC



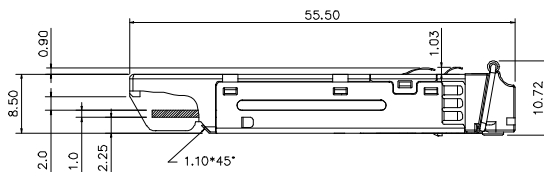
## PACKAGE DIAGRAM

Units in mm

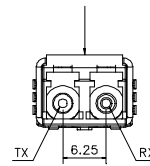
Top View



Front View

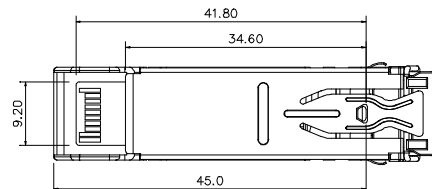


LATCH COLOR  
BLUE : SM



Side View

Bottom View



Note: Specifications subject to change without notice.

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## Revision History

Version	Subject	Release Date
1.0	Initial datasheet	2008/7/1

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