Elektronische Bauelemente

SMG2307

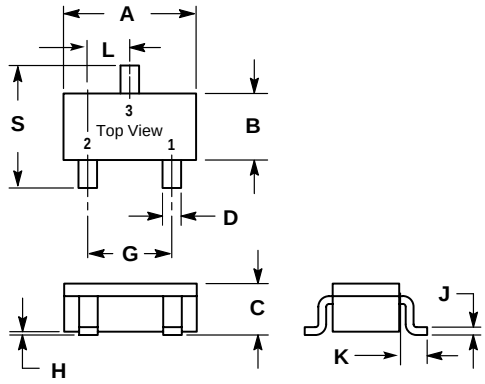
-4.0A, -16V,$R_{DS(ON)}$ 60mΩ

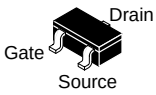
P-Channel Enhancement Mode Power Mos.FET

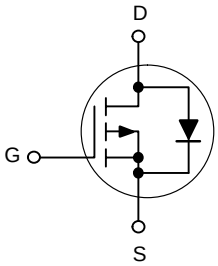
RoHS Compliant Product

Description

The SMG2307 is universally preferred for all commercial industrial surface mount application and suited for low voltage applications such as DC/DC converters.







SC-59		
Dim	**Min**	**Max**
A	2.70	3.10
B	1.40	1.60
C	1.00	1.30
D	0.35	0.50
G	1.70	2.10
H	0.00	0.10
J	0.10	0.26
K	0.20	0.60
L	0.85	1.15
S	2.40	2.80
All Dimension in mm		

Features

* Super high dense cell design for extremely low $R_{DS(ON)}$
* Reliable and rugged

Applications

* Power Management in Notebook Computer
* Protable Equipment
* Battery Powered System

Marking : 2307

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	-16	V
Sate-Source Voltage	V_{GS}	±8	V
Continuous Drain Current[3]	I_D@T_A=25°C	-4.0	A
Continuous Drain Current[3]	I_D@T_A=70 °C	-3.3	A
Pulsed Drain Current	I_{DM}	-12	A
Total Power Dissipation	P_D@T_A=25 °C	1.38	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature Range	Tj, Tstg	-55~+150	°C

Thermal Data

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient[3]	Rthj-a	90	°C/W

Electrical Characteristics(Tj=25°C Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Drain-Source Breakdown Voltage	BV_{DSS}	-16	–	–	V	V_{GS}=0V, I_D=-250uA
Breakdown Voltage Temp. Coefficient	$\triangle BV_{DS}/\triangle Tj$	–	-0.01	–	V/°C	Reference to 25°C, I_D=-1mA
Gate Threshold Voltage	$V_{GS(th)}$	-0.5	–	–	V	V_{DS}=V_{GS}, I_D=-250uA
Gate-Source Leakage Current	I_{GSS}	–	–	±100	nA	V_{GS}=±8V
Drain-Source Leakage Current (Tj=25°C)	I_{DSS}	–	–	-1	uA	V_{DS}=-16V,V_{GS}=0
Drain-Source Leakage Current (Tj=70°C)		–	–	-25	uA	V_{DS}=-12V,V_{GS}=0
Static Drain-Source On-Resistance[2]	$R_{DS(ON)}$	–	–	60	mΩ	V_{GS}=-4.5V, I_D=-4A
		–	–	70		V_{GS}=-2.5V, I_D=-3A
Total Gate Charge[2]	Qg	–	15	24	nC	I_D=-4.0A V_{DS}=-12V V_{GS}=-4.5V
Gate-Source Charge	Qgs	–	1.3	–		
Gate-Drain ("Miller") Charge	Qgd	–	4	–		
Turn-on Delay Time[2]	$Td_{(ON)}$	–	8	–	nS	V_{DS}=-10V I_D=-1A V_{GS}=-10V R_G=3.3Ω R_D=10Ω
Rise Time	Tr	–	11	–		
Turn-off Delay Time	$Td_{(Off)}$	–	54	–		
Fall Time	T_f	–	36	–		
Input Capacitance	Ciss	–	985	1580	pF	V_{GS}=0V V_{DS}=-15V f=1.0MHz
Output Capacitance	Coss	–	180	–		
Reverse Transfer Capacitance	Crss	–	160	–		
Forward Transconductance	Gfs	–	12	–	S	V_{DS}=-5V, I_D=-4A

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Forward On Voltage[2]	V_{SD}	–	–	-1.2	V	I_S=-1.2A, V_{GS}=0V.
Reverse Recovery Time[2]	Trr	–	39	–	nS	I_S=4.0A, V_{GS}=0 dl/dt=100A/uS
Reverse Recovery Charge	Qrr	–	26	–	nC	

Notes: 1.Pulse width limited by Max. junction temperature.

2.Pulse width≦300us, dutycycle≦2%.

3.Surface mounted on 1 inch$_2$ copper pad of FR4 board; 270°C/W when mounted on min. copper pad.

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P-Channel Enhancement Mode Power Mos.FET

Characteristics Curve

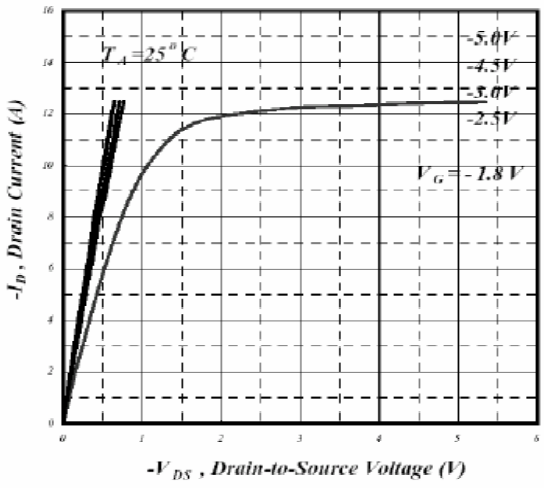

Fig 1. Typical Output Characteristics

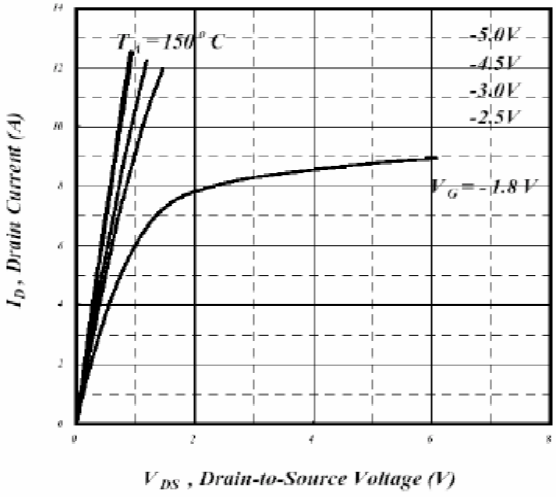

Fig 2. Typical Output Characteristics

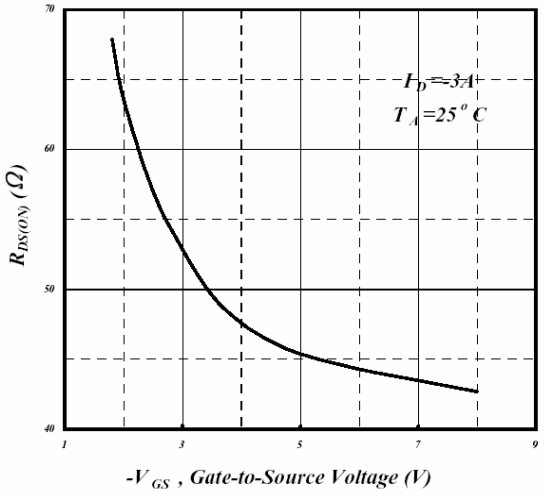

Fig 3. On-Resistance v.s. Gate Voltage

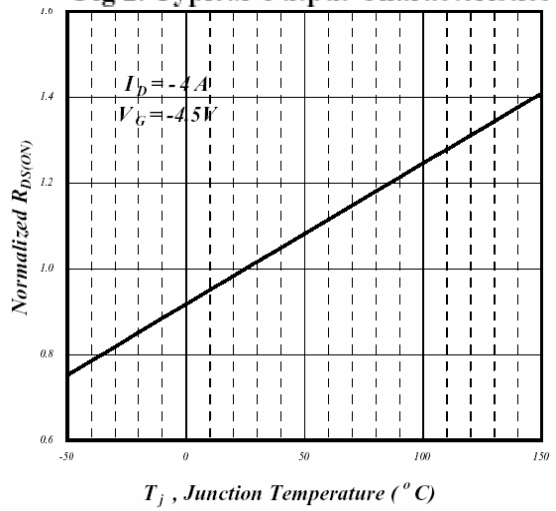

Fig 4. Normalized On-Resistance v.s. Junction Temperature

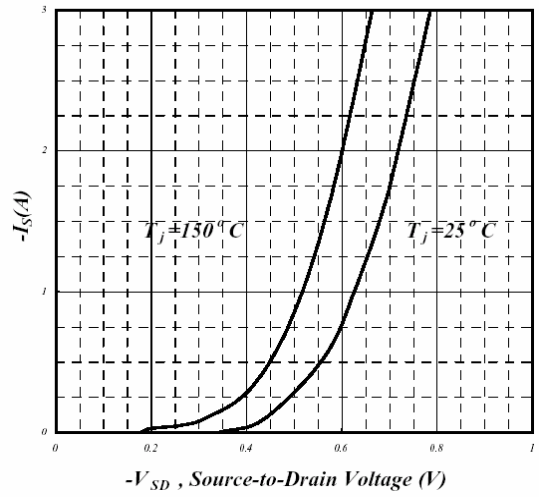

Fig 5. Forward Characteristic of Reverse Diode

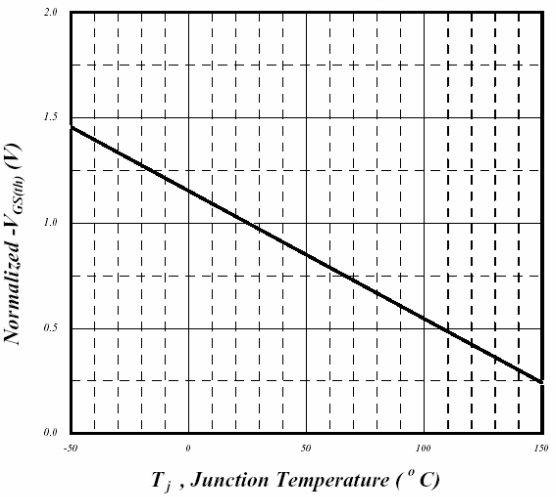

Fig 6. Gate Threshold Voltage v.s. Junction Temperature

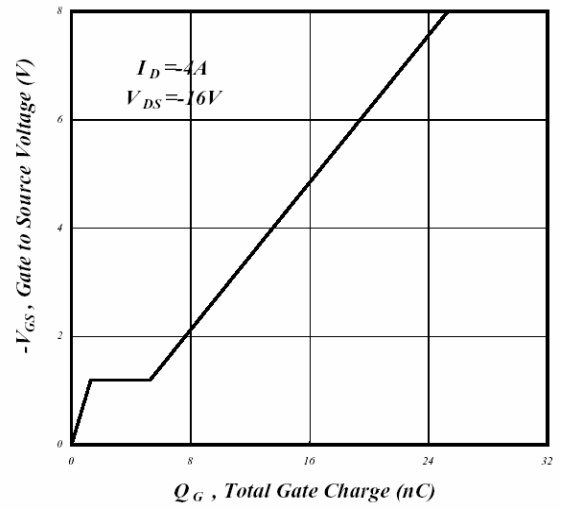

Fig 7. Gate Charge Characteristics

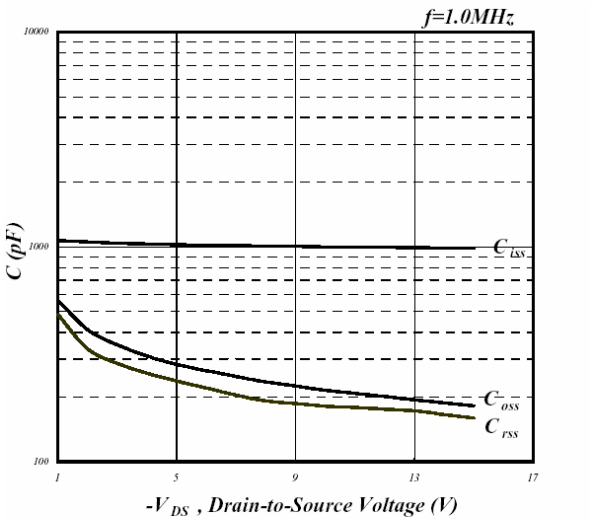

Fig 8. Typical Capacitance Characteristics

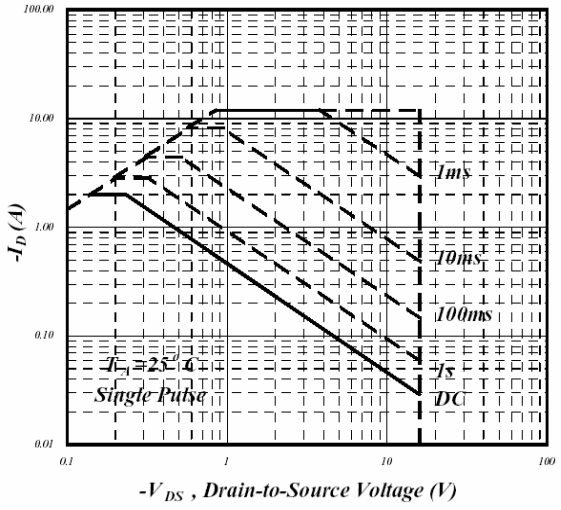

Fig 9. Maximum Safe Operating Area

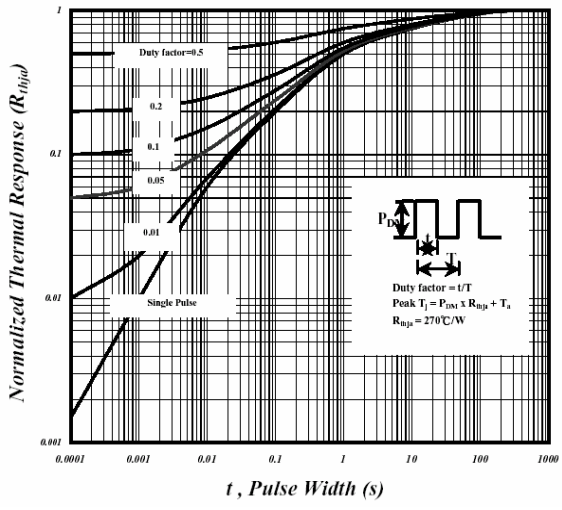

Fig 10. Effective Transient Thermal Impedance

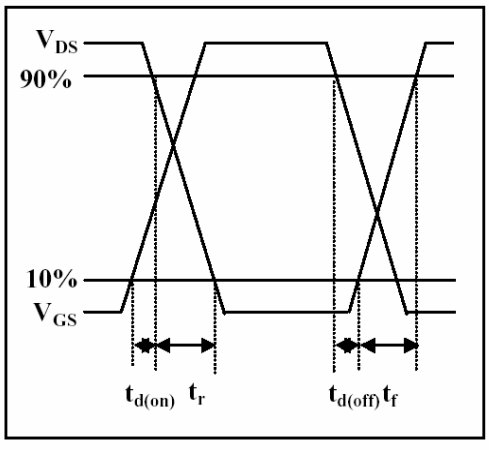

Fig 11. Switching Time Waveform

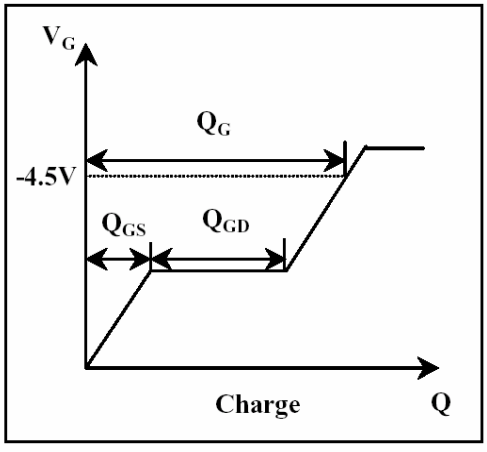

Fig 12. Gate Charge Waveform