

N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D max T _A = 25°C
30V	15mΩ @ V _{GS} = 10V	10.7A
	18.5mΩ @ V _{GS} = 4.5V	9.6A

Features

- DIOFET utilizes a unique patented process to monolithically integrate a MOSFET and a Schottky in a single die to deliver:
 - Low R_{DS(ON)} minimizes conduction losses
 - Low V_{SD} reducing the losses due to body diode conduction
 - Low Q_{rr} lower Q_{rr} of the integrated Schottky reduces body diode switching losses
 - Low gate capacitance (Q_g/Q_{gs}) ratio reduces risk of shootthrough or cross conduction currents at high frequencies
 - Avalanche rugged I_{AR} and E_{AR} rated
- Lead Free, RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- ESD Protected
- Qualified to AEC-Q101 Standards for High Reliability

Description and Applications

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power management functions

Mechanical Data

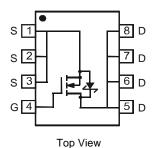
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.072 grams (approximate)







Top View



Internal Schematic

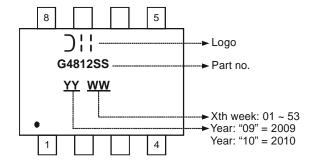
Ordering Information (Note 3)

Part Number	Case	Packaging
DMG4812SSS-13	SO-8	2500 / Tape & Reel

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information





Maximum Ratings @TA = 25°C unless otherwise specified

Character	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 4) V _{GS} = 10V	Steady State	TA = 25°C TA = 85°C	I _D	8 6.4	А
Continuous Drain Current (Note 5) V _{GS} = 10V	$t \le 10 \text{ sec}$	TA = 25°C TA = 85°C	I _D	10.7 8.6	А
Continuous Drain Current (Note 5) V _{GS} = 4.5V	current (Note 5) $V_{GS} = 4.5V$ $t \le 10 \text{ sec}$ $TA = 25^{\circ}C$ $TA = 85^{\circ}C$		I _D	9.6 7.7	А
Pulsed Drain Current (Note 6)			I _{DM}	45	Α
Avalanche Current (Notes 6 & 7)			I _{AR}	13	Α
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.3mH			E _{AR}	25.4	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P _D	1.54	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 4)	$R_{\theta JA}$	81	°C/W
Power Dissipation (Note 5)	PD	2.8	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 5)	$R_{\theta JA}$	45	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @ T_A = 25°C unless otherwise stated

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	150	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1.0	-	2.3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		-	11	15	mΩ	$V_{GS} = 10V, I_D = 10.7A$	
Static Drain-Source On-Resistance	R _{DS} (ON)	1	16.5	18.5		$V_{GS} = 4.5V, I_D = 9.6A$	
Forward Transfer Admittance	Y _{fs}	-	20	-	S	$V_{DS} = 5V, I_{D} = 10.7A$	
Diode Forward Voltage	V_{SD}	-	0.36	0.5	V	$V_{GS} = 0V, I_{S} = 1A$	
Maximum Body-Diode + Schottky Continuous Current	Is	-	-	5	Α	-	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	ı	1849	ı	рF	=	
Output Capacitance	Coss	-	158	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	ı	123	ı	рF	1 = 1.0IVII 12	
Gate Resistance	R_{g}	0.54	2.0	4.0	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge V _{GS} = 4.5V	Q_{g}	-	18.5	-	nC		
Total Gate Charge V _{GS} = 10V	Q_{g}	-	43	-	nC	$V_{DS} = 15V, V_{GS} = 10V,$	
Gate-Source Charge	Q _{gs}	-	4.7	-	nC	$I_D = 9.6A$	
Gate-Drain Charge	Q _{qd}	-	4.0	-	nC	1	
Turn-On Delay Time	t _{D(on)}	-	6.62	-	ns		
Turn-On Rise Time	t _r	-	8.73	-	ns	$V_{GS} = 10V, V_{DS} = 15V,$	
Turn-Off Delay Time	t _{D(off)}	-	36.41	-	ns	$R_G = 3\Omega, R_L = 15\Omega, I_D = 1A$	
Turn-Off Fall Time	t _f	-	4.69	-	ns	1	

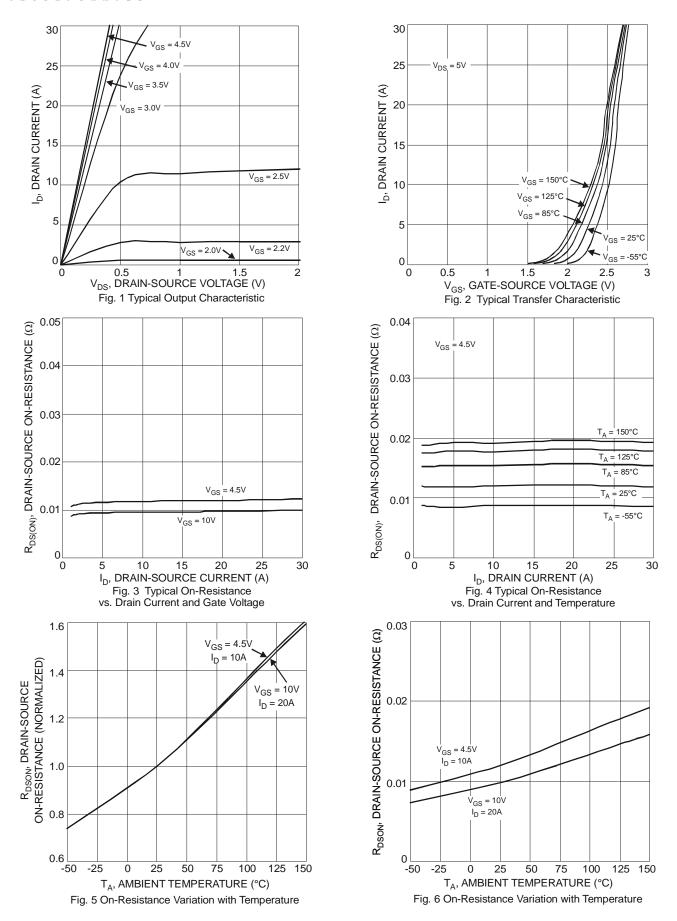
4. Device mounted on FR-4 PCB with minimum recommended pad layout. The value in any given application depends on the user's specific board design. 5. Device mounted on 1" x 1" FR-4 PCB with high coverage 1 oz. Copper, single sided , device is measured at t ≤ 10 sec.

^{6.} Repetitive rating, pulse width limited by junction temperature.

^{7.} I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep T_J = 25°C 8. Short duration pulse test used to minimize self-heating effect.

^{9.} Guaranteed by design. Not subject to production testing.







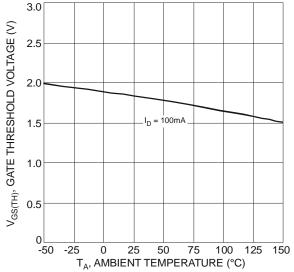
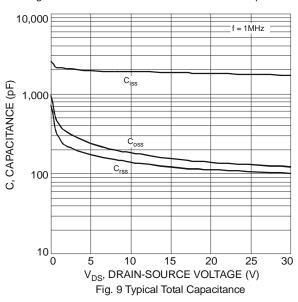
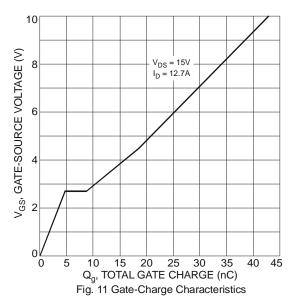
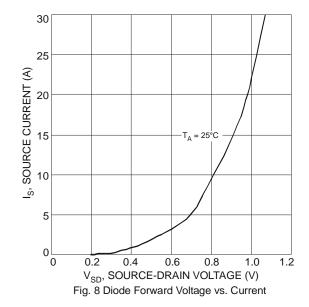
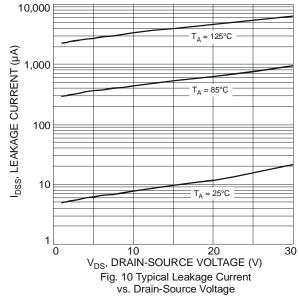


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

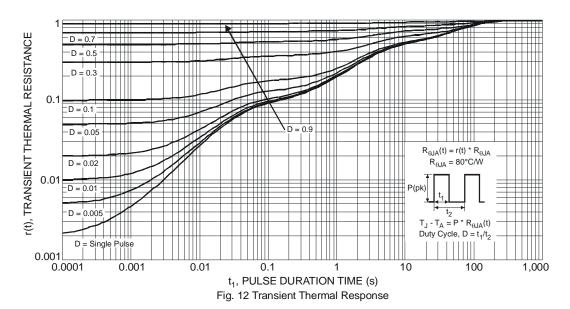




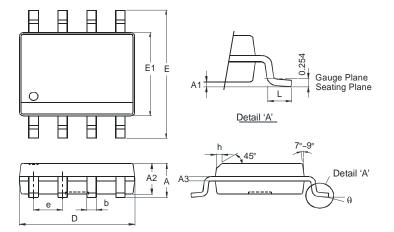






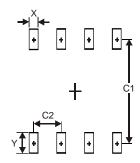


Package Outline Dimensions



SO-8				
Dim	Min	Max		
Α	-	1.75		
A 1	0.10	0.20		
A2	1.30	1.50		
А3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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