

# -200mA / -30V Low $V_{\text{CE}}$ (sat) Digital transistors (with built-in resistors)

# DTB723YE / DTB723YM

#### Applications

Inverter, Interface, Driver

#### ●Feature

- 1) VCE (sat) is lower than conventional products.
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

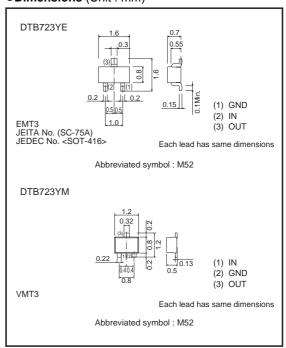
#### Structure

PNP epitaxial plannar silicon transistor (Resistor built-in type)

Packaging specifications

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	Package	EMT3	VMT3				
	Packaging type	Taping	Taping				
	Code	TL	T2L				
Part No.	Basic ordering unit (pieces)	3000	8000				
DTB723YE		0	_				
DTB723YM		-	0				

### ●Dimensions (Unit:mm)

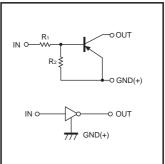


#### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
- alametei	Symbol	DTB723YE DTB723YM	Offic	
Supply voltage	Vcc	-30	V	
Input voltage	Vin	−15 to +5	V	
Collector current *1	IC (max)	-200	mA	
Power dissipation *2	Po	150	mW	
Junction temperature	Tj	150	င	
Storage temperature	Tstg	-55 to +150	ာ	

<sup>\*1</sup> Characteristics of built-in transistor.

#### •Inner circuit



 $R_1=2.2k\Omega / R_2=10k\Omega$ 

<sup>\*2</sup> Each terminal mounted on a recommended land.

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## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	-	-0.3	V	Vcc=-5V, Io=-100μA
	VI(on)	-2.5	-	-		Vo=-0.3V, Io=-20mA
Output voltage	Vo(on)	-	-70	-300	mV	Io/I:= -50mA / -2.5mA
Input current	lı	-	_	-3.0	mA	VI= -5V
Output current	IO(off)	-	-	-500	nA	Vcc= -30V, Vi=0V
DC current gain	Gı	140	-	_	_	Vo= -2V, Io=-100mA
Transition frequency *	f⊤	_	260	_	MHz	Vc=-10V, Ie=5mA, f=100MHz
Input resistance	R <sub>1</sub>	1.54	2.2	2.86	kΩ	-
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	3.6	4.5	5.5	_	-

<sup>\*</sup> Characteristics of built-in transistor.

#### •Electrical characteristic curves

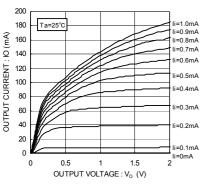


Fig.1 Output Current vs. Output Voltage

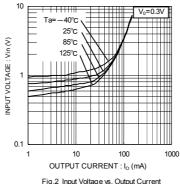


Fig.2 Input Voltage vs. Output Current

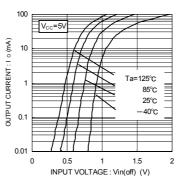


Fig.3 Output Current vs. Input Voltage

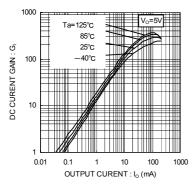


Fig.4 DC Current Gain vs. Output Current

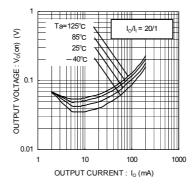


Fig.5 Output Voltage vs. Output Current

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