Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

## 2SK2835

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

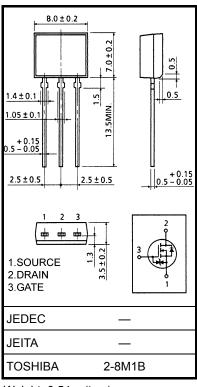
• Low drain-source ON resistance :  $R_{DS (ON)} = 0.56 \Omega (typ.)$ 

High forward transfer admittance : |Y<sub>fS</sub>| = 4.5 S (typ.)
 Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 200 V)

• Enhancement mode :  $V_{th}$  = 1.5 to 3.5 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	200	V	
Drain-gate voltage (Ro	<sub>GS</sub> = 20 kΩ)	$V_{DGR}$	200	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	ΙD	5	А	
	Pulse (Note 1)	I <sub>DP</sub>	20		
Drain power dissipation	n	$P_{D}$	1.3	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	65	mJ	
Avalanche current		I <sub>AR</sub>	5	Α	
Repetitive avalanche e	energy (Note 3)	E <sub>AR</sub>	0.13	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.54 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	96.1	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 50 V,  $T_{ch}$  = 25°C (initial), L = 4.2 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

#### **Electrical Characteristics (Ta = 25°C)**

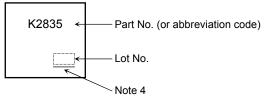
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cut-off cur	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V	-	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	200	_	_	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.5	V
Drain-source Ol	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.5 A	-	0.56	0.8	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.5 A	2.0	4.5	_	S
Input capacitano	е	C <sub>iss</sub>			440	_	pF
Reverse transfer	Reverse transfer capacitance $C_{rss}$ $V_{DS} = 10 \text{ V}, \text{ V}$		V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	35	_	
Output capacitance		C <sub>oss</sub>	1		120	_	
Switching time	Rise time	t <sub>r</sub>	$V_{\rm GS}$ $\stackrel{10{ m V}}{\stackrel{1}{ m OV}}$ $\stackrel{I_{\rm D}=2.5{ m A}}{\stackrel{1}{ m E}}$ $\stackrel{{ m V}}{\stackrel{{ m CO}}{ m E}}$ $\stackrel{{ m R}_{\rm L}=40{ m \Omega}}{\stackrel{{ m V}}{ m DD}}$	_	15	_	- ns
	Turn-on time	t <sub>on</sub>		_	20	_	
	Fall time	t <sub>f</sub>		_	15	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10\mu s$	_	60	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	10	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		6	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>		_	4	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	-	_	_	20	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	_	_	-2.0	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 5 \text{ A}, V_{GS} = 0 \text{ V}, dI_{DR} / dt = 100 \text{ A} / \mu \text{s}$		150		ns
Reverse recovery charge	Q <sub>rr</sub>			0.45		μC

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#### Marking

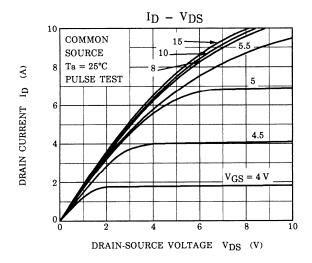


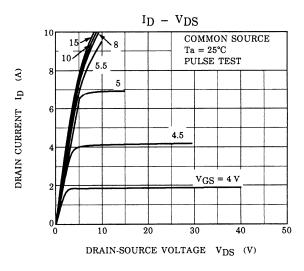
Note 4: A line under a Lot No. identifies the indication of product Labels.

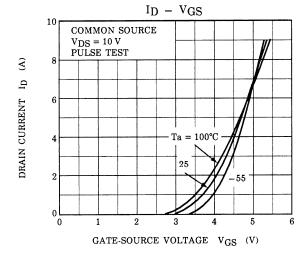
Not underlined: [[Pb]]/INCLUDES > MCV

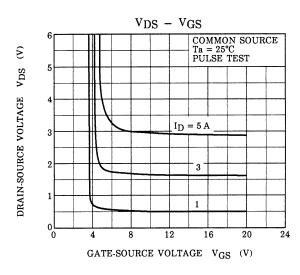
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

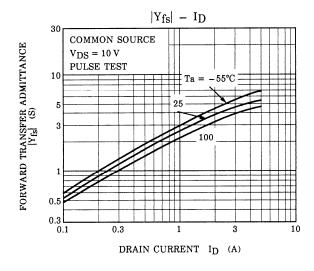
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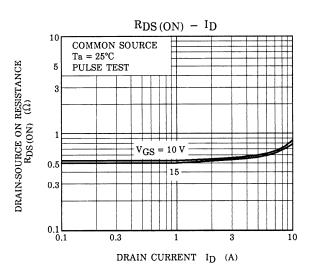




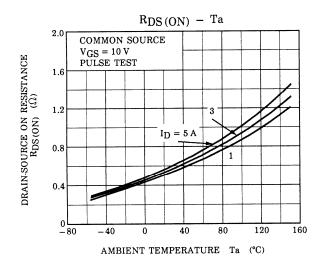


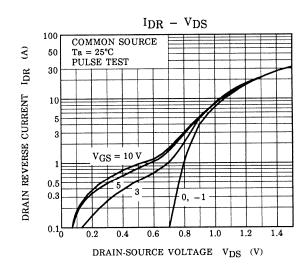


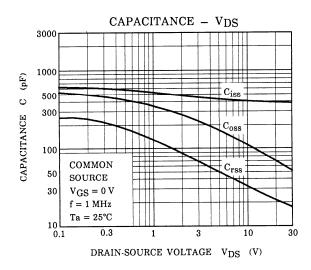


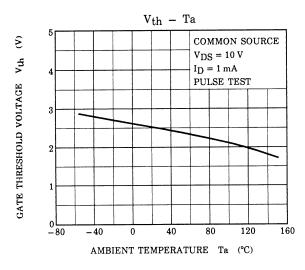


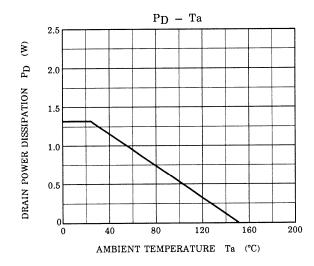
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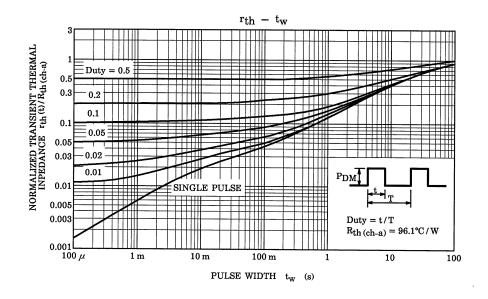


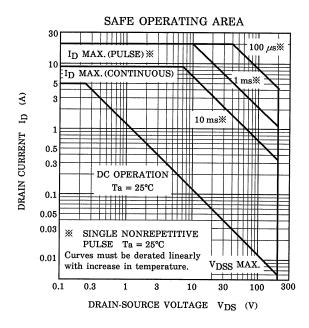


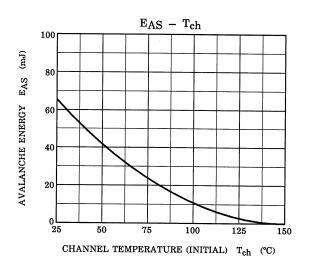


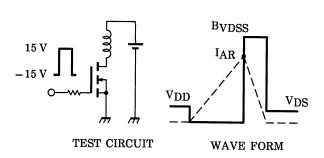












$$R_G$$
 = 25  $\Omega$   
 $V_{DD}$  = 25 V, L = 4.2 mH

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$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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