TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOSV)

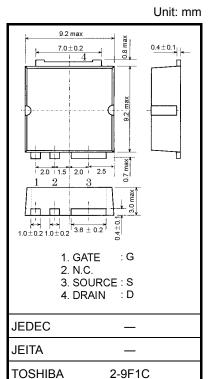
# 2SK3438

# DC-DC Converter, Relay Drive and Motor Drive Applications

- Low drain-source ON-resistance:  $R_{DS(ON)} = 0.74 \Omega$  (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 4.5 S (typ.)
- Low leakage current:  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 600 \text{ V)}$
- Enhancement mode:  $V_{th} = 3.0$  to 5.0 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	600	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	600	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	10		
	Pulse (Note 1)	I <sub>DP</sub>	30	Α	
Drain power dissipation	(Tc = 25°C)	PD	80	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	252	mJ	
Avalanche current		I <sub>AR</sub>	10	Α	
Repetitive avalanche en	E <sub>AR</sub>	8	mJ		
Channel temperature	_	T <sub>ch</sub>	150	°C	
Storage temperature rar	ge	T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

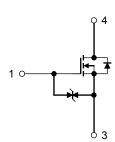
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.56	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90~V,~T_{ch} = 25^{\circ}C$  (initial), L = 4.41 mH, RG = 25 $\Omega$ , IAR = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



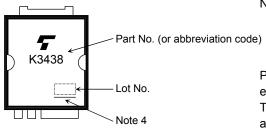
## **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Gate-source breakdown voltage		V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_	_	V
Gate threshold voltage		V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	3.0	_	5.0	V
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	_	0.74	1.0	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5 A		4.5	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	1200	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	10	_	
Output capacitance		Coss		_	130	_	
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} & 10 \text{ V} \\ & \text{VGS} \\ & \text{O V} \end{array} \begin{array}{c} & \text{I}_D = 5 \text{ A} \\ & \text{VOUT} \\ & \text{O V} \end{array} \begin{array}{c} & \text{RL} = 60 \Omega \\ & \text{VDD} \approx 300 \text{ V} \end{array}$	_	13	_	
	Turn-on time	t <sub>on</sub>			40		
	Fall time	t <sub>f</sub>			8	_	ns
	Turn-off time	t <sub>off</sub>		_	50	_	
Total gate charge (gate-source plus gate-drain)		Qg			28	_	nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$	_	16	_	
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	12	_	

# **Source-Drain Ratings and Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	$I_{DR}$	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	30	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$	_	1600	_	ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> /dt = 100 A/μs	_	17	_	μС

### Marking

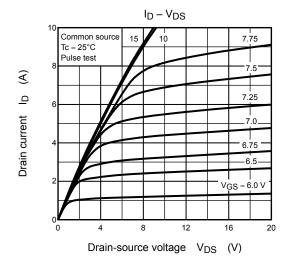


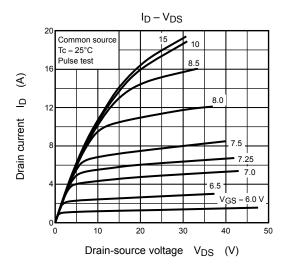
Note 4: A line under a Lot No. identifies the indication of product Labels.

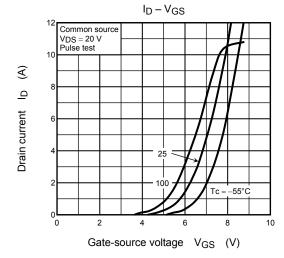
Not underlined: [[Pb]]/INCLUDES > MCV

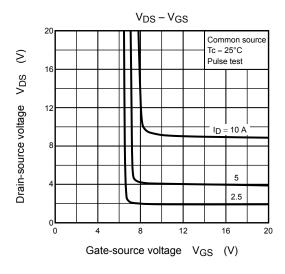
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

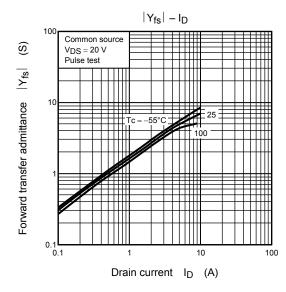
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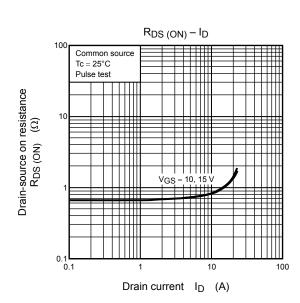




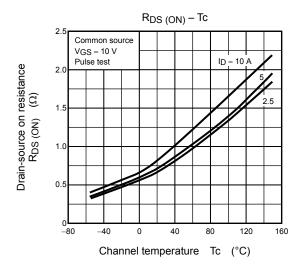


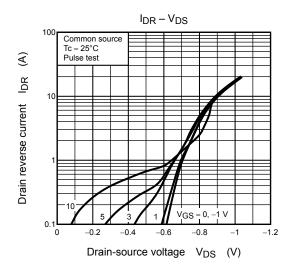


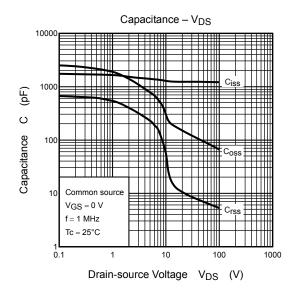


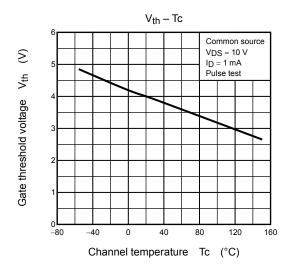


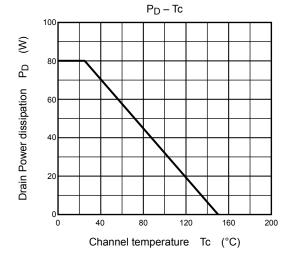
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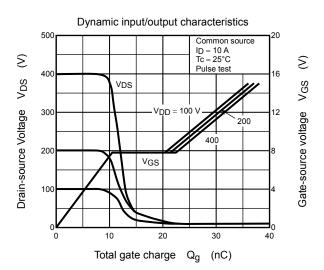


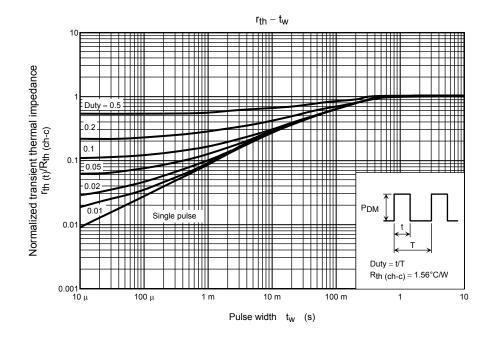


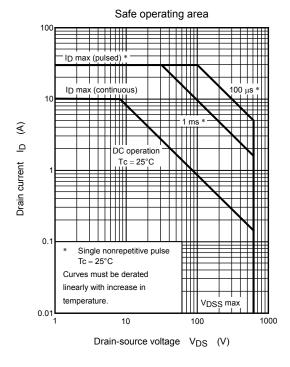


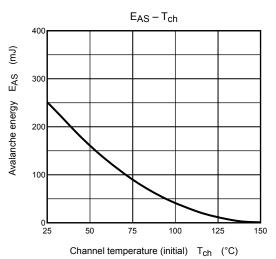


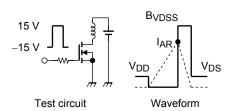












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 4.41~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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