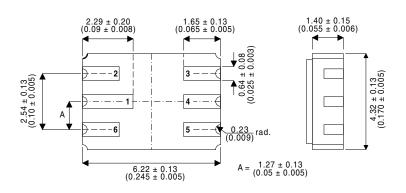
# 2N2894ADCSM



# DUAL HIGH SPEED, MEDIUM POWER PNP GENERAL PURPOSE TRANSISTOR IN A HERMETICALLY SEALED CERAMIC SURFACE MOUNT PACKAGE

#### MECHANICAL DATA Dimensions in mm (inches)



### FEATURES

- SILICON PLANAR EPITAXIAL DUAL PNP TRANSISTOR
- HERMETIC CERAMIC SURFACE MOUNT PACKAGE
- CECC SCREENING OPTIONS AVAILABLE
- SPACE QUALITY LEVELS OPTIONS
- HIGH SPEED SATURATED SWITCHING

#### LCC2 PACKAGE Underside View

PAD 1 - Collector 1	PAD 4 - Collector 2
PAD 2 - Base 1	PAD 5 - Emitter 2
PAD 3 - Base 2	PAD 6 - Emitter 1

# **APPLICATIONS:**

For high reliablitity general purpose applications requiring small size and low weight devices.

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>case</sub> = 25°C unless otherwise stated)		PER SIDE	TOTAL	
V <sub>CBO</sub>	Collector – Base Voltage	-12V		
V <sub>CEO</sub>	Collector – Emitter Voltage	–12V		
V <sub>EBO</sub>	Emitter – Base Voltage	-4.5V		
I <sub>C</sub>	Collector Current	–200mA		
P <sub>D</sub>	Device Dissipation	300mW	500mW	
P <sub>D</sub>	P <sub>D</sub> Derate above 50°C		3.3mW / °C	
R <sub>ja</sub>	Thermal Resistance Junction to Ambient	420°C / W	250°C / W	
т	Max Junction Temperature	200°C		
T <sub>stg</sub>	Storage Temperature	–65 to 200°C		

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise stated)

	Parameter	Test Con	Test Conditions		Тур.	Max.	Unit
V <sub>(BR)CBO*</sub>	Collector – Base Breakdown Voltage	I <sub>C</sub> = 10μA	I <sub>E</sub> = 0	- 12			
V <sub>(BR)CEO</sub>	Collector – Emitter Breakdown Voltage	I <sub>C</sub> = 10mA	I <sub>B</sub> = 0	- 12			V
V <sub>(BR)EBO</sub>	Emitter – Base Breakdown Voltage	I <sub>E</sub> = 100μA	$I_{\rm C} = 0$	- 4.5			-
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = -10V$	$T_{amb} = 125^{\circ}C$			- 10	μA
I <sub>CES</sub>	Collector Cut-off Current	$V_{BE} = 0$	$V_{CE} = -10V$			- 50	nA
V <sub>CE(sat)</sub> Collector – Emitter Saturation V	Collector – Emitter Saturation Voltage	I <sub>C</sub> = -10mA	$I_{B} = -1.0 \text{mA}$			-0.130	v
		I <sub>C</sub> = -30mA	I <sub>B</sub> = –3mA			-0.190	
		$I_{\rm C} = -100 {\rm mA}$	$I_B = -10 mA$			- 0.450	
		I <sub>C</sub> = -10mA	$I_{B} = -1.0 \text{mA}$	-0.78		-0.920	
V <sub>BE(sat)</sub>	Base – Emitter On Voltage	I <sub>C</sub> = -30mA	I <sub>B</sub> = -3mA	-0.85		-1.15	V
		$I_{\rm C} = -100 {\rm mA}$	$I_{B} = -10 \text{mA}$			-1.5	
h <sub>FE</sub> DC Current Gain		I <sub>C</sub> = -10mA	$V_{CE} = -0.3V$	30			
		I <sub>C</sub> = -30mA	$V_{CE} = -0.5V$	40		150	
	DC Current Gain	$I_{\rm C} = -100 {\rm mA}$	$V_{CE} = -1.0V$	30			1
		I <sub>C</sub> = -30mA	$V_{CE} = -0.5V$	00			-
			T <sub>amb</sub> = -55°C	20			
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -10V$	f = 100MHz	800			NAL 1-
		I <sub>C</sub> = –30mA					MHz
C <sub>ebo</sub>	Emitter – Base – Capacitance	$V_{EB} = -5V$	I <sub>C</sub> = 0			6	pF
		f = 140kHz					
C <sub>cbo</sub> Collector – Base – Capacitan	Collector Doog Conseitores	$V_{CB} = -5V$	I <sub>E</sub> = 0			4.5	pF
	Collector – Base – Capacitance	f = 140kHz					
t <sub>on</sub>	Turn on Time	I <sub>C</sub> = –30mA	$V_{CE} = -2V$				
		$I_{B2} = -1.5 mA$				60	ns
t <sub>off</sub>	Turn off Time	I <sub>C</sub> = –30mA	$V_{CE} = -2V$			35	ns
		I <sub>B1</sub> = I <sub>B2</sub> = -1.5	ōmA			33	

\* Pulse Test:  $t_p \leq 300 \mu s, \, \delta \leq 2\%$ .

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