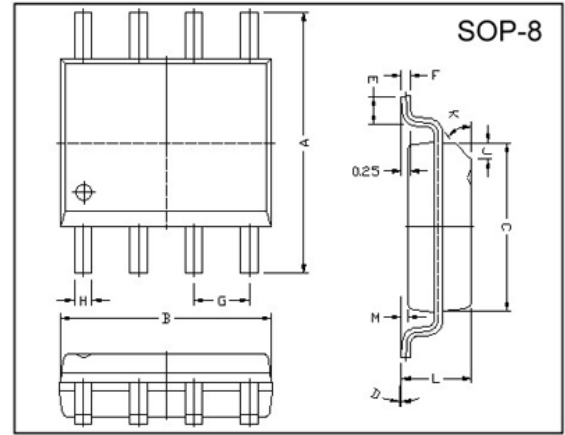


RoHS Compliant Product

Description

The SSC2127 series positive, linear regulators feature low quiescent current (45µA typ.) with low dropout voltage, making them ideal for battery applications. Output voltage are set at the factory and trimmed to 1.5% accuracy. These rugged devices have both Thermal Shutdown and Current Fold-back to prevent device failure under the "Worst" of operating conditions. An additional feature is a "Power Good" detector, which pulls low when the output is out of regulation. The SSC2127 is stable with an output capacitance of 4.7µF or greater.



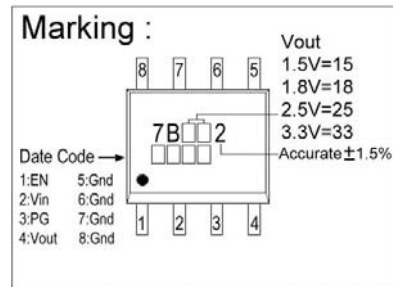
Features

- * Low Temperature Coefficient
- * Over-Temperature Shutdown
- * Power Good Output Function
- * Very Low Dropout Voltage
- * Noise Reduction Bypass Capacitor
- * Short Circuit Current Fold-back
- * Guaranteed 750mA output
- * Current Limiting
- * Power-Saving Shutdown Mode

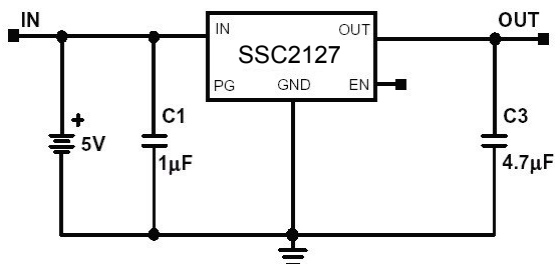
REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	5.80	6.20	M	0.10	0.25
B	4.80	5.00	H	0.35	0.49
C	3.80	4.00	L	1.35	1.75
D	0°	8°	J	0.375 REF.	
E	0.40	0.90	K	45°	
F	0.19	0.25	G	1.27 TYP.	

Applications

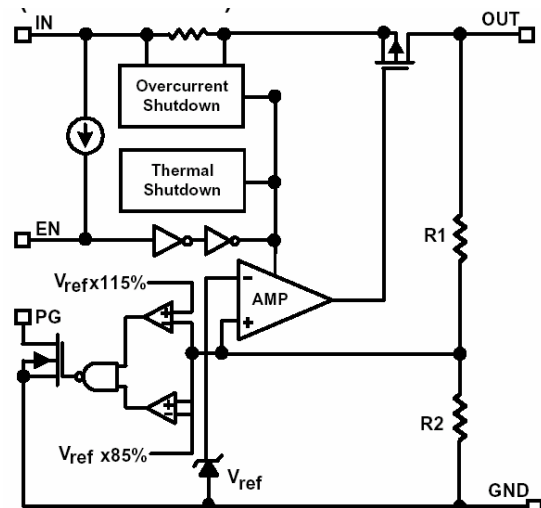
- * PC Peripherals
- * Wireless Devices
- * Portable Electronics
- * Battery Powered Widgets
- * Instrumentation



Typical Application Circuit



Functional Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Input Voltage	V_{IN}	8	V
Output Current	I_{OUT}	$P_D/(V_{IN}-V_O)$	mA
Output Voltage	V_{OUT}	1.5~3.3	V
Operating Ambient Temperature	T_{opr}	-40~+85	°C
Junction Temperature	T_j	-40~+125	°C
Max. Junction Temperature	$T_j \text{ Max.}$	150	°C
Power Dissipation ($\Delta T=100^\circ\text{C}$)	P_D	810	mW
EDS Classification		B	

Electrical Characteristics $T_a=25^\circ\text{C}$ unless otherwise noted ($V_{IN}=V_{OUT}(T)+2V, V_{EN}=V_{IN}, C_{IN}=1\mu\text{F}, C_{OUT}=4.7\mu\text{F}$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition	
Output Voltage	$V_{OUT}(E)^1$	-1.5%	$V_{OUT}(T)^2$	1.5%	V	$V_{IN}=V_{OUT}(T)+2V, I_o=1\text{mA}$	
Output Current	I_o	750	-	-	mA	$V_o>1.2V$	
Current Limit	I_{LIM}	750	-	-	mA	$V_o>1.2V$	
Load Regulation	REG_{LOAD}	-1	0.2	1	%	$V_{IN}=V_{OUT}(T)+2V, I_o=1\text{mA} \sim 750\text{mA}$	
Dropout Voltage	$V_{DROPOUT}$	-	-	1000	mV	$I_o=750\text{mA}$ $V_o=V_{OUT}(E)-2\%$	
		-	-	650			$V_{OUT}(T)=1.5V$
		-	-	500			$V_{OUT}(T)=1.8V$ $V_{OUT}(T) \geq 2.0V$
Quiescent Current	I_q	-	45	70	μA	$V_{IN}=V_{OUT}(T)+2V, I_o=0\text{mA}$	
Line Regulation	REG_{LINE}	-0.15	-	0.15	%	$I_o=1\text{mA}$ $V_{IN}=V_{OUT}(T)+1$ to $V_{OUT}(T)+2$	
		-0.1	0.02	0.1			$V_{OUT}(T)<2.0V$
		-0.4	-	0.4			$2.0V \leq V_{OUT}(T) < 4.0V$ $4.0V \leq V_{OUT}(T)$
Input Voltage	V_{IN}	Note ³	-	7	V		
Over Temperature Shutdown	O_{TS}	-	150	-	°C		
Over Temperature Hysteresis	O_{TH}	-	30	-	°C		
Output Voltage Temperature Coefficient	T_c	-	30	-	ppm/°C		
PG Voltage Low	V_{OL}	-	-	0.4	V	$I_{SINK}=0.25\text{mA}$	
Power Supply Rejection	PSRR	-	75	-	dB	$I_o=100\text{mA}$ $C_o=4.7\mu\text{F}$ (ceramic)	
		-	55	-			$f=1\text{kHz}$
		-	30	-			$f=10\text{kHz}$ $f=100\text{Hz}$
Output Voltage Noise	e_N	-	30	-	μV_{rms}	$f=10\text{Hz} \sim 100\text{kHz}, I_o=10\text{mA}$ $C_o=4.7\mu\text{F}$	
EN Input Threshold	V_{EH}	2	-	V_{IN}	V	$V_{IN}=2.7V$ to $7V$	
	V_{EL}	0	-	0.4			
EN Input Bias Current	I_{EH}	-	-	1	μA	$V_{EN}=V_{IN}, V_{IN}=2.7V$ to $7V$ $V_{EN}=0V, V_{IN}=2.7V$ to $7V$	
	I_{EL}	-	-	1			
Shutdown Supply Current	I_{SD}	-	0.5	2	μA	$V_{IN}=5V, V_o=0V, V_{EN}<V_{EL}$	
Output Under Voltage	V_{UV}	-	-	84	% $V_{OUT}(T)$	PG goes low when V_{OUT} too high	
Output Over Voltage	V_{OV}	105	-	-	% $V_{OUT}(T)$	PG goes low when V_{OUT} too high	
PG Leakage Current	I_{LC}	-	-	1.0	μA	$V_{PG}=7V$	

Note 1: $V_{OUT}(E)$ =Effective Output Voltage (i.e. the output voltage when " $V_{OUT}(T)+2.0V$ " is provided at the V_{IN} pin while maintaining a certain I_{OUT} value).

2: $V_{OUT}(T)$ =Specified Output Voltage

3: $V_{IN}(MIN) = V_{OUT} + V_{DROPOUT}$



Elektronische Bauelemente

SSC2127

750mA CMOS

Positive Voltage Regulator

Ordering Information(contd.)

Part Number	Marking	Output Voltage	Part Number	Marking	Output Voltage
SSC2127-15	7B152 XXXX	1.5V	SSC2127-18	7B182 XXXX	1.8V
SSC2127-25	7B252 XXXX	2.5V	SSC2127-33	7B332 XXXX	3.3V

Detailed Description

The SSC2127 of CMOS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection and thermal shutdown. The P-channel pass transistor receives data from the error amplifier, over-current shutdown, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 140°C, or the current exceeds 2.2A. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120°C. The SSC2127 behaves like a current source when the load reaches 2.2A. However, if the load impedance drops below 0.3 Ω, the current drops back to 600mA to prevent excessive power dissipation. Normal operation is restored when the load resistance exceeds of 0.75 Ω.

External Capacitors

The SSC2127 is stable with an output capacitance to ground of 4.7uF or greater. Ceramic capacitors have the lowest ESR, and will offer the best AC performance. Conversely, Aluminum Electrolytic capacitors exhibit the highest ESR, resulting in the poorest AC response. Unfortunately, large value ceramic capacitors are comparatively expensive. One option is to parallel a 0.1uF ceramic capacitor with a 10uF Aluminum Electrolytic. The benefit is low ESR, high capacitance, and low overall cost. A second capacitor is recommended between the input and ground to stabilize V_{IN}. The input capacitor should be at least 0.1uF to have a beneficial effect. All capacitors should be placed in closed proximity to the pins. A "Quiet" ground termination is desirable. This can be achieved with a "Star" connection.

Enable

When EN pin is pulled low, the PMOS pass transistor shuts off, and all internal circuits are powered down. In this state, the quiescent current is less than 2uA. This pin behaves much like an electronic switch. 100KΩ resistor is necessary between V_{EN} source and EN pin when V_{EN} is high than V_{IN}. (Note: There is no internal pull-up for EN pin. It can not be floating.)

Power Good

The SSC2127 includes the Power Good feature. When the output is not within ± 15% of the specified voltage, it pulls low. This can occur under the following conditions:

1. Input voltage too low.
 2. During over-temperature.
 3. During over-current.
 4. If output is pulled up.
- (Note: PG pin is an open-drain output.)

Characteristics Curve

