



# **TSic™- 106**

## Rapid Response, Low-Cost Temperature Sensor IC

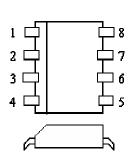
Feature Sheet

#### **Features**

- Low cost, precision temperature sensor
- Single-wire 11-bit digital serial signal output compatible with state-of-the-art µP controllers
- Communication range > 10 meters
- Resolution: better than 0.1°C
- Accuracy: ±0.5°C at room temperature;
  ±1.0°C over span of 40°C
- Wide measurement range: −50 to +150 °C
- Signal read-out every 0.1s (other rates available on request)
- V+ supply voltage: 2.97 to 5.5V (industry standard); 3.3V or 5V (±10%) power supplies
- Package: 8-pin SOIC
- Low quiescent current: <80µA at 25°C with 3.3V minimizes self-heating errors for applications such as wall-mounted thermostats
- System-on-a-chip based on advanced mixedsignal technology integrating precision temperature sensing bandgap reference with proportionalto-absolute-temperature (PTAT) output, digital signal processor (DSP) core, and electrically erasable memory (EEPROM)

### **Package Information**

TSic<sup>TM</sup> 106 SOP8: 150mil, Standard SMT Package, SOIC, Based on IEC 191-2Q, Type 076E35 B. Other packages available on demand: TSic<sup>TM</sup> 106 e-line; 3 pin THT package; TSic<sup>TM</sup> 106 bare die or wafer level.



Pin	Name	Description
1	V+	Supply voltage (3.0-5.5V)
2	Signal	Temperature output signal
4	Gnd	Ground
3, 5-8	TP/NC	Test pin / NC Do not connect

#### **Brief Description**

The TSic<sup>™</sup> temperature sensor IC family are fully tested and calibrated sensors with absolute measurement accuracy on delivery – no further calibration needed. The TSic<sup>™</sup> combines outstanding accuracy with long term stability, yet it is very simple to use.

The TSic<sup>™</sup> series is specifically designed for high performance, cost-effective solutions for sensing temperature in building automation, automotive, industrial, office automation, white goods and low-power/mobile applications.

TSic<sup>TM</sup> employs a high precision bandgap reference with PTAT output; a low-power, precision ADC; and an on-chip DSP core with EEPROM to precisely calibrate the output temperature signal. The TSic<sup>TM</sup> series includes ICs with two linear analog signal output options, such as standard 0~1Vout (V+ = 2.97V to 5.5V) or ratiometric (10~90% of V+; i.e., 0.5~4.5Vout @ V+ = 5V) or the digital serial output signal for interfacing with  $\mu$ P controllers.

#### **Benefits**

- Several accuracy classes available with 100% upward compatibility
- No calibration by customer needed; absolute calibration specified
- Simple to integrate, reducing cost and time for application-development
- Fast data measurement optimal for temperature control
- Packages for standard SMD, THT or application specific assembly
- Miniaturized solutions with Bare-Chip (COB, COF, CSP\*) or e-line packages – very fast response time for COF
- Very low power consumption ideal for mobile and standard applications
- Field reconfiguration/recalibration option available (high volume customers only)
- Outstanding long term stability

<sup>\*</sup> COB: Chip-On-Board; COF: Chip-On-Flex; CSP: Chip Scale Packaging





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### **Absolute Maximum Ratings**

PARAMETER	MIN	TYP	MAX	UNITS
Supply Voltage (V+)	-0.3		6.0	V
Voltages at Analog I/O Pins (V <sub>INA</sub> , V <sub>OUTA</sub> )	-0.3		V <sub>DDA</sub> + 0.3	V
Storage Temperature Range (T <sub>stor</sub> )	-50		150	°C

### **Operating Conditions**

PARAMETER	MIN	TYP	MAX	UNITS
Supply Voltage to Gnd (V+) <sup>1</sup>	2.97	5.0	5.5	V
Supply Current (I <sub>V+</sub> ) @ V+ = 3.3V, RT	30	45	80	μΑ
Ambient Temperature Range $\left(T_{amb}\right)^2$	-50		150	°C
Output Load Capacitance (C <sub>L</sub> ) <sup>3</sup>		10	15	nF
External Capacitance Between V+ and Gnd (C <sub>V+</sub> ) <sup>4</sup>	80	100	470	nF
Output Load Resistance (R <sub>L</sub> ) Signal to Gnd (or V+) <sup>5</sup>	2.5	10		ΚΩ

- 1 With supply voltage 2.7V 2.97V, accuracy is slightly reduced; below 2.7V, functionality is unknown.
- 2 Output signal is limited to this ambient temperature (applies to calibration, offset and gain).
- 3 When using the output as a digital output, the load capacitor  $C_l$  is limited by maximum rise time for ZACwire<sup>TM</sup>
- 4 Locate as close as possible to TSic's V+ and Gnd pins.
- 5 When using the output as a digital output, no pull-down resistor is allowed.

## **Temperature Accuracy**

PARAMETER	MIN	TYP	MAX	UNITS
Wide Range Device for -50° to 150°C				
At room temperature	-0.5	±0.3	0.5	°C 1
+0°C to +40°C	-1.0		+1.0	°C 1
-50 to 0, +40 to 150°C		+1.5		°Ç

<sup>&</sup>lt;sup>1</sup> 2s value, plus quantization error of 1 bit (0.1°C)

# Output Examples for TSic<sup>™</sup> Devices

		Temperature Measurement Range -50°C to 150°C or -58°F to 302°F (Wide Range Device)			
		TSic-101	TSic-106		
Temp (°C)	Temp (°F)	Analog 0~1V	Analog ratiometric 10~90%	Digital <sup>1</sup>	
-50	-58	0.000	10	0x000	
-10	14	0.200	26	0x199	
0	32	0.250	30	0x200	
25	77	0.375	40	0x2FF	
60	140	0.550	54	0x465	
125	257	0.875	80	0x6FE	
150	302	1.000	90	0x7FF	

<sup>&</sup>lt;sup>1</sup>Temperature = (Digital Signal / 2047 \* 200 - 50)°C



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