Ordering number : ENN7718



## SANYO Semiconductors DATA SHEET

P-Channel Silicon MOSFET

# **VEC2305** — General-Purpose Switching Device **Applications**

#### **Features**

- · For load switches, DC / DC converters.
- · 1.8V drive.
- · Composite type with 2 P-Channel MOSFETs (MCH3319) contained in a singlepackage, facilitaing high-density mounting.

#### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-12	V
Gate-to-Source Voltage	VGSS		±8	V
Drain Current (DC)	ID		-2.5	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-10	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm <sup>2</sup> X0.8mm)1unit	0.9	W
Total Dissipation	PT	Mounted on a ceramic board (900mm <sup>2</sup> X0.8mm)	1.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Farameter			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-12			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-12V, V <sub>GS</sub> =0			-10	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±6.4V, V <sub>DS</sub> =0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-6V, I <sub>D</sub> =-1mA	-0.3		-1.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-6V, I <sub>D</sub> =-1.5A	2.7	4.5		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =-1.5A, V <sub>G</sub> S=-4.5V		87	115	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-0.8A, V <sub>G</sub> S=-2.5V		122	172	mΩ
	RDS(on)3	ID=-0.4A, VGS=-1.8V		162	275	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =-6V, f=1MHz		450		pF
Output Capacitance	Coss	V <sub>DS</sub> =-6V, f=1MHz		100		pF
Reverse Transfer Capacitance	Crss	VDS=-6V, f=1MHz		85		pF

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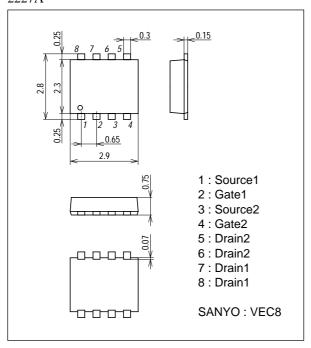
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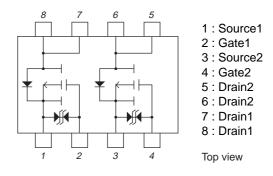
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		15		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		75		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		64		ns
Fall Time	tf	See specified Test Circuit		50		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-6V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.5A		6.5		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =-6V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.5A		0.8		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-6V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.5A		2.0		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-2.5A, V <sub>GS</sub> =0		-0.85	-1.5	٧

#### **Package Dimensions**

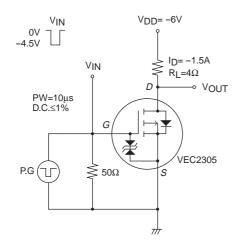
unit : mm 2227A



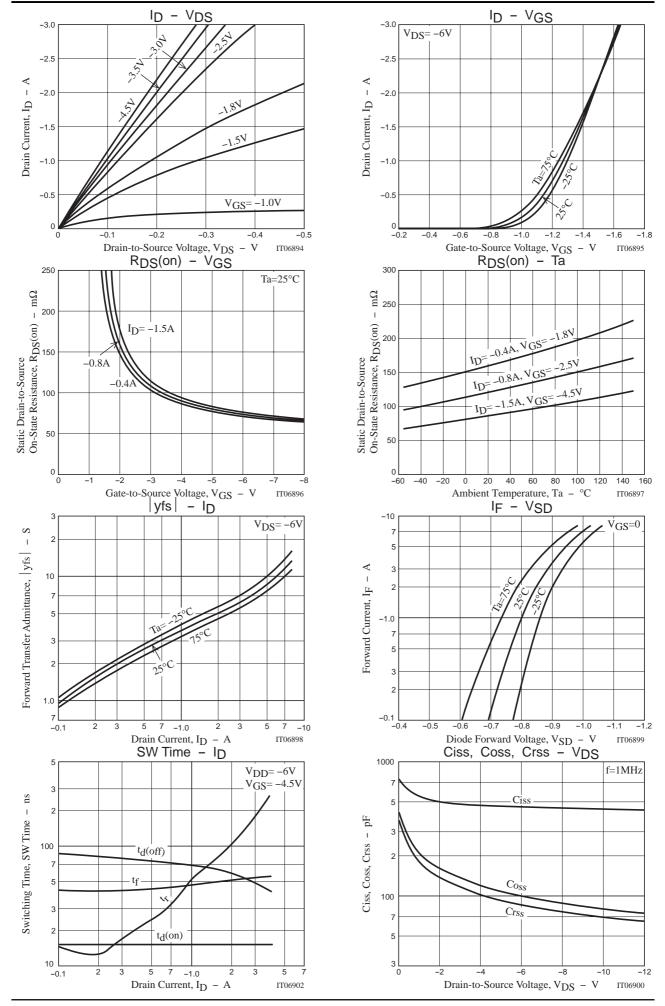
#### **Electrical Connection**



### **Switching Time Test Circuit**



#### **VEC2305**



0.4

0.2

0

20

60

80

Ambient Temperature, Ta - °C

100

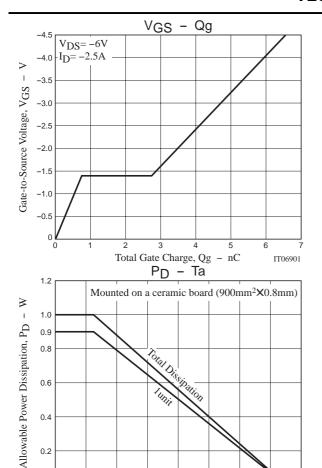
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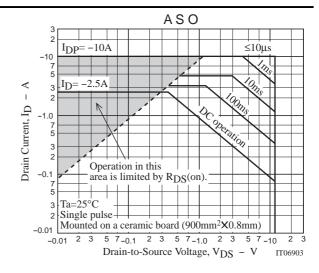
140

160

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#### **VEC2305**





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