晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR
LCD   MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	**AM-320240NSTNQW-00H**
APPROVED BY	
DATE	

☐ **Approved For Specifications**
☑ **Approved For Specifications & Sample**

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2007/7/13	-	New Release	Sunglin
2007/7/18	9	Modify LCD Viewing Angle	Edward
2007/12/6		Modify Optical characteristic of the LCD	Sunglin
2008/4/1	21	Add guarantee declaration	Kasha
2008/6/17	4&12	Remove the CCFL statement & modify Pin30,31 define.	Sunglin
2009/11/26	3	Modify PCB for improvement DCLK signal	Edward
	22-23	Update Mechanical drawing with new logo	Edward
2009/11/27	5	Update LED Life time	Edward

Date : 2009/11/26 　　　AMPIRE CO., LTD. 　　　 2

1 Features

5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 5.7" TFT-LCD panel, a driver circuit and backlight unit.

(1) Construction: 5.7" a-Si color TFT-LCD, White LED Backlight and PCB.

(2) Resolution (pixel): 320(R.G.B) X240

(3) Number of the Colors : 262K colors (R , G , B 6 bit digital each)

(4) LCD type : Transmissive Color TFT LCD (normally White)

(5) Interface: 33 pin

(6) Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.

(7) Viewing Direction: 12 O'clock (The direction it's hard to be discolored)

(8) Improve DCLK signal.

2 Physical specifications

Item	Specifications	Unit
Display resolution(dot)	960 (W) x 240(H)	mm
Active area	115.2 (W) x 86.4 (H)	mm
Screen size	5.7(Diagonal)	mm
Pixel size	120 (W) x 360 (H)	um
Color configuration	R.G.B stripe	
Overall dimension	144.0(W)x104.6(H)x13.0(D)	mm
Weight	T.B.D	mg
Backlight unit	LED	

3 Electrical specification

3.1 Absolute max. ratings

3.1.1 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VCC	VSS=0	-0.3	6.0	V	
Input voltege	V_{in}		-0.3	VCC+0.3	V	Note 1

Note1:Hsync, Vsync, ENAB, CK, R0~R5, G0~G5, B0~B5

3.1.2 Environmental Absolute max. ratings

Item	OPERATING		STORAGE		Remark
	MIN	MAX	MIN	MAX	
Temperature	-20	70	-30	80	Note2,3,4,5,7
Humidity	Note1		Note1		
Corrosive Gas	Not Acceptable		Not Acceptable		

Note1 : Ta <= 40℃ : 85% RH max

Ta > 40℃ : Absolute humidity must be lower than the humidity of

85%RH at 40℃

Note2 : For storage condition Ta at -30℃ < 48h , at 80℃ < 100h

For operating condition Ta at -20℃ < 100h

Note3 : Background color changes slightly depending on ambient
temperature. This phenomenon is reversible.

Note4 : The response time will be slower at low temperature.

Note5 : Only operation is guarantied at operating temperature. Contrast ,
response time, another display quality are evaluated at +25℃

3.2 Electrical characteristics

3.2.1 DC Electrical characteristic of the LCD

Typical operating conditions (VSS=0V)

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply		VCC	3.0	3.3	3.6	V	
Input Voltage for logic	H Level	V_{IH}	0.7 VCC	-	VCC	V	Note 1
	L Level	V_{IL}	0	-	0.3 VCC	V	
Power Supply current		ICC		45	55	mA	Note 2

Note1: Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

Note2: fv =60Hz , Ta=25℃ , Display pattern : All Black

#### 3.2.2 Electrical characteristic of LED Back-light

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
LED voltage	V_{AK}	--	10.5	12	V	I_{LED} =330mA,Ta=25℃
LED forward current	I_{LED}	--	330	360	mA	Ta=25℃
	I_{LED}	--	210	240	mA	Ta=60℃
Lamp life time		--	50,000	--	Hr	I_{LED} =330mA,Ta=25℃

■ The constant current source is needed for white LED back-light driving.

When LCM is operated over 60℃ ambient temperature, the I_{LED} of the LED

back-light should be adjusted to 105mA max
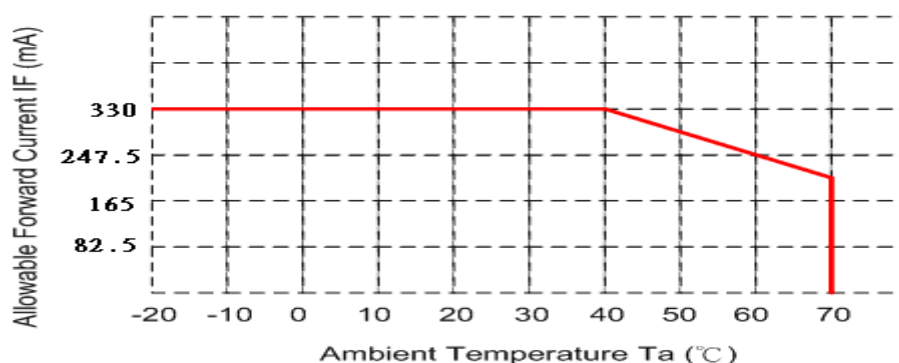

3.3 AC Timing characteristic of the LCD

a. Timing condition

Signal	Parameter		Symbol	Min.	Typ.	Max.	Unit.	Remark
DCLK	DCLK period		T$_{OSC}$	-	156	-	ns	
	Frequency		F$_{OSC}$	-	6.4	-	MHz	
	DCLK High plus width		T$_{CH}$	-	78	-	ns	
	DCLK Low plus width		T$_{CL}$	-	78	-	ns	
RGB DATA	Data setup time		T$_{SU}$	12	-	-	ns	
	Data hold time		T$_{HD}$	12	-	-	ns	
Hsync	Hsync period		T$_H$	-	408	-	T$_{OSC}$	
	Hsync pulse width		T$_{HS}$	5	30	-	T$_{OSC}$	
	Back-Parch		T$_{HB}$		38		T$_{OSC}$	
	Front-Parch		T$_{HF}$		20		T$_{OSC}$	
	Hsync rising time		T$_{Cr}$	-	-	700	ns	
	Hsync falling time		T$_{Cf}$	-	-	300	ns	
Vsync	Vsync period	NTSC		-	262.5	-	T$_H$	
		PAL		-	312.5	-	T$_H$	
	Vsync pulse width		T$_{VS}$	1	3	5	T$_H$	
	Back-Porch	NTSC	T$_{VB}$		15		T$_H$	
		PAL			23		T$_H$	
	Display Period		T$_{VD}$		240		T$_H$	
	Front Porch	NTSC	T$_{VF}$		4.5		T$_H$	
		PAL			46.5		T$_H$	
	Vsync rising time		T$_{Vr}$	-	-	700	ns	
	Vsync falling time		T$_{Vf}$	-	-	1.5	µs	
	Vsync falling to Hsync rising time for odd field		T$_{HVO}$	1	-	-	T$_{OSC}$	
	Vsync falling to Hsync falling time for even field		T$_{HVE}$	1	-	-	T$_{OSC}$	
ENAB	Vsync-DEN time	NTSC	T$_{VSE}$	-	18	-	T$_H$	
		PAL	T$_{VSE}$	-	26	-	T$_H$	
	Hsync-DEN time		T$_{HE}$	36	68	88	T$_{OSC}$	
	DEN plus width		T$_{EP}$	-	320	-	T$_{OSC}$	

Note : If ENAB is fixed to low, the SYNC mode is used. Otherwise DE mode is used. When SYNC mode is used, 1st data start from 68th CK after Hsync falling
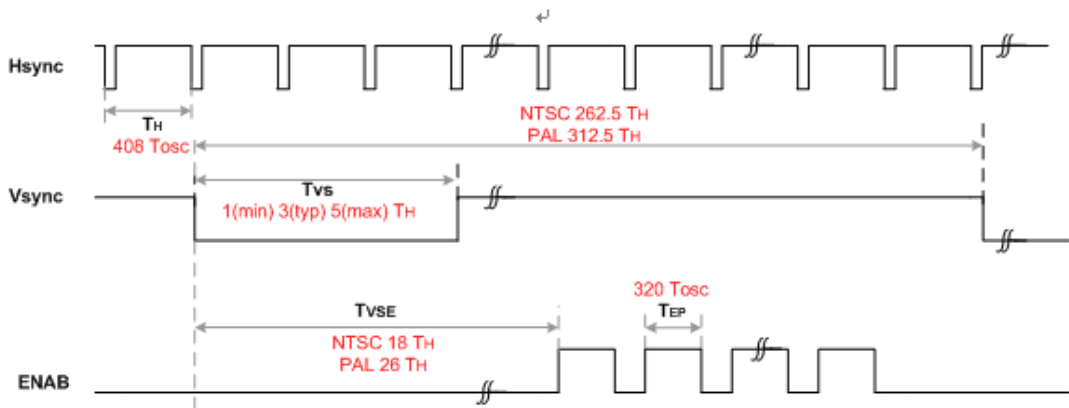
- Horizontal display timing
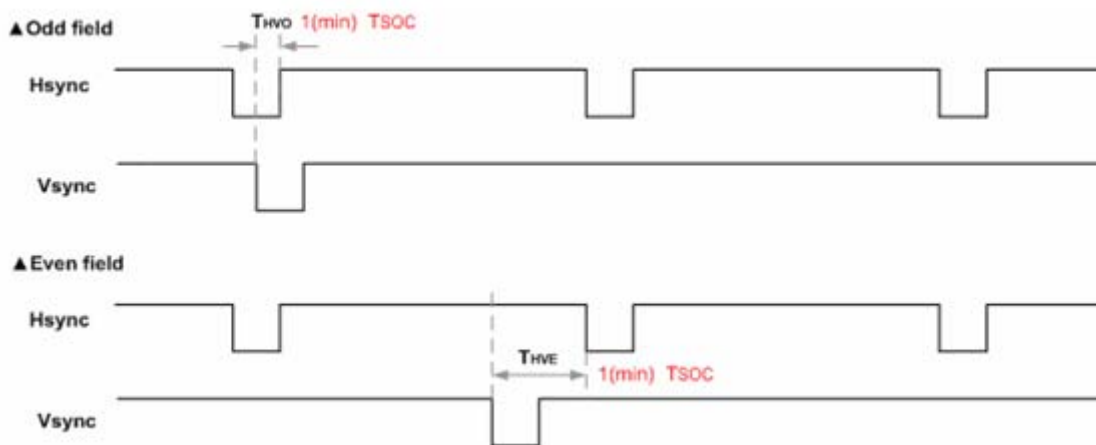
Note: Data is latched rising edge trigger of CLK.
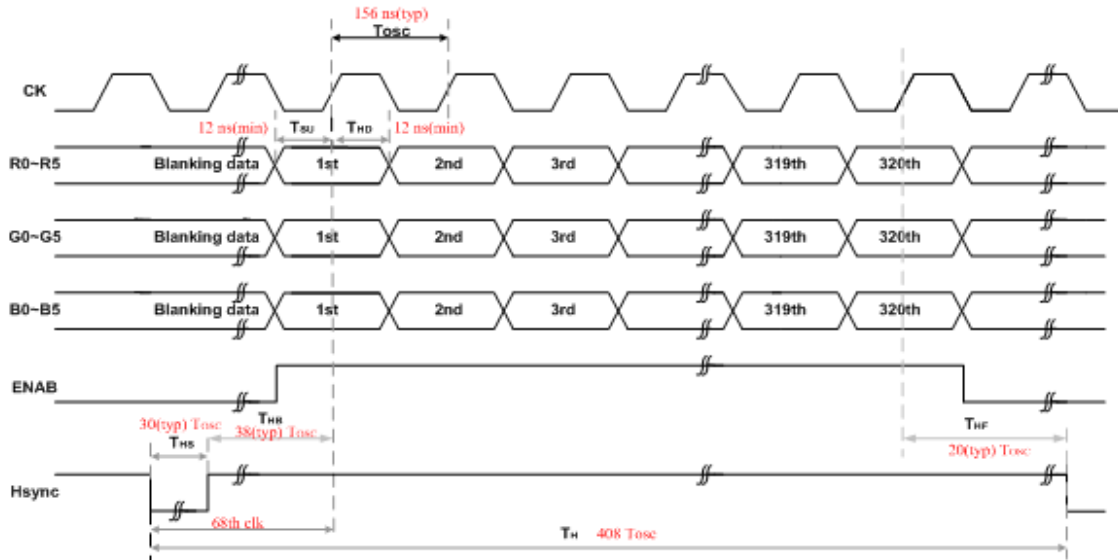
If ENAB is fixed to low, the SYNC mode is used. Otherwise DE mode is used.
When SYNC mode is used, 1st data start from 68th CK after Hsync falling

- Vertical display timing

  - Vertical display timing

- Hsync and Vsync timing

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4 Optical specification

4.1 Optical characteristic of the LCD

| Item		Symbol	Conditon	Min.	Typ.	Max.	Unit	Remark
Response Time	Rise	T_r	Θ=0°	-	15	30	ms	Note 1,2,3,5
	Fall	T_f		-	35	50	ms	
Contrast ratio		CR	At optimized viewing angle		350	-		Note 1,2,4,5
Viewing Angle	Top		CR≧10	55	60	-	deg.	Note1,2, 5,6
	Bottom			45	50	-		
	Left			55	60	-		
	Right			55	60	-		
Brightness		Y_L	I_{LED}=330mA, 25℃	--	500	-	cd/m^2	Note 7
Red chromaticity		X_R	Θ=0° Θ=0°	0.582	0.622	0.672		Note 7 For reference only. These data should be update according the prototype.
		Y_R		0.326	0.366	0.406		
Green chromaticity		X_G		0.317	0.357	0.397		
		Y_G		0.520	0.560	0.600		
Blue chromaticity		X_B		0.101	0.141	0.181		
		Y_B		0.055	0.095	0.135		
White chromaticity		X_W		0.277	0.327	0.367		
		Y_W		0.298	0.348	0.388		

( )For reference only. These data should be update according the prototype.

Note 1:Ambient temperature=25℃,and lamp current I_L=6 mArms.To be measured in the dark room.

Note 2:To be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-7,after 10 minutes operation.

Note 3.Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from"white" to "black" (rising time),respectively.The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.
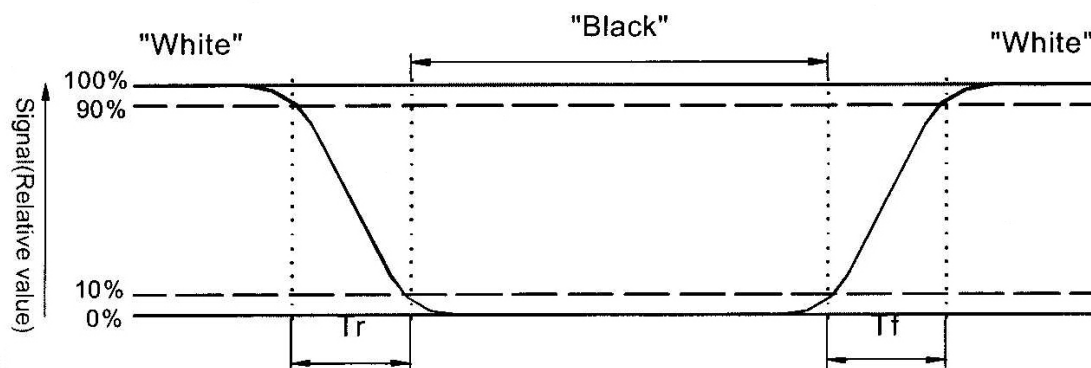
Note 4.Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

Contrast ratio(CR)= $\dfrac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$

Note 5:White $V_i=V_{i50}+1.5V$

Black $V_i=V_{i50}+2.0V$

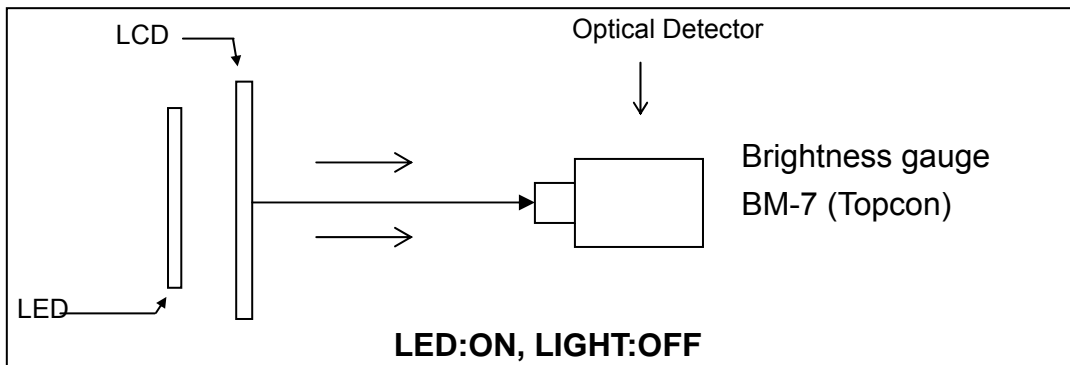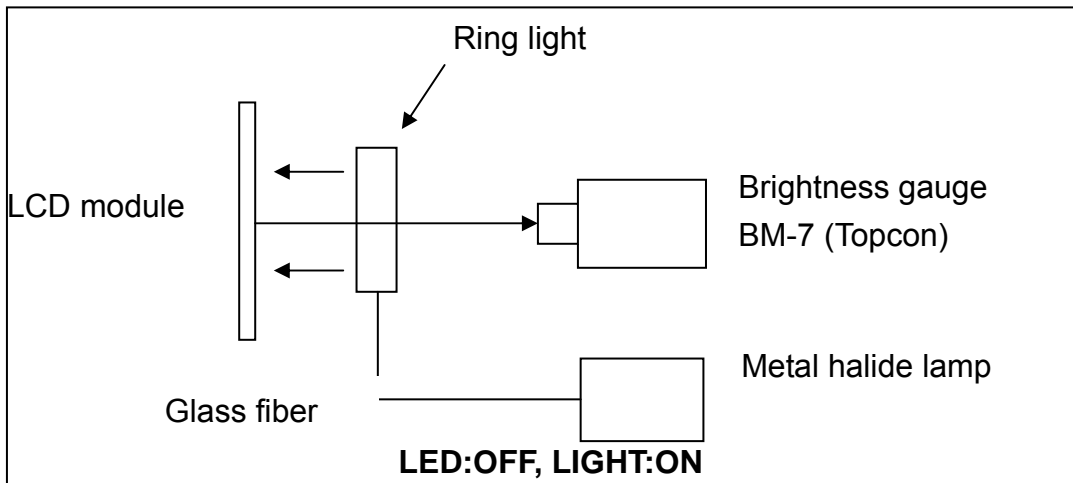"±"means that the analog input signal swings in phase with V_{COM} signal.

"$\mp$" means that the analog input signal swings out of phase with V_{COM} signal.

V_{i50} : The analog input voltage when transmission is 50%.The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note 6.Definition of viewing angle,Refer to figure as below.
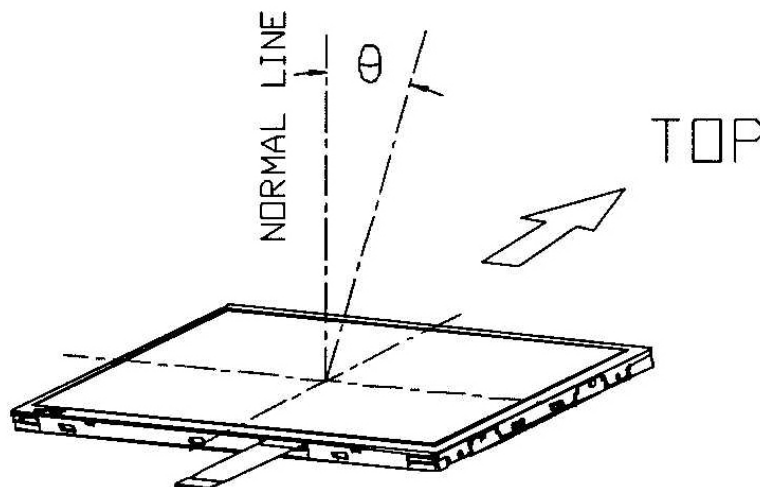
Note 7.Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

LCD module — Ring light

Brightness gauge
BM-7 (Topcon)

Metal halide lamp

Glass fiber

LED:OFF, LIGHT:ON

LCD — Optical Detector

Brightness gauge
BM-7 (Topcon)

LED

LED:ON, LIGHT:OFF

4.2 Optical characteristic of the Back-light

ITEM	MIN	TYP	MAX	UNIT	Condition
Bare Brightness	--	7000	--	Cd/m2	I_{LED} =330mA,Ta=25℃
AVG. X of 1931 C.I.E.	0.30	0.33	0.36	--	I_{LED} =330mA,Ta=25℃
AVG. Y of 1931 C.I.E.	0.31	0.34	0.37	--	I_{LED} =330mA,Ta=25℃
Brightness Uniformity	75	--	--	%	I_{LED} =330mA,Ta=25℃

(   )For reference only. These data should be update according the prototype.

Note1 : Measurement after 10 minutes from LED operating.

Note2 :   Measurement of the following 9 places on the display.
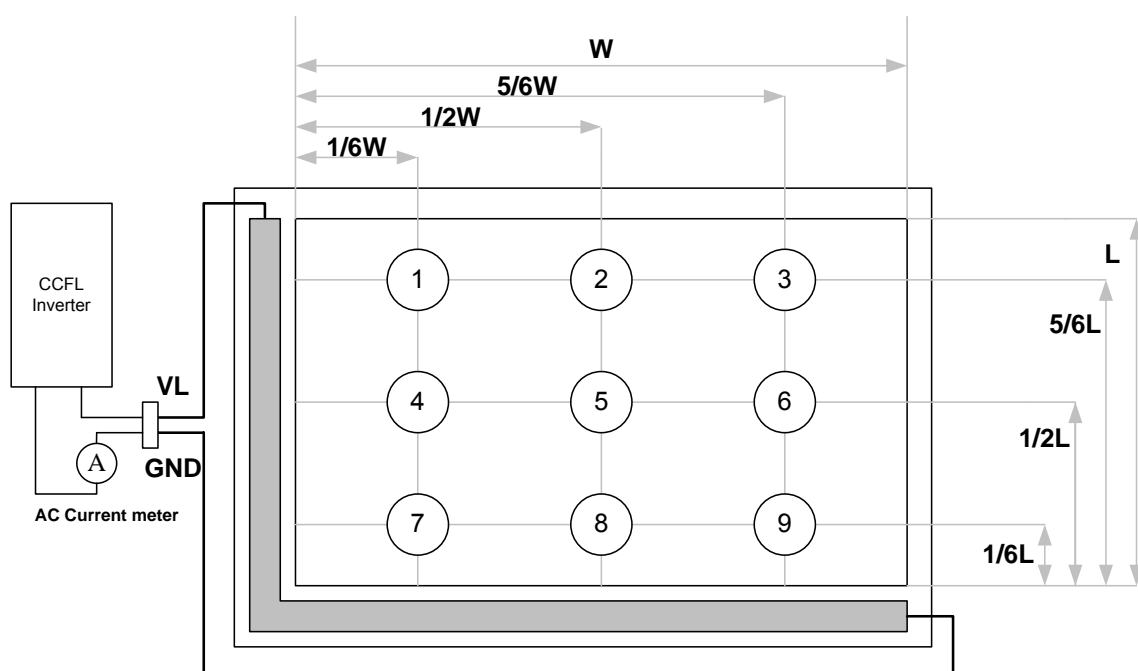

Note3: The Uniformity definition

(Min Brightness / Max Brightness) x 100%

5 Interface specifications

5.1 Driving signals for the TFT panel

Pin no	Symbol	I/O	Description	Remark
1	GND		Gound	
2	CK		Clock signal. Latching data at the rising edge	
3	Hsync		Horizontal sync input in digital RGB mode	
4	Vsync		Vertical sync input in digital RGB mode	
5	GND	I	Gound	
6	R0	I		
7	R1	I		
8	R2	I	Red data	
9	R3	I		
10	R4	I		
11	R5	I		
12	GND	I	Gound	
13	G0	I		
14	G1	I		
15	G2	I	Green data	
16	G3	I		
17	G4	I		
18	G5	I		
19	GND	I	Gound	
20	B0	I		
21	B1	I		
22	B2	I	Blue data	
23	B3			
24	B4	I		
25	B5	I		
26	GND	I	Gound	
27	ENAB	I	Input data enable control	
28	VCC	I	+3.3V Power Supply	
29	VCC	I		
30	NC	I	No connection	
31	NC	I	No connection	
32	NC	I	Not use	
33	GND	I	Gound	

6 BLOCK DIAGRAM
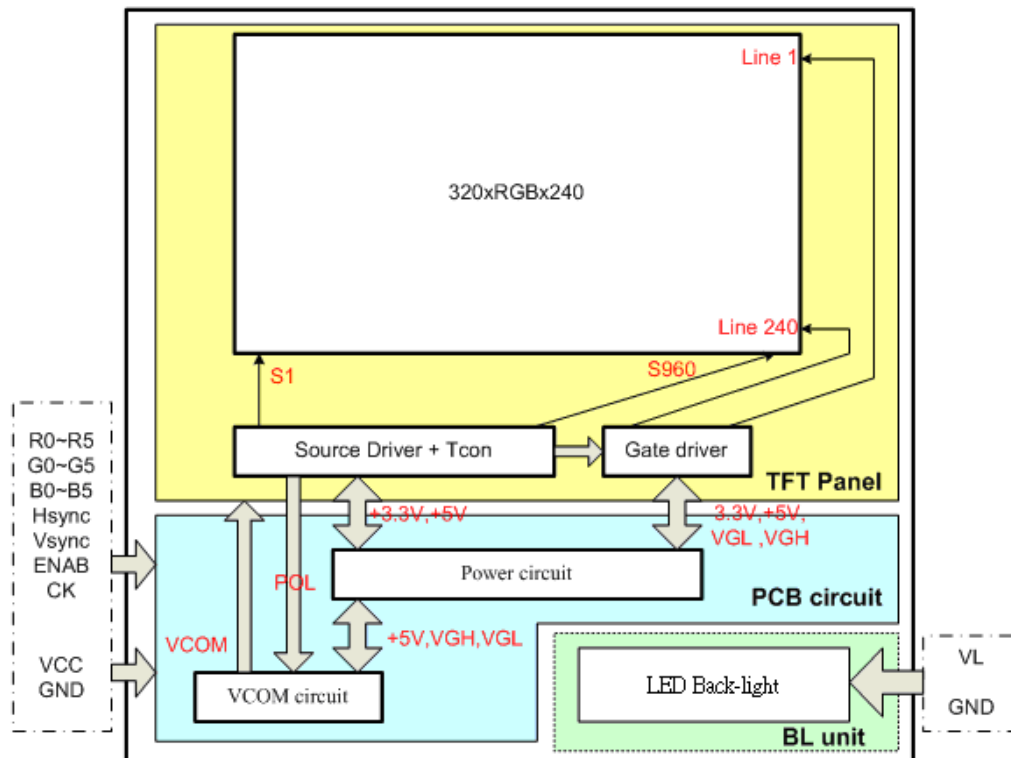
7 DISPLAYED COLOR AND INPUT DATA

	Color & Gray Scale	DATA SIGNAL																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

8 QUALITY AND RELIABILITY

8.1 TEST CONDITIONS

Tests should be conducted under the following conditions :
Ambient temperature : $25 \pm 5°C$
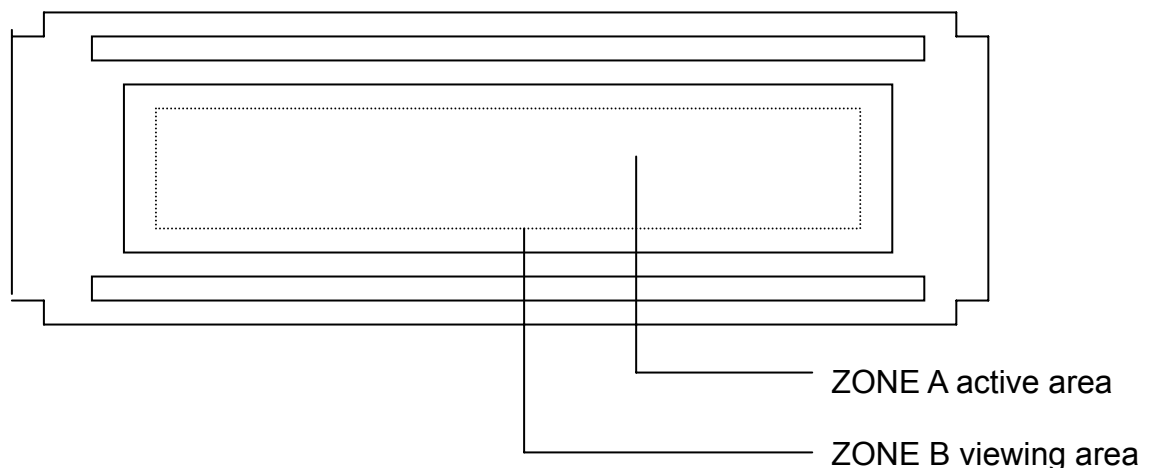Humidity : $60 \pm 25\%$ RH.

8.2 SAMPLING PLAN

Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .
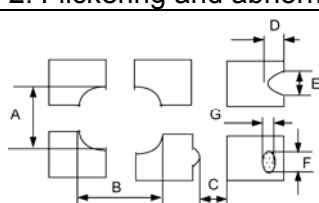
8.3 ACCEPTABLE QUALITY LEVEL

A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

8.4 APPEARANCE
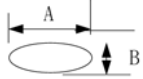
An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under flourescent light. The inspection area of LCD panel shall be within the range of following limits.

ZONE A active area

ZONE B viewing area

8.5 INSPECTION QUALITY CRITERIA

No.	Item	Criterion for defects	Defect type
1	Non display	No non display is allowed	Major
2	Irregular operation	No irregular operation is allowed	Major
3	Short	No short are allowed	Major
4	Open	Any segments or common patterns that don't activate are rejectable.	Major
5	Mura/Spot	ND 3% filter visible is reject. ND 3% filter visible is reject	Major
6	Line defect	<table><tr><td>Inspected Items</td><td>Criteria</td></tr><tr><td>Weak line</td><td>ND 6%see,Reject</td></tr><tr><td>Clear Line</td><td>Reject</td></tr><tr><td>Broken Line</td><td>Reject</td></tr><tr><td>One point one weak line / Sipder missing line</td><td>Reject</td></tr></table>	Major
7	Black/White spot (II)	<table><tr><td>Size D (mm)</td><td>Acceptable number</td></tr><tr><td>D ≤ 0.30</td><td>Ignore</td></tr><tr><td>0.30 < D ≤ 0.50</td><td>5</td></tr><tr><td>0.50 < D ≤ 1.20</td><td>2</td></tr><tr><td>1.20 < D</td><td>0</td></tr></table>	Minor
8	Black/White line (II)	<table><tr><td>Length (mm)</td><td>Width (mm)</td><td>Acceptable number</td></tr><tr><td>20 < L</td><td>0.05 < W ≤ 0.07</td><td>5</td></tr><tr><td>10 < L ≤ 20</td><td>0.07 < W ≤ 0.09</td><td>3</td></tr><tr><td>5.0 < L ≤ 10</td><td>0.09 < W ≤ 0.10</td><td>2</td></tr><tr><td>L ≤ 5.0</td><td>0.10 < W ≤ 0.15</td><td>1</td></tr></table>	Minor
9	Back Light	1. No Lighting is rejectable 2. Flickering and abnormal lighting are rejectable	Major
10	Display pattern	Unit:mm $\dfrac{A+B}{2} \le 0.30$ | 0 < C | $\dfrac{D+E}{2} \le 0.25$ | $\dfrac{F+G}{2} \le 0.25$ Note: 1. Acceptable up to 3 damages 2. NG if there're to two or more pinholes per dot	Minor

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		11.1 Bright defect use ND10% to inspect, if they will be seen, then					
11	Foreign Material Defect.	SPEC		Quantity			Note.
				A area	B area	total	
		Bright line defect	L ≤ 3.0mm	Ignore			W≤0.05mm
			0.3mm< L ≤ 3.0mm	2	4	6	
			L>0.6mm	Reject			
		Bright Spot defect	D ≤ 0.15mm	Ignore			W>0.05mm
			0.15mm< D ≤ 0.3mm	N<3			
			D>0.3mm	Reject			
		Dark line defect	L ≤ 2.0mm	Ignore			W≤0.05mm
			2mm< L ≤ 3.0mm	N<4			
			L>3mm	Reject			
		Dark Spot defect	D ≤0.15mm	Ignore			W>0.05mm
			0.15mm<D ≤0.5mm	N<5			
			L>0.5mm	Reject			

using 11.1 Bright defect use ND 10%to inspect, if they will be seen, then using point defect or line defect to judge again.

11.2 Any two points distance ≥ 5mm

Row 11 judgment: Minor

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12. Scratch on Polarizer — Minor

Width (mm)	Length (mm)	Acceptable number
W≤0.03	Ignore	Ignore
0.03<W≤0.05	L ≤ 2.0	Ignore
	L > 2.0	1
0.05<W≤0.08	L > 1.0	1
	L ≤ 1.0	Ignore
0.08<W	Note (1)	Note(1)

Note(1) Regard as a blemish
Note(2) Distance LCM 30cm, base on visible scratch.

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13. Bubble in polarizer — Minor

Distance LCM 30cm, base on visible.

Size D (mm)	Acceptable number
D ≤ 0.20	Ignore
0.20 < D ≤ 0.50	3
0.50 < D ≤ 0.80	2
0.80 < D	0

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14. Stains on LCD panel surface — Minor

Stains that cannot be removed even when wiped lightly with a soft cloth or similar cleaning too are rejectable.
Distance LCM 30cm, base on visible.

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15. Rust in Bezel — Minor

Rust which is visible in the bezel is rejectable.

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16. Defect of land surface contact (poor soldering) — Minor

Evident crevices which is visible are rejectable.

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17. Parts mounting

1. Failure to mount parts — Major
2. Parts not in the specifications are mounted — Major
3. Polarity, for example, is reversed — Major

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18. Parts alignment

1. LSI, IC lead width is more than 50% beyond pad outline. — Minor
2. Chip component is off center and more than 50% of the leads is off the pad outline. — Minor

19	Conductive foreign matter (Solder ball, Solder chips)	1. 0.45<φ ,N≥1
		2. 0.30<φ≤0.45 ,N≥1
		   φ:Average diameter of solder ball (unit: mm)
		3. 0.50<L ,N≥1
		   L: Average length of solder chip (unit: mm)
			Major
			Minor
			
			Minor

20	Faulty PCB correction	1. Due to PCB copper foil pattern burnout, the pattern is connected, using a jumper wire for repair; 2 or more places are corrected per PCB. 2. Short circuited part is cut, and no resist coating has been performed.	Minor Minor
21	Defect Dot	The TFT panel may have bright dot or Dark dot. The acceptable number defection: <table><tr><td>Bright dot</td><td>Dark dot</td><td>Total dot</td><td>Distance between Dark-- dark</td></tr><tr><td>3</td><td>5</td><td>7</td><td>L≥5 mm</td></tr></table>	Minor

8.6 RELIABILITY

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=96 hrs	
Low Temperature Operation	-20±3°C , t=96 hrs	
High Temperature Storage	80±3°C , t=96 hrs	1,2
Low Temperature Storage	-30±3°C , t=96 hrs	1,2
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 m in. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2
Humidity Test	40 °C, Humidity 90%, 96 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions

(15-35°C , 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

USE PRECAUTIONS

9   HANDLING PRECAUTIONS

(1) An LCD module is a fragile item and should not be subjected to strong mechanical shocks.

(2) Avoid applying pressure to the module surface. This will distort the glass and cause a change in colour.

(3) Under no circumstances should the position of the bezel tabs or their shape be modified.

(4) Do not modify the display PCB in either shape or positioning of components.

(5) Do not modify or move location of the zebra or heat seal connectors.

(6) The device should only be soldered to during interfacing. Modification to other areas of the board should not be carried out.

(7) In the event of LCD breakage and resultant leakage of fluid do not inhale, ingest or make contact with the skin. If contact is made rinse immediately.

(8) When cleaning the module use a soft damp cloth with a mild solvent, such as Isopropyl or Ethyl alcohol. The use of water, ketone or aromatic is not permitted.

(9) Prior to initial power up input signals should not be applied.

(10) Protect the module against static electricity and observe appropriate anti-static precautions.

9.1   Installing precautions

1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.

2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.

3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.

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4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

9.2 Storage precautions

1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.

2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.

3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

9.3 Operating precautions

1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.

2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.

3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.

4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.

5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.

6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.

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7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

9.4　Other

1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.

2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.

3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

10 OUTLINE DIMENSION

Note:
1. Unless indicated, Tolerance Grade "B" is adopted.
2. UV Glue For OLB Protection.
3. Main LCD 320x240 (R.G.B) TFT LCD => 5.7" TFT LCD

TOLERANCE GRADE(±)	A	B	A	B
~6	0.05	0.1		
6~18	0.08	0.18		
18~50	0.1	0.25		
50~180	0.2	0.4		
180~	0.3	0.5		

	DIM.	MM

REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	04-10-07	SNOW
1	Rename TF320240-18-0 to 320240NS-Q & Modify A Block	04-20-07	SNOW

DWN.	SNOW	DATE	04-10-07
CHK.		DATE	
APPD.		DATE	

PARTS NO. LCM
320240NS-Q

晶采光電科技
AMPIRE

TITLE 320240NS-Q (5.7")

DWG. NO. *O7O415MA SHEET 1 OF 1

Back view

CN1 ZIF(Elco) 08-6210-033-340-800A+

CN2 JST BHR-03VS-1
White
Red

VCDC
VCAC

13.0Max.
12.4±0.3
9.7

96.4±0.3
27.2
25.3
24.1±0.3
37.4
30.7±0.3
25.4
36.7±0.3
3.7

15.8
9.0
9.8

56.2±0.5
6.0
136.6±0.3
144.0±0.5
72.0±0.3

35.0±10.0
104.6±0.5
68.5±0.3
49.4±0.3
44.4±0.3
41.4±0.3
16.0
10.0
11.7

4-Φ2.6
Depth 5.0

13.0Max.
12.4±0.3

Revision Record

		DATE	NAME
		04-10-07	SNOW
		04-20-07	SNOW

2	Modify CN2 JST BHR-02VS-1N to BHR-03VS-1	09-23-09	SNOW
1	Rename TF320240-18-0 to 320240NS-Q & Modify A Block		
0	NEW RELEASE		
REV	REVISION RECORD		

CN2

1	K
2	NC
3	A

CN1

1	GND	18	G5
2	CK	19	GND
3	HSYNC	20	B0
4	VSYNC	21	B1
5	GND	22	B2
6	R0	23	B3
7	R1	24	B4
8	R2	25	B5
9	R3	26	GND
10	R4	27	ENAB
11	R5	28	VCC
12	G0	29	VCC
13	G0	30	R/L
14	G1	31	U/D
15	G2	32	NC
16	G3	33	GND
17	G4		

Note:
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50~180	0.2	0.4
180~	0.3	0.5

AMPIRE 晶采光電科技

TITLE: 320240NS-Q (5.7")
DWG. NO. *0704116MB
SHEET 1 OF 2

DWN.	SNOW	DATE	04-10-07
CHK.		DATE	
APPD.		DATE	

DIM. MM
IE NO.
PARTS NO. LCM-1
320240NS-Q