

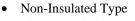
# BCR2AS-14A

## Triac Low Power Use

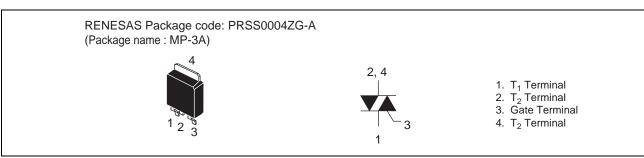
#### Features

- $I_{T (RMS)}$ : 2 A
- V<sub>DRM</sub> : 700 V
- $I_{FGTI}$ ,  $I_{RGTI}$ ,  $I_{RGT}$  : 10 mA

#### Outline



Planar Passivation Type



## Applications

Small motor control, heater control, and other general purpose AC power control applications

#### Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
Falameter	Symbol	14	Onic	
Repetitive peak off-state voltage <sup>Note1</sup>	V <sub>DRM</sub>	700	V	
Non-repetitive peak off-state voltage <sup>Note1</sup>	V <sub>DSM</sub>	840	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I <sub>T (RMS)</sub>	2	A	Commercial frequency, sine full wave 360° conduction
Surge on-state current	I <sub>TSM</sub>	9	A	50Hz sinewave 1 full cycle, peak value, non-repetitive
I <sup>2</sup> t for fusing	l <sup>2</sup> t	0.41	A <sup>2</sup> s	Value corresponding to 1 cycle of half wave 50Hz, surge on-state current
Peak gate power dissipation	P <sub>GM</sub>	1	W	
Average gate power dissipation	P <sub>G (AV)</sub>	0.1	W	
Peak gate voltage	$V_{GM}$	6	V	
Peak gate current	I <sub>GM</sub>	1	А	
Junction temperature	Tj	– 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	—	0.26	g	Typical value

Notes: 1. Gate open.

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## **Electrical Characteristics**

Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state of	current	I <sub>DRM</sub>	_		1.0	mA	Tj = 125°C, V <sub>DRM</sub> applied
On-state voltage		V <sub>TM</sub>	_	_	2.1	V	$Tc = 25^{\circ}C$ , $I_{TM} = 3A$ , instantaneous measurement
Gate trigger voltage <sup>Note2</sup>	Ι	$V_{FGTI}$	_		2.0	V	$Tj = 25^{\circ}C, V_{D} = 6 V, R_{L} = 6 \Omega,$
	II	V <sub>RGTI</sub>	_	_	2.0	V	R <sub>G</sub> = 330 Ω
	III	V <sub>RGTIII</sub>	_		2.0	V	
Gate trigger curent <sup>Note2</sup>	Ι	I <sub>FGTI</sub>	_		10	mA	$Tj = 25^{\circ}C, V_D = 6 V, R_L = 6 \Omega,$
	II	I <sub>RGTI</sub>	_	_	10	mA	R <sub>G</sub> = 330 Ω
	III	I <sub>RGTIII</sub>	—	—	10	mA	
Gate non-trigger voltage		$V_{GD}$	0.2			V	$Tj = 125^{\circ}C, V_D = 1/2 V_{DRM}$
Thermal resistance		R <sub>th (j-c)</sub>	_	_	4.0	°C/W	Junction to case <sup>Note3</sup>
Critical-rate of rise of off-s commutation voltage <sup>Note4</sup>	tate	(dv/dt)c	0.5	—	—	V/µs	Tj = 125°C

Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

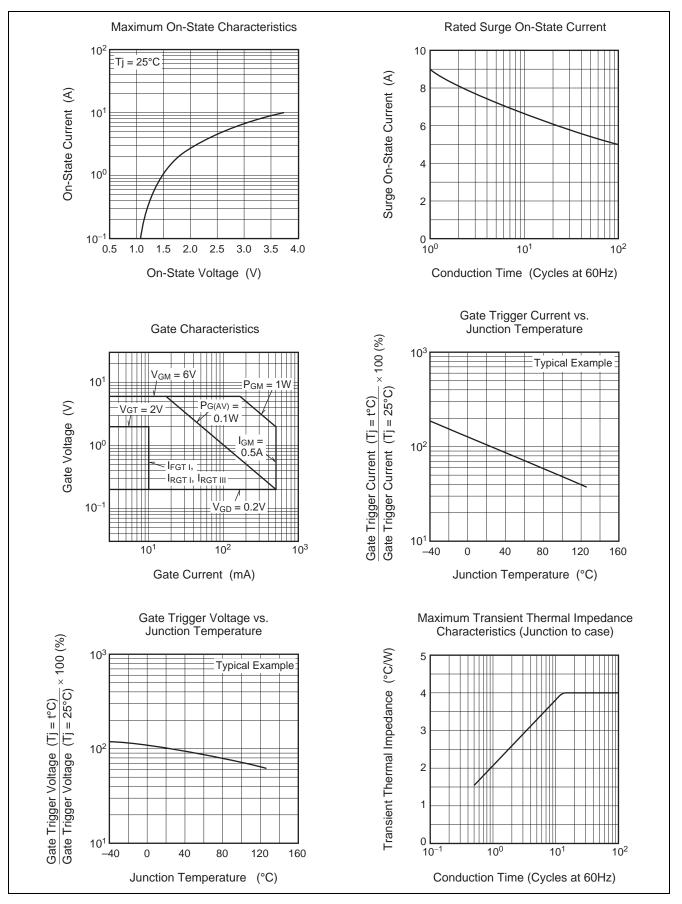
3. Case temperature is measured on the  $T_2$  tab.

4. Test conditions of the critical-rate of rise of off-state commutation voltage is shown in the table below.

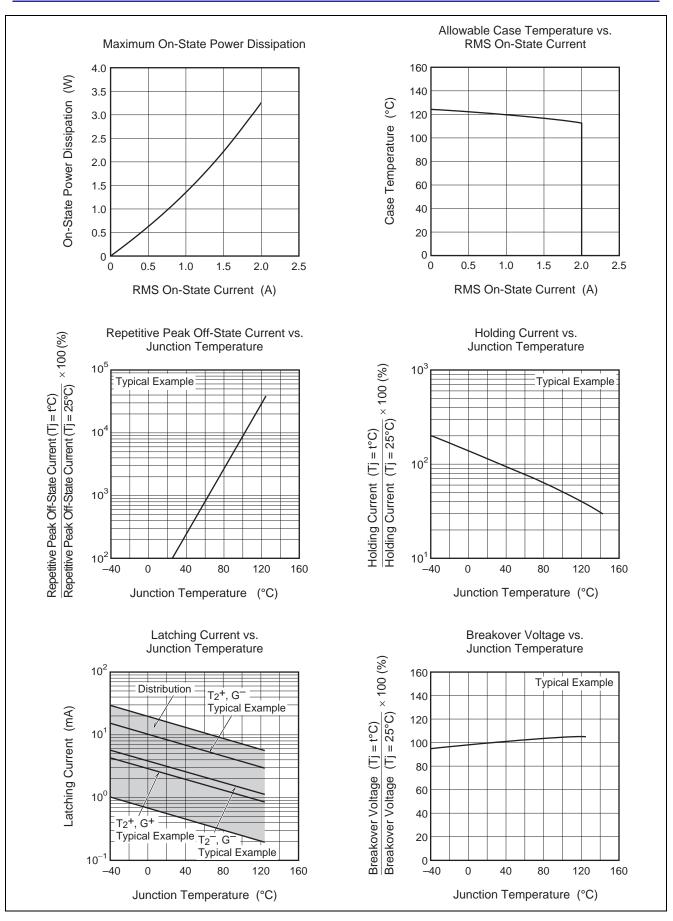
Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage
2. Rate of decay of on-state commutating current (di/dt)c = -1.0 A/ms	Main Current → Time
3. Peak off-state voltage $V_D = 400 \text{ V}$	Main Voltage Time (dv/dt)c V <sub>D</sub>



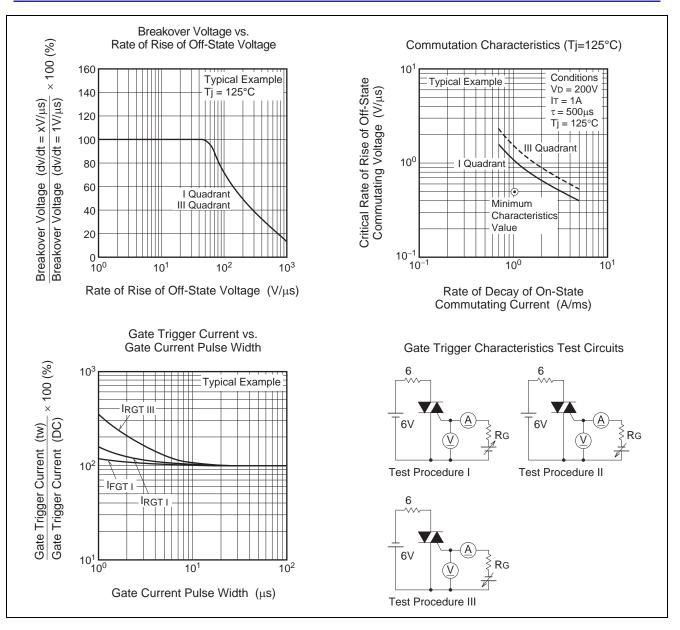
#### **Performance Curves**





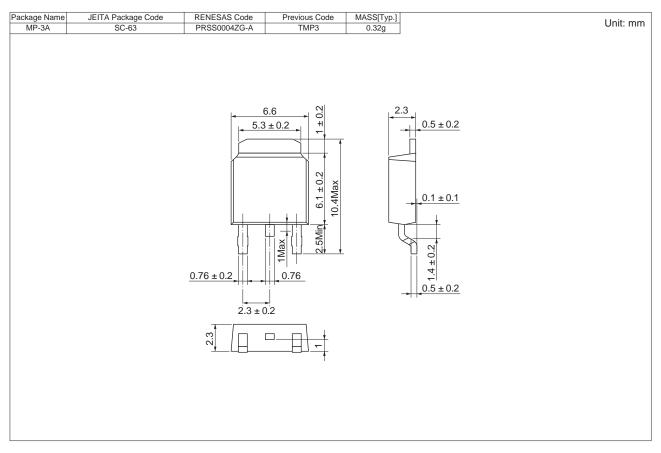








#### **Package Dimensions**



### **Ordering Information**

Orderable Part Number	Packing	Quantity	Remark
BCR2AS-14A#B00	Tube	75 pcs.	—
BCR2AS-14A-T13#B00	Embossed Tape	3000 pcs.	Taping direction "T1"

Note : Please confirm the specification about the shipping in detail.



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