GaAs MMIC Bi-Phase Modulator

HMC135

FEBRUARY 1995

Features

CHIP INTEGRATES DIRECTLY INTO MIC DESIGNS

30 dB OF CARRIER SUPPRESSION

DIRECT MODULATION IN THE 1.8-5.2 GHz BAND

FUNCTIONS ALSO AS A PHASE DETECTOR

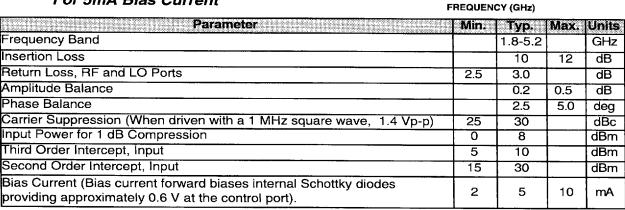
General Description

The HMC135 Bi-Phase Modulator is designed to phase-modulate an RF signal into reference and 180 degree states. Device input is at the RF port and output is at the LO port. The polarity of the bias current at the control port (IF port) defines the phase states. Excellent amplitude and phase balance provided by closely matched monolithic balun and diode circuits delivers 30 dB of carrier suppression in a tiny monolithic chip.

The device also functions as a demodulator or phase comparator. As a demodulator, data emerges at the control port when a modulated signal at the RF port is compared to a reference signal at the LO port. As a phase comparator, the phase angle between two signals applied to the RF and LO ports is represented by an analog voltage at the control port.

Except for carrier suppression, the data presented here was measured under static conditions in which a DC bias current (nominally 5 mA) is applied to the control port.

Electrical Performance For 5mA Bias Current

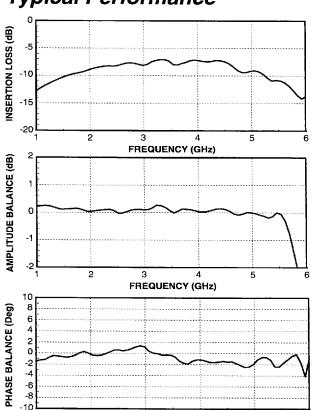


21 Cabot Road, Woburn, Massachusetts 01801

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Typical Performance

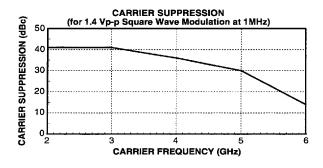


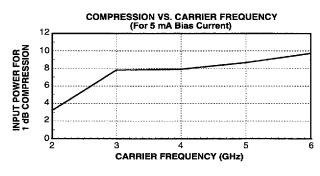


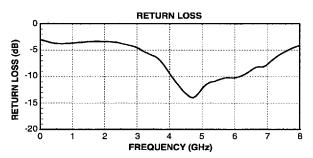
GaAs MMIC Bi-Phase Modulator

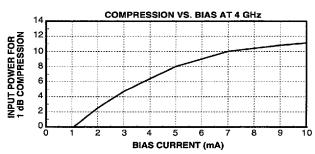
HMC135

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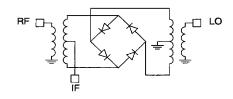






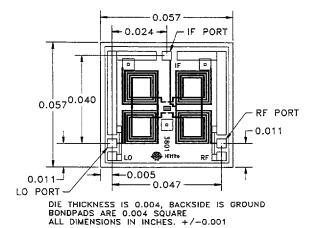


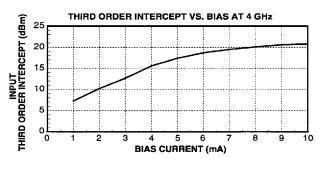
Schematic



THIRD ORDER INTERCEPT VS. FREQUENCY (For 5mA Bias Current) 15 10 20 3 4 5 CARRIER FREQUENCY (GHz)

Outline





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