

APLUS INTEGRATED CIRCUITS INC.

APR9301

Single-Chip Voice Recording & Playback Device for Single 20 to 30 Second Message

Features

- Single-chip, high quality voice recording & playback solution
 - No external ICs required
 - Minimum external components
- Non-volatile Flash memory technology
 - No battery backup required
 - 100K record cycles (typical)
 - 100-year message retention (typical)
- Single message of 20 to 30 seconds, with external resistor selection
- User-friendly, easy-to-use operation
 - Programming & development systems not required
 - Level-activated recording & edge-activated playback switches
- Low power consumption
 - Operating current: 25 mA (typical, no load)
 - Standby current: 1 A (typical, no load)
- Automatic power-down feature for longer battery life
- Chip enable pin for simple message expansion
- Single 5 V power supply

General Description

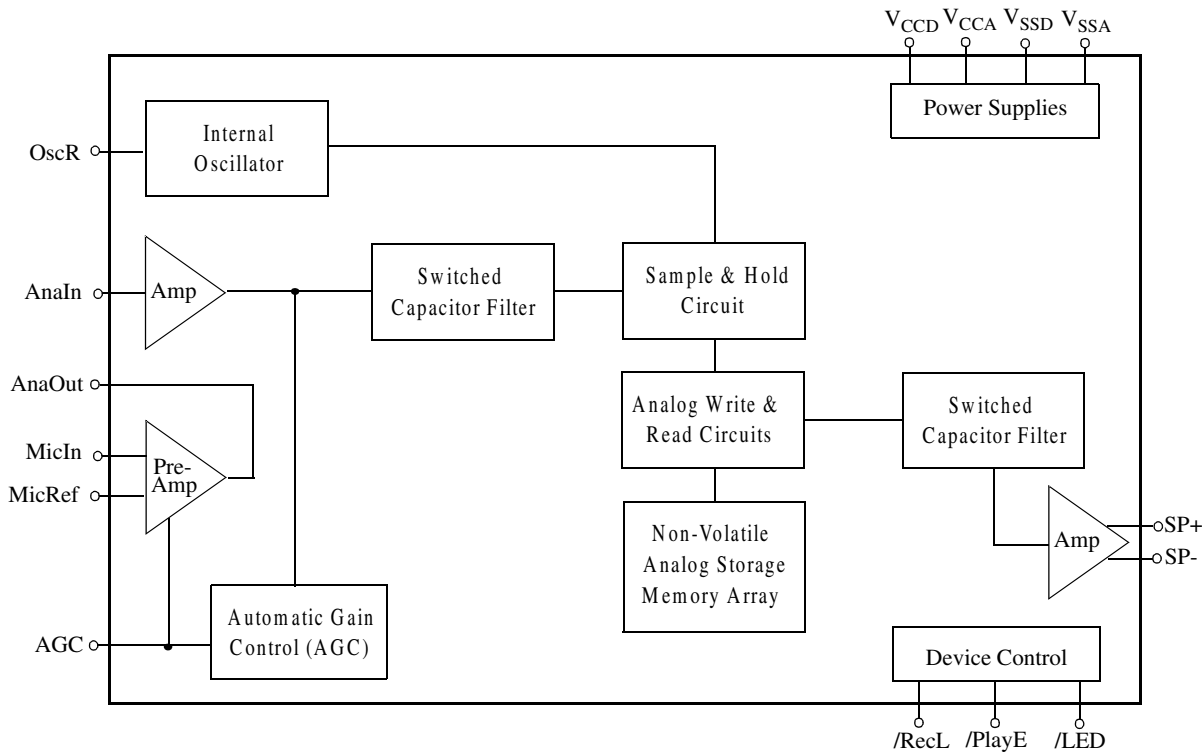
The APR9301 device offers true single-chip solid-state storage capability and requires no software or microcontroller support. It provides high-quality recording and playback with a single 20- to 30-second message. It is ideal for portable voice recorders, toys, and many other consumer and industrial applications.

APLUS integrated analog/multi-level storage technology is implemented in advanced Flash non-volatile memory cells, each of which can typically store more than 256 voltage levels. The APR9301 device stores and reproduces voice signals in their natural forms, eliminating the distortion that is often introduced by encoding and compression. The device combines a small size with low power consumption, non-volatility, and ease-of-use for a cost effective solution to voice recording and playback.

Functional Block Diagram

Figure 2 shows the functional block diagram for the APR9301 device.

Figure 2 APR9301 Functional Block Diagram



Sample Application

Figure 3 shows the diagram for a single, 20-second message recording and playback application using the APR9301 device. When pins are connected as shown in this example, the operating modes are as follows:

Record Mode (Level-Activated)

A single voice message of up to 20 seconds can be recorded. The /LED pin will go low during the actual recording process to provide a visual indication if an LED light is connected to this pin. The chip is in record mode as long as the /RecL pin stays low (level-activated). If the message lasts longer than 20 seconds, recording will terminate automatically after the last available memory cell is written. If the message is shorter than 20 seconds, the recording operation will stop when the /RecL pin goes high. The speaker driver is automatically tristated during the recording operation.

Messages of up to 30 seconds can be recorded by using different OscR resistor values (see Table 1).

Playback Mode (Edge-Activated)

Playback always starts from the beginning of the message. The chip is in playback mode after the /PlayE pin pulses low (edge-activated). Playback will stop immediately when the /PlayE pin pulses low a second time. If the newly recorded message is shorter than the previously recorded message, the remaining portion of the previous message will not be played after the new message is played back. The input pre-amplifier, AGC, and main amplifier circuits are disabled during playback.

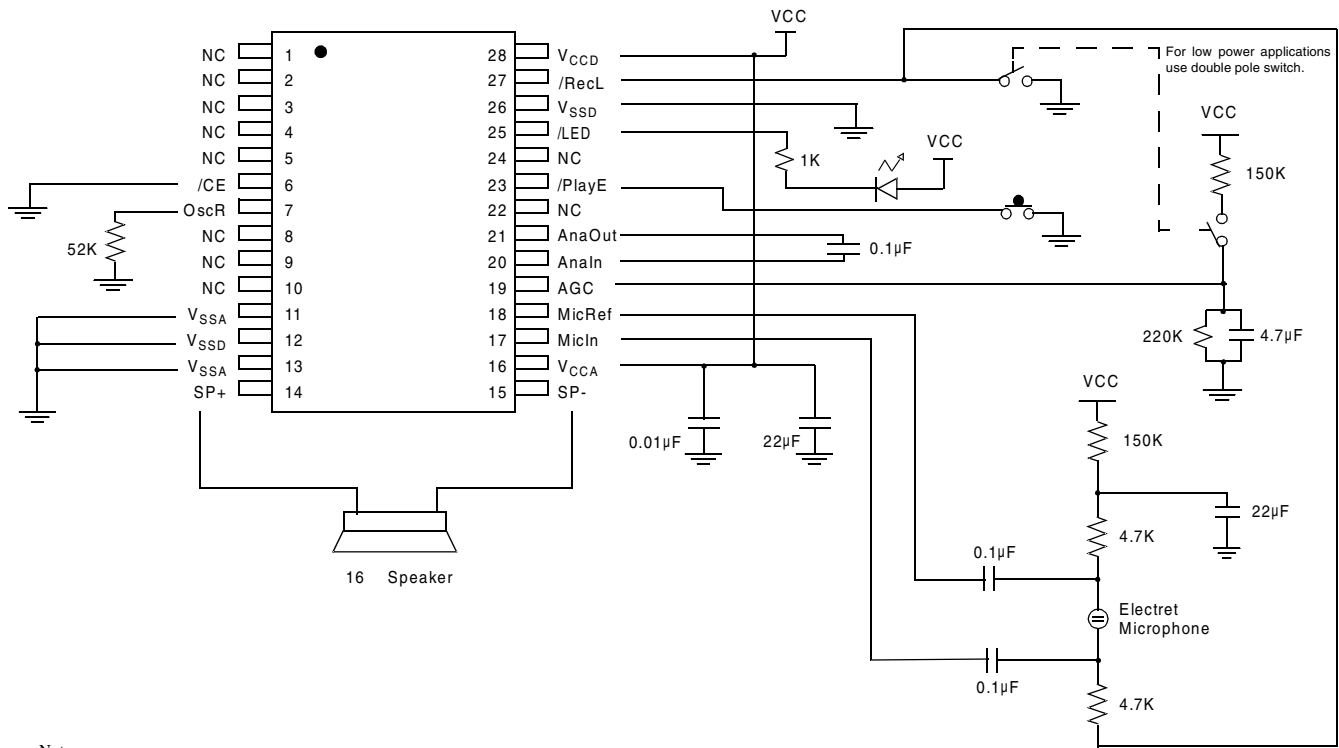
Standby Mode (/CE = "0")

The chip will automatically return to the standby state after recording or playback operation is completed.

Power Down Mode (/CE = "1")

The chip is always in standby state. No recording or playback is allowed. Current consumption is typically less than 1 A.

Figure 3 Sample Application for the APR9301



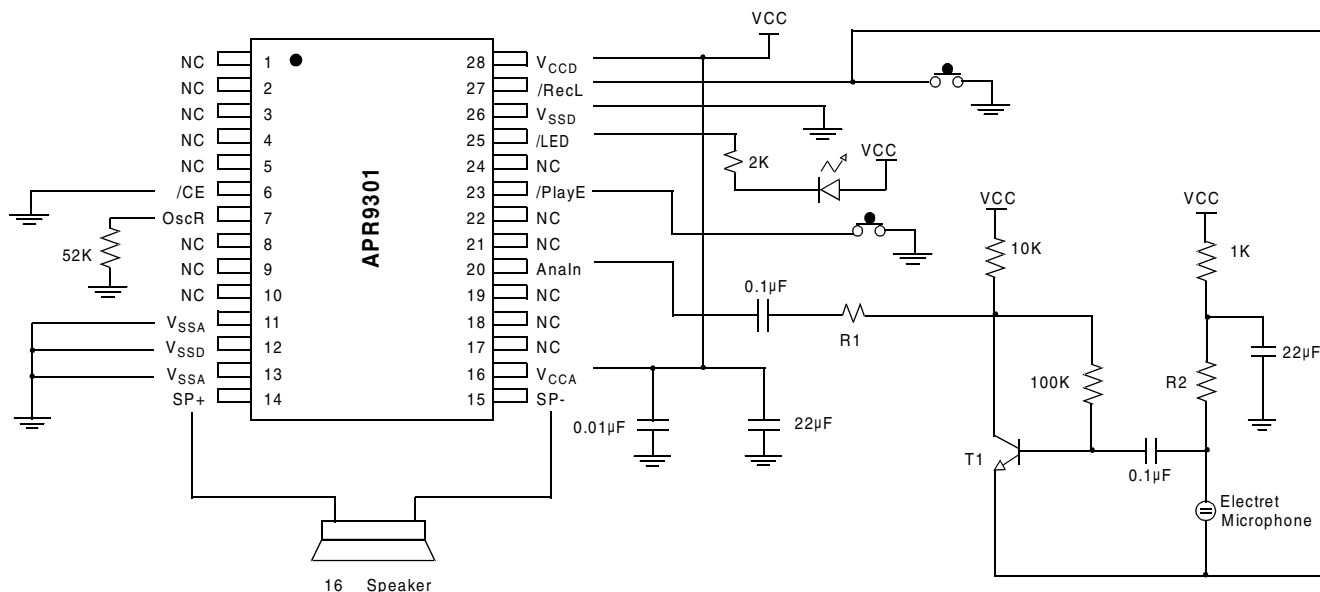
Notes:
 NC = No Connect (must be floating).
 Pins 23 and 27 have internal pull-up resistors.
 The typical sampling frequency is 6.4 kHz with OscR = 52 k .

Table 1 Typical Dependence of Sampling Frequency and Total Voice Duration on OscR Resistor Value ($V_{CCA} = V_{CCD} = 5V$; $V_{SSA} = V_{SSD} = 0V$; $T_A = 25^\circ C$)

Pin 7 - OscR	Typical Sampling Frequency	Cutoff Frequency (3 dB Point)	Typical Total Voice Duration
38 k	8 kHz	3.4 kHz	16 seconds
52 k	6.4 kHz	2.7 kHz	20 seconds
67 k	5.3 kHz	2.3 kHz	24 seconds
89 k	4.0 kHz	1.7 kHz	32 seconds

Figure 4 shows the same application using external microphone biasing instead of the microphone amplifier on the APR9301 device, thus bypassing the preamplifier portion of the chip function.

Figure 4 Sample Application Using External Microphone Biasing



Notes:
 NC = No Connect (must be floating).
 Pins 23 and 27 have internal pull-up resistors.
 The typical sampling frequency is 6.4 kHz with OscR = 52 k .
 NPN bipolar transistor, T1 model #MPS3904 or equivalent.
 R1 and R2 are recommended to be 30 k and 10 k , respectively, for typical applications.

Electrical Characteristics

Tables 2 through 4 list Absolute Maximum Ratings, Recommended DC Characteristics, and recommended Analog Characteristics for the APR9301 device.

Absolute Maximum Ratings

Stresses greater than those listed in Table 2 may cause permanent damage to the device. These specifications represent a stress rating only. Operation of the device at these or any other conditions above those specified in the recommended DC Characteristics or recommended Analog Characteristics of this specification is not implied. Operation of the device at maximum conditions for extended periods may affect reliability.

Table 2 Absolute Maximum Ratings.

Item	Symbol	Condition	Min	Max	Unit
Power Supply Voltage	V_{CC}	$T_A = 25\text{ }^\circ\text{C}$	4.5	6.5	V
Input Voltage	V_{IN1}	$T_A = 25\text{ }^\circ\text{C}$	-0.3	$V_{CC} + 0.3$	V
Input Voltage	V_{IN2}	$ I_{IN} < 20\text{ mA}$	-1.0	$V_{CC} + 1.0$	V
Storage Temperature	T_{STG}	-	-65	150	$^\circ\text{C}$
Temperature Under Bias	T_{BS}	-	-65	125	$^\circ\text{C}$
Lead Temperature	T_{LD}	$< 10\text{ s}$	-	300	$^\circ\text{C}$

Table 3 DC Characteristics ^{Note 1}

Item	Symbol	Condition	Min	Typ	Max	Unit
Input High Voltage	V_{IH}	-	0.8 V_{CC}	$\times V$	-	V
Input Low Voltage	V_{IL}	-	-	-	0.8	V
Output High Voltage	V_{OH}	$I_{OH} = -1.6\text{ mA}$	2.4	-	-	V
Output Low Voltage	V_{OL}	$I_{OL} = 4.0\text{ mA}$	-	-	0.45	V
Input Leakage Current	I_{IH}	$V_{IH} = V_{CC}$	-	-	1.0	A
Input Leakage Current	I_{IL}	$V_{IL} = V_{SS}$ ^{Note 2}	-1.0	-	-	A
Output Tristate Leakage Current	I_{OZ}	$V_{OUT} = V_{CC}$ or $V_{OUT} = V_{SS}$	-1.0	-	1.0	A
Operating Current Consumption	I_{CC}	Internal Clock, No Load	-	25	-	mA
Standby Current Consumption	I_{CCS}	No Load	-	1.0	-	A

Table 4 Analog Characteristics ^{Note 1}

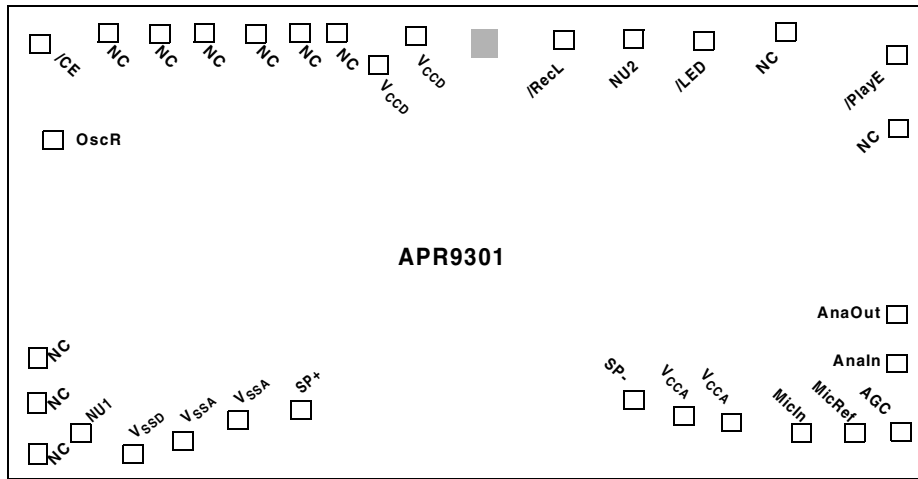
Item	Symbol	Condition	Min	Typ	Max	Unit
MicIn Input Voltage	V_{MI}	-	-	-	20	mV _{P-P}
MicIn Input Resistance	R_{MI}	-	-	10	-	k
MicIn Amp Gain (1)	G_{MI1}	$AGC \leq 2.2\text{ V}$	-	24	-	dB
MicIn Amp Gain (2)	G_{MI2}	$AGC \geq 3.0\text{ V}$	-	-45	-15	dB
AnaIn Input Voltage	V_{ANI}	-	-	-	50	mV _{P-P}
AnaIn Input Resistance	R_{ANI}	-	-	10	-	k
AnaIn Amp Gain	G_{ANI}	AnaIn to SP+/-	-	22	-	dB
AGC Output Resistance	R_{AGC}	-	-	1	-	k
SP+/- Output Power	P_{SP}	$R_{SP+/-} = 16$	-	12.2	-	mW
Voltage Amplitude Across SP+/-	V_{SP}	$R_{SP+/-} \geq 16$	-	1.25	-	V _{P-P}
Total Harmonic Distortion	THD	@ 1kHz & 20mV _{p-p} input	-	1	-	%

Note 1: Typical Values: $V_{CCD} = V_{CCA} = 5\text{ V}$; $V_{SSD} = V_{SSA} = 0\text{ V}$; $T_A = 25\text{ }^\circ\text{C}$

Note 2: Except pins 23 and 27 which have internal pull-up resistors.

Bonding Pad Diagram & Description of Bonding Pad Coordinates

Figure 5 APR9301 Die Bonding Pad Diagram



Notes:

Die diagram is with respect to die center (μm).

Die Dimensions:
 $x = 214 \pm 1$ mils
 $y = 144 \pm 1$ mils

Die Thickness:
 13.8 ± 1.0 mils
 $(350 \pm 25 \mu\text{m})$

Pad Opening:
 $110 \mu\text{m}$
 4.3 mils

Connect substrate to ground.

Table 5 APR9301 Bonding Pad Coordinates

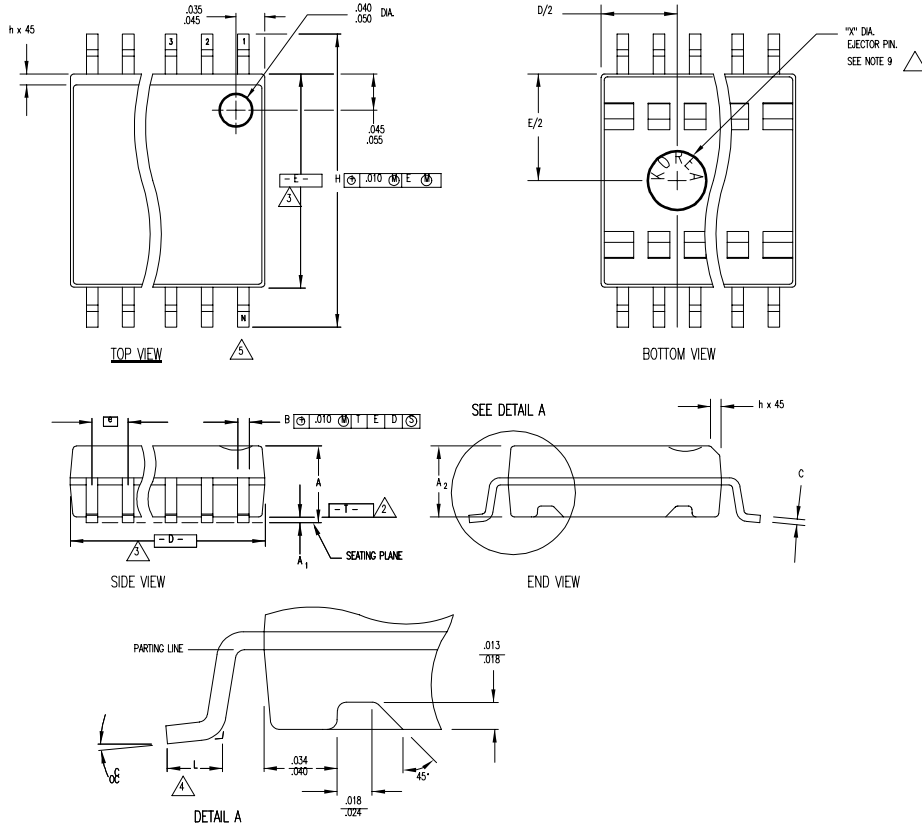
Pin	Pin Name	X Axis ^(Note)	Y Axis ^(Note)
/CE	Chip Enable	-2496.20	1565.80
OscR	Oscillator Frequency-Setting Resistor	-2459.55	729.80
NU1	Connect to Ground	-1808.45	-1496.10
VSSD	Digital Ground Supply	-1564.05	-1572.00
VSSA	Analog Ground Supply	-1384.05	-1548.70
VSSA	Analog Ground Supply	-1204.35	-1477.10
SP+	Non-Inverting Speaker Output	-707.15	-1390.00
SP-	Inverting Speaker Output	479.15	-1389.90
VCCA	Analog Power Supply	976.45	-1492.00
VCCA	Analog Power Supply	1190.40	-1523.70
MicIn	Microphone Input	1619.45	-1551.40
MicRef	Microphone Reference Input	2035.45	-1551.40
AGC	Automatic Gain Control	2487.45	-1551.40
AnaIn	Analog Signal Input	2487.45	-1049.90
AnaOut	Analog Signal Output	2487.45	-648.90
/PlayE	Edge-Activated Playback	2493.65	1371.10
/LED	LED Output	1430.70	1565.80
NU2	Connect to Ground	865.75	1565.80
/Recl	Level-Activated Record	258.15	1565.80
VCCD	Digital Power Supply	-229.40	1579.05
VCCD	Digital Power Supply	-510.80	1541.60

Note: With respect to die center (μm)

Package Outlines

The APR9301 device is available in the following packages. Packages conform to JEDEC and EIAJ standards.

Figure 6 28-Pin Plastic Small-Outline Integrated Circuit (SOIC)



NOTES:

1. MAXIMUM DIE THICKNESS ALLOWABLE IS .025.
- △ DIMENSIONING & TOLERANCES PER ANSI Y14.5M - 1982.
- △ "T" IS A REFERENCE DATUM.
- △ "D" & "E" ARE REFERENCE DATUMS AND DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS, BUT DOES INCLUDE MOLD MISMATCH AND ARE MEASURED AT THE MOLD PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
- △ "L" IS THE LENGTH OF TERMINAL FOR SOLDERING TO A SUBSTRATE.
- △ "N" IS THE NUMBER OF TERMINAL POSITIONS.
- △ TERMINAL POSITIONS ARE SHOWN FOR REFERENCE ONLY.
- △ FORMED LEADS SHALL BE PLANAR WITH RESPECT TO ONE ANOTHER WITHIN .003 INCHES AT SEATING PLANE.
- △ COUNTRY OF ORIGIN LOCATION AND EJECTOR PIN ON PACKAGE BOTTOM IS OPTIONAL AND DEPEND ON ASSEMBLY LOCATION.
10. THE POCKETS ON THE BOTTOM ARE OPTIONAL.
11. CONTROLLING DIMENSION: INCHES.

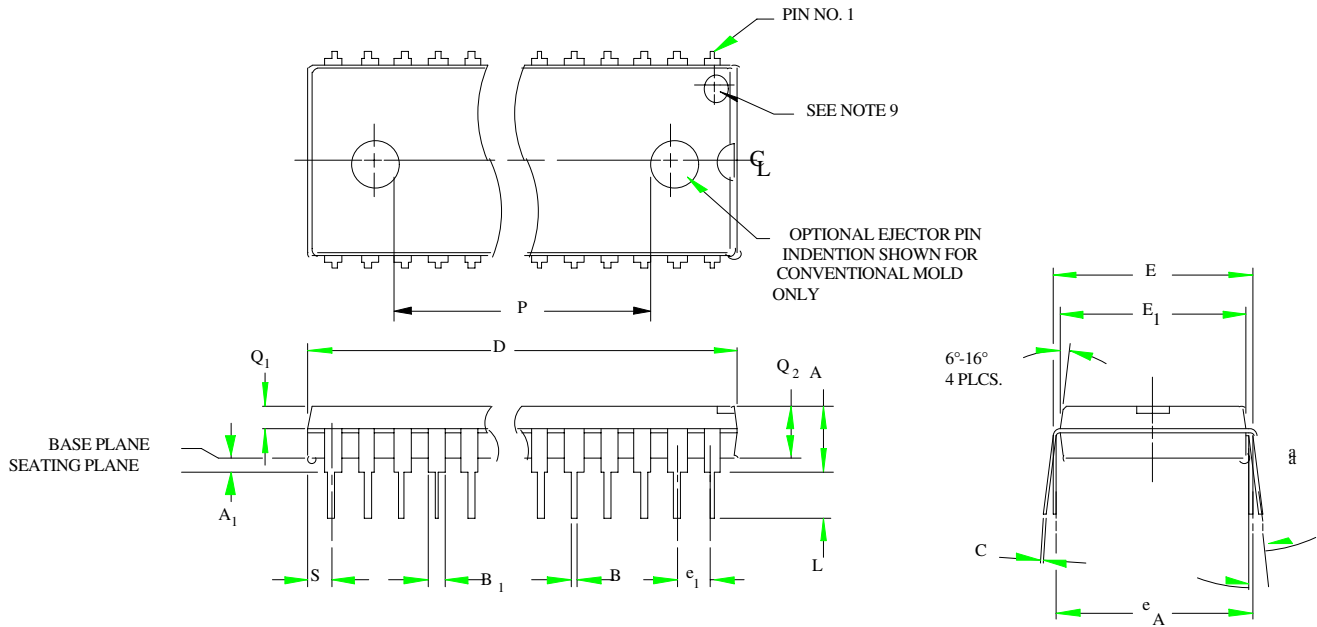
THIS TABLE IN INCHES

Symbol	COMMON DIMENSIONS			NOTE VARIATIONS	3 D			5 N
	MIN.	NOM.	MAX.		MIN.	NOM.	MAX.	
A	.097	.101	.104	AA	.402	.407	.412	16
A ₁	.0050	.009	.0115	AB	.451	.456	.461	18
A ₂	.090	.092	.094	AC	.500	.505	.510	20
B	.014	.016	.019	AD	.602	.607	.612	24
C	.0091	.010	.0125	AE	.701	.706	.711	28
D	SEE VARIATIONS			3				
E	.292	.296	.299					
e	.050 BSC							
H	.400	.406	.410					
h	.010	.013	.016					
L	.024	.032	.040					
N	SEE VARIATIONS			5				
de	0	5	8					
X	.085	.093	.100					

THIS TABLE IN MILLIMETERS

Symbol	COMMON DIMENSIONS			NOTE VARIATIONS	3 D			5 N
	MIN.	NOM.	MAX.		MIN.	NOM.	MAX.	
A	2.46	2.56	2.64	AA	10.21	10.34	10.46	16
A ₁	0.127	0.22	0.29	AB	11.46	11.58	11.71	18
A ₂	2.29	2.34	2.39	AC	12.70	12.83	12.95	20
B	0.35	0.41	0.48	AD	15.29	15.42	15.54	24
C	0.23	0.25	0.32	AE	17.81	17.93	18.06	28
D	SEE VARIATIONS			3				
E	7.42	7.52	7.59					
e	1.27 BSC							
H	10.16	10.31	10.41					
h	0.25	0.33	0.41					
L	0.61	0.81	1.02					
N	SEE VARIATIONS			5				
de	0	5	8					
X	2.16	2.36	2.54					

Figure 7 28-Pin Plastic Dual In-Line Package (P-600)



SYMBOL	INCHES			MILLIMETERS			NOTE
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
A			.190			4.83	
A ₁	.015			0.38			
B	.015	.018	.022	0.38	0.46	0.56	
B ₁	.055	.060	.065	1.40	1.52	1.65	
C	.008	.010	.012	0.20	0.25	0.30	
D	1.445	1.450	1.455	36.70	36.83	36.96	5
E	.600		.625	15.24		15.88	
E ₁	.530	.540	.550	13.46	13.72	13.97	5
e ₁	.100 BSC			2.54 BSC			
e _A	.600 BSC			15.24 BSC			
L	.125		.135	3.18		3.43	3
a	0°		15°	0°		15°	4
N	28			28			
P	.685			17.40			
Q ₁	.065	.070	.075	1.65	1.78	1.91	
Q ₂		.150			3.18		
S	.070	.075	.080	1.78	1.91	2.03	

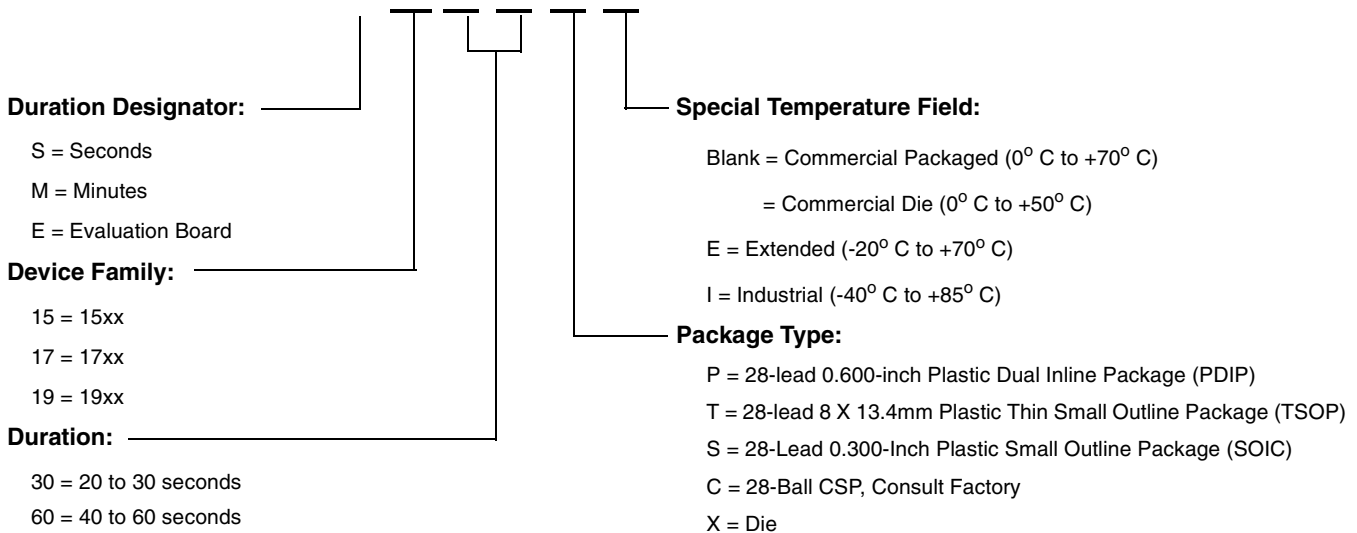
NOTES:

1. REFER TO APPLICABLE SYMBOL LIST.
2. DIMENSIONING AND TOLERANCE PER ANSI Y14.5-1982.
3. APPLIES TO SPREAD LEADS PRIOR TO INSTALLATION.
4. N IS THE MAXIMUM QUANTITY OF LEAD POSITIONS
5. DIMENSION D & E ARE TO BE MEASURED AT MAXIMUM MATERIAL CONDITION BUT DO NOT INCLUDE MOLD FLASH. ALLOWABLE MOLD FLASH IS 0.010 INCH/0.254 mm.
6. CONTROLLING DIMENSION: INCH
7. DIMENSION A, A₁ & L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
8. THIS PACKAGE CONFORMS TO JEDEC REFERENCE MS-011, VARIATION AB.
9. IT IS AN OPTION TO SHOW PIN 1 IDENTIFIER.

Ordering Information

Package	Pin	Grade	Part Number
SOIC	28	Commercial	APR9301S
P-600	28	Commercial	APR9301P
Die		Commercial	APR9301X

Figure 8 Product Number Descriptor Key



Note:

Not all options or combinations are available. Consult your local Distributor or Sales Representative for option availability.

APLUS INTEGRATED CIRCUITS INC.

APR9301 V2

Single-Chip Voice Recording & Playback Device for Single 20 to 30 Second Message

Features

- Single-chip, high quality voice recording & playback solution
 - No external ICs required
 - Minimum external components
- Non-volatile Flash memory technology
 - No battery backup required
 - 100K record cycles (typical)
 - 100-year message retention (typical)
- Single message of 20 to 30 seconds, with external resistor selection
- User-friendly, easy-to-use operation
 - Programming & development systems not required
 - Level-activated recording & edge-activated playback switches
- Low power consumption
 - Operating current: 25 mA (typical, no load)
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- Automatic power-down feature for longer battery life
- Chip enable pin for simple message expansion
- Single 5 V power supply

General Description

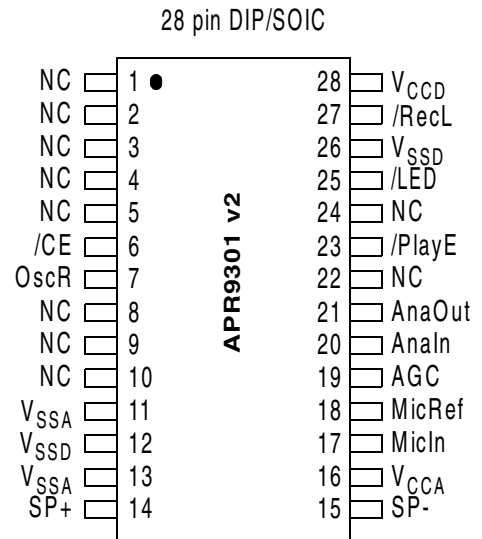
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APLUS integrated analog/multi-level storage technology is implemented in advanced Flash non-volatile memory cells, each of which can typically store more than 256 voltage levels. The IAPR9301 device stores and reproduces voice signals in their natural forms, eliminating the distortion that is often introduced by encoding and compression. The device combines a small size with low power consumption, non-volatility, and ease-of-use for a cost effective solution to voice recording and playback.

Pinout Diagram

Figure 1 shows the pinouts for the APR9301

Figure 1 APR9301 V2 Pinout Diagram*



*NC = No Connect (must be floating)