MAOC-009265-PKG003

Voltage Controlled Oscillator
9.4 - 10.8 GHz

Rev. V1

Features

- Low Phase Noise
- Wide Tuning Range
- Divide-by-Two Output
- Integrated Buffer Amplifier
- Excellent Temperature Stability
- +5V Bias Supply
- Lead-Free 5 mm 32-Lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MAOC-009265 is an InGaP HBT-based voltage controlled oscillator for frequency generation. No external matching components are required. This VCO is easily integrated into a phase lock loop using the divide-by-two output. The extremely low phase noise makes this part ideal for many radio applications including high capacity digital radios.

The MAOC-009265 primary applications are Point-to-Point Radio, Point-to-Multipoint Radio, Communications Systems, and Low Phase Noise applications.

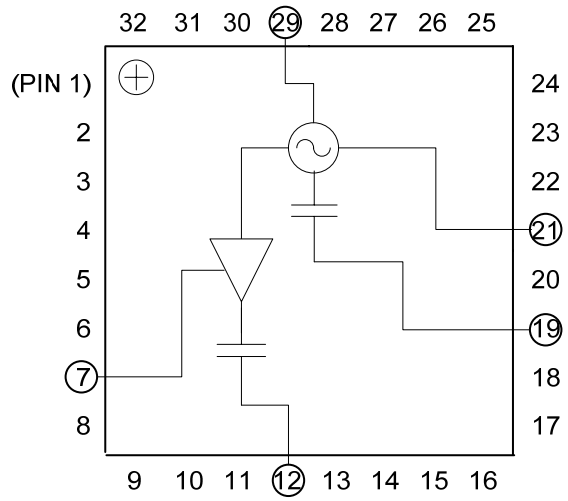
The 5 mm PQFN package has a lead-free finish that is RoHS compliant and compatible with a 260ºC reflow temperature. The package also features low lead inductance and an excellent thermal path.

Ordering Information[1]

Part Number	Package
MAOC-009265-TR0500	500 piece reel
MAOC-009265-TR1000	1000 piece reel
MAOC-009265-SMB003	Sample Board

1. Reference Application Note M513 for reel size information.

Block Diagram



Pin Designations[2]

Pin	Function	Pin	Function
1	N/C	17	N/C
2	N/C	18	N/C
3	N/C	19	RF
4	N/C	20	N/C
5	N/C	21	V_{CC}
6	N/C	22	N/C
7	V_{BUFFER}	23	N/C
8	N/C	24	N/C
9	N/C	25	N/C
10	N/C	26	N/C
11	N/C	27	N/C
12	RF/2	28	N/C
13	N/C	29	V_{TUNE}
14	N/C	30	N/C
15	N/C	31	N/C
16	N/C	32	N/C

2. The exposed pad centered on the package bottom must be connected to RF and DC ground. Connecting all N/C pins to RF/DC Ground in the layout is also recommended.

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Electrical Specifications: T_A=+25°C, V_{CC} = V_{BUFFER} = 5.0 V[3], Z_0 = 50 Ω

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Output Power	RF Port, 9.4 - 10.8 GHz RF/2 Port, 4.7 - 5.4 GHz	dBm	4 1	7 4	—
SSB Phase Noise V_{CC}=V_{BUFFER}=V_{TUNE}=5V	RF Port, 10KHZ Offset RF Port, 100KHZ Offset	dBc/Hz	—	-86 -113	—
Harmonics/Subharmonics V_{CC}=V_{BUFFER}=V_{TUNE}=5V	RF Port, $^1/_2$ F_o RF Port, 2 F_o	dBc	—	19 27	—
Pulling (Sensitivity to Match) V_{CC}=V_{BUFFER}=V_{TUNE}=5V	RF Port, VSWR = 1.95:1 to 2.25:1	MHz pk-pk	—	8.2	—
Pushing (Sensitivity to Supply Voltage)	RF Port, V_{TUNE} = 5 V RF/2 Port, V_{TUNE} = 5 V	MHz/V	—	20 4	—
Frequency Drift Rate (Sensitivity to Temperature)	RF Port, 9.4 - 10.8 GHz RF/2 Port, 4.7 - 5.4 GHz	MHz/ºC	—	0.9 0.5	—
Output Return Loss	RF Port, 9.4 - 10.8 GHz RF/2 Port, 4.7 - 5.4 GHz	dB	—	-3 -7	—
Tuning Sensitivity @ RF Port	V_{TUNE} = 5 V	GHz/V	—	0.12	—
Supply Current	I_{TOTAL} (I_{CC} + I_{BUFFER}) I_{CC} I_{BUFFER}	mA	—	175 155 20	205 175 30
Tune Voltage	V_{TUNE}	V	1	—	13
Tuning Current Leakage	V_{TUNE} = 13 V	µA	—	5	10

3. VCO can operate over the 4.75 V to 5.25 V supply voltage range.

Absolute Maximum Ratings [4,5,6]

Parameter	Absolute Maximum
Supply Voltage (V_{CC} & V_{BUFFER})	+5.5 Vdc
V_{TUNE}	0 to +15 Vdc
Storage Temperature	-55ºC to +150ºC
Operating Temperature	-40ºC to +85ºC
Case Temperature (T_C) (measured @ exposed pad)	+100ºC
Junction Temperature[7]	+135ºC

4. Exceeding any one or combination of these limits may cause permanent damage to this device.
5. M/A-COM Technology does not recommend sustained operation near these survivability limits.
6. Operating @ T_C ≤ +85ºC will ensure MTBF > 2.5 x 10^6 hours.
7. Junction Temperature (T_J) = T_C + Θjc * (V * I)
   Typical thermal resistance (Θjc) = 35° C/W.
   a) For T_C = 25°C, T_J = 55.6°C @ 5 V, 175 mA
   b) For T_C = 85°C, T_J = 116.5°C @ 5 V, 180 mA

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

ESD Rating: Class 1A

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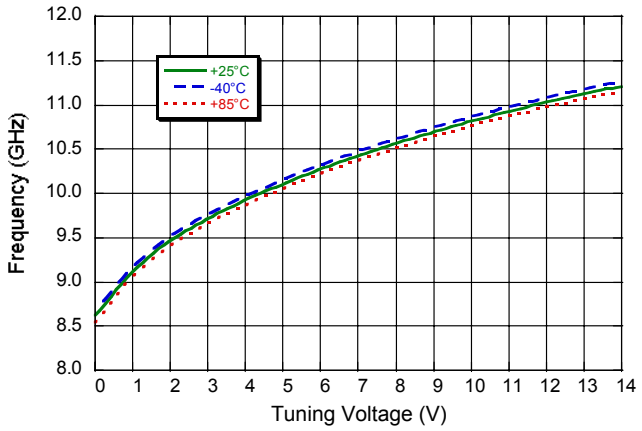
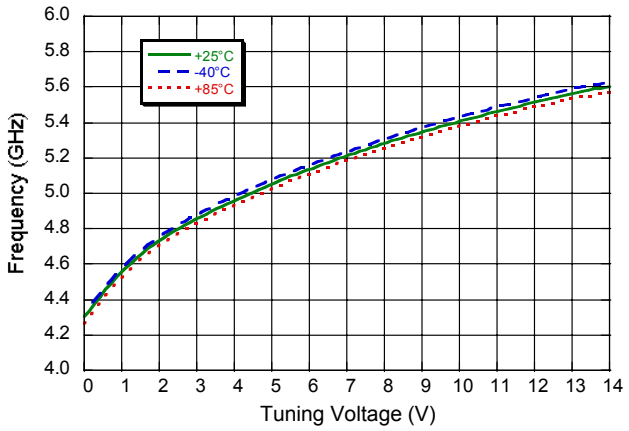
Typical Performance Curves: V_{CC} = V_{BUFFER} = 5V, T_A = +25°C (unless otherwise indicated)

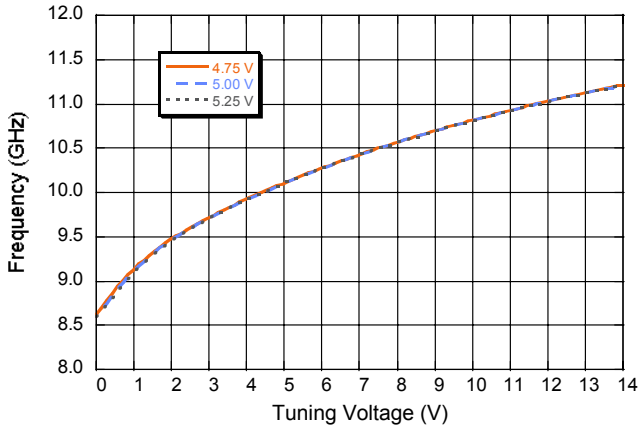
Output Frequency vs. Tuning Voltage - RF Port



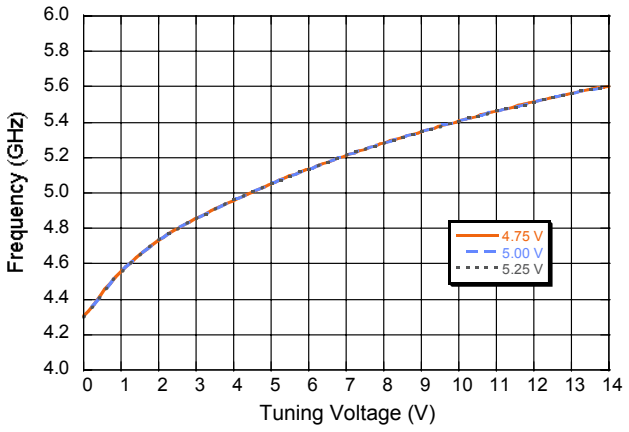
Output Frequency vs. Tuning Voltage - RF/2 Port



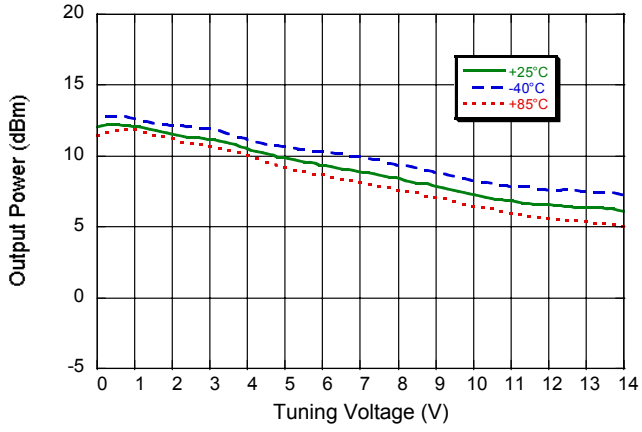
Output Frequency vs. Tuning / Supply Voltage - RF Port



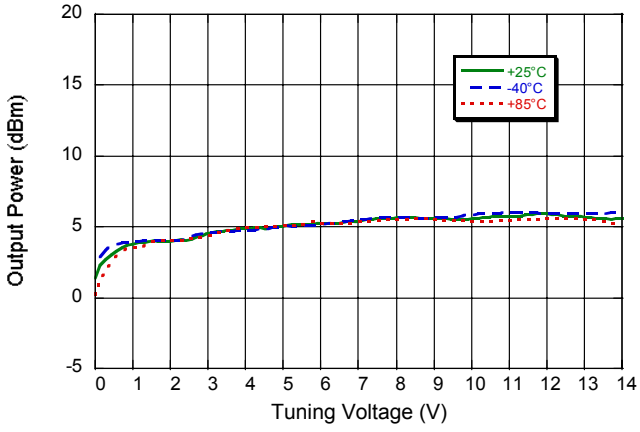
Output Frequency vs. Tuning / Supply Voltage - RF/2 Port



Output Power vs. Tuning Voltage - RF Port



Output Power vs. Tuning Voltage - RF/2 Port



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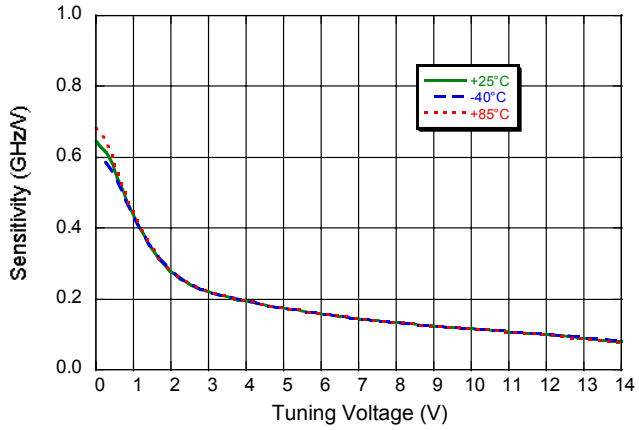
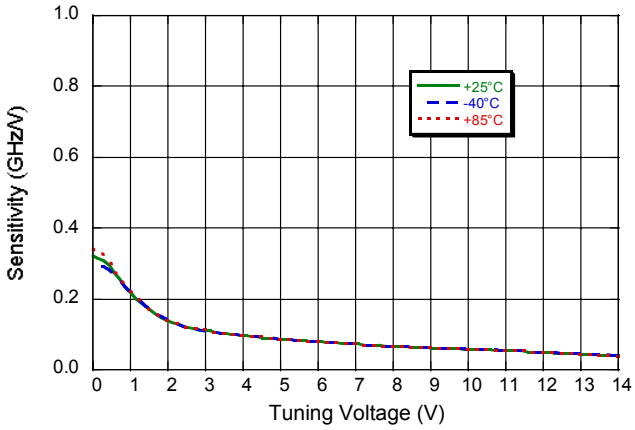
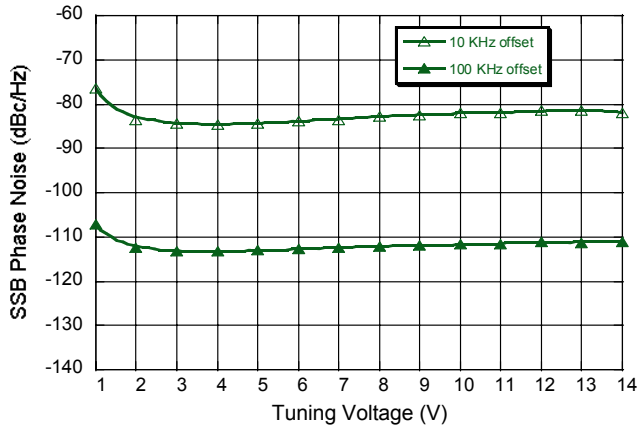
Frequency Sensitivity vs. Tuning Voltage - RF Port



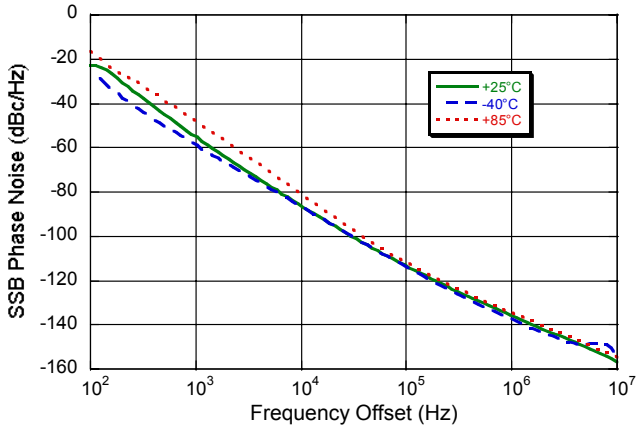
Frequency Sensitivity vs. Tuning Voltage - RF/2 Port



Single Side Band Phase Noise vs. Tuning Voltage RF Port



Single Side Band Phase Noise vs. Frequency Offset RF Port (V_{TUNE} = 5V)



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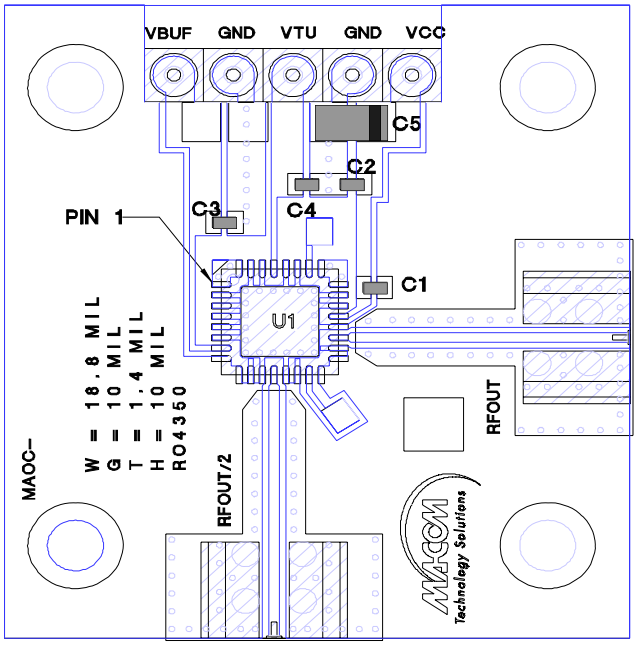
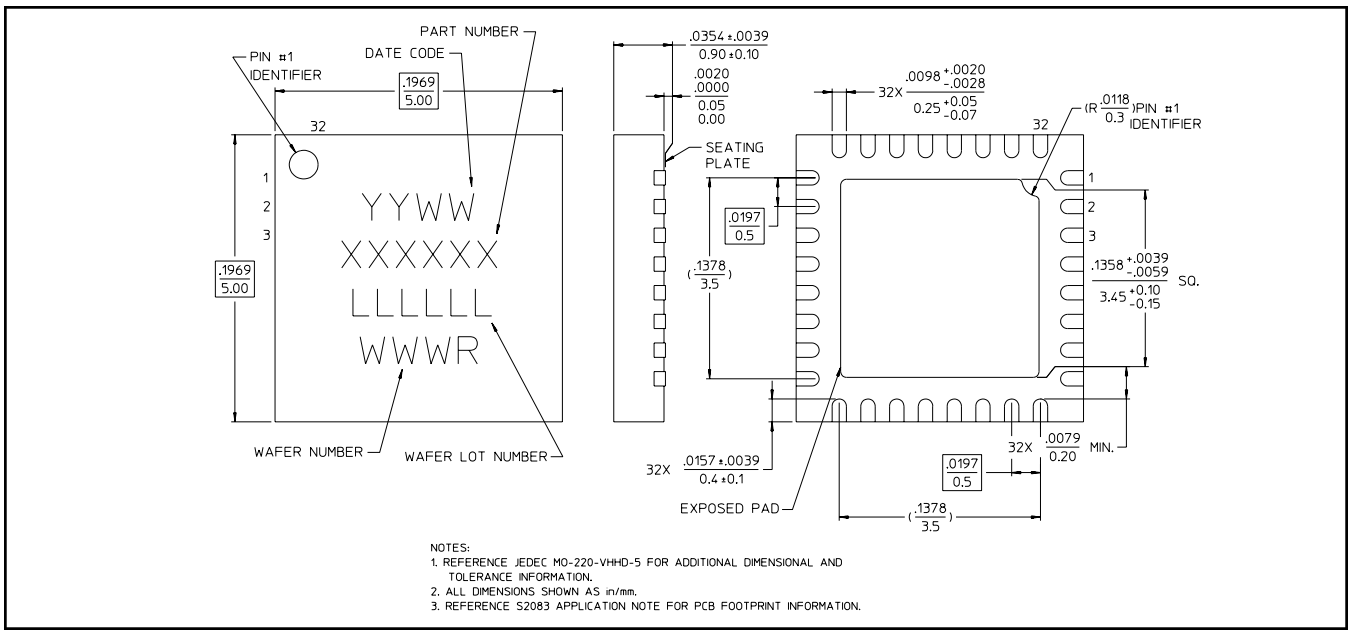
Sample Board



Parts List

Component	Value	Case Size
C1	100 pF	0402
C2, C3, C4	0.1 µF	0402
C5	10 µF Tantalum	1206

Lead-Free 5 mm 32-Lead PQFN†



† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.
Plating is 100% matte tin over copper.

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