Unit: mm



TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK3067

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance :  $R_{DS}$  (ON) = 4.2  $\Omega$  (typ.) • High forward transfer admittance :  $|Y_{fs}| = 1.7 S$  (typ.)

• Low leakage current :  $IDSS = 100 \mu A (max) (VDS = 600 V)$ 

• Enhancement-mode :  $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$ 

#### Maximum Ratings (Tc = 25°C)

Charac	teristics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	600	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	600	V
Gate-source voltage	ge	V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	2	Α
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	5	А
	Pulse (t = 100 µs) (Note 1)	I <sub>DP</sub>	8	А
Drain power dissipa	ation	P <sub>D</sub>	25	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	93	mJ
Avalanche current		I <sub>AR</sub>	2	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	2.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55~150	°C

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Weight: 1.9 g (typ.)

#### **Electrical Characteristics (Tc = 25°C)**

Characteristics	Symbol	Max	Unit
Thermal reverse, channel to case	R <sub>th (ch-c)</sub>	5.0	°C/W
Thermal reverse, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

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Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 41 mH,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 2 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution.

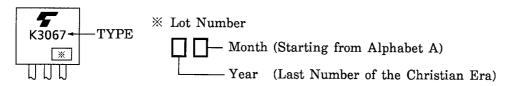
## **Electrical Characteristics (Tc = 25°C)**

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	$I_{GSS}$	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold v	roltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1 A	_	4.2	5.0	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 A	0.8	1.7	_	S
Input capacitano	е	C <sub>iss</sub>		_	380	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	40	_	pF
Output capacitance		C <sub>oss</sub>		_	120	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{OV}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{DD}$ $V_{OUT}$	_	15	_	
	Turn-on time	t <sub>on</sub>		_	25	-	ns
	Fall time	t <sub>f</sub>		_	20	ı	115
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10 \mu s$	_	80		
Total gate charge (Gate-source plus gate-drain)		Qg			9		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 480 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$		5	_	nC
Gate-drain ("miller") charge		$Q_{gd}$			4	_	

## **Source-Drain Ratings and Characteristics (Tc = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	2	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	t = 1 ms	_	_	5	Α
	I <sub>DRP</sub>	t = 100 μs	_	_	8	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V dI <sub>DR</sub> / dt = 100 A / μs	_	1000	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs	_	5.0		μC

## Marking



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