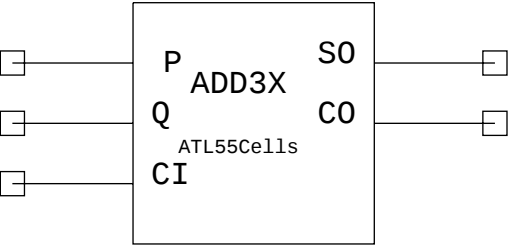


ADD3X

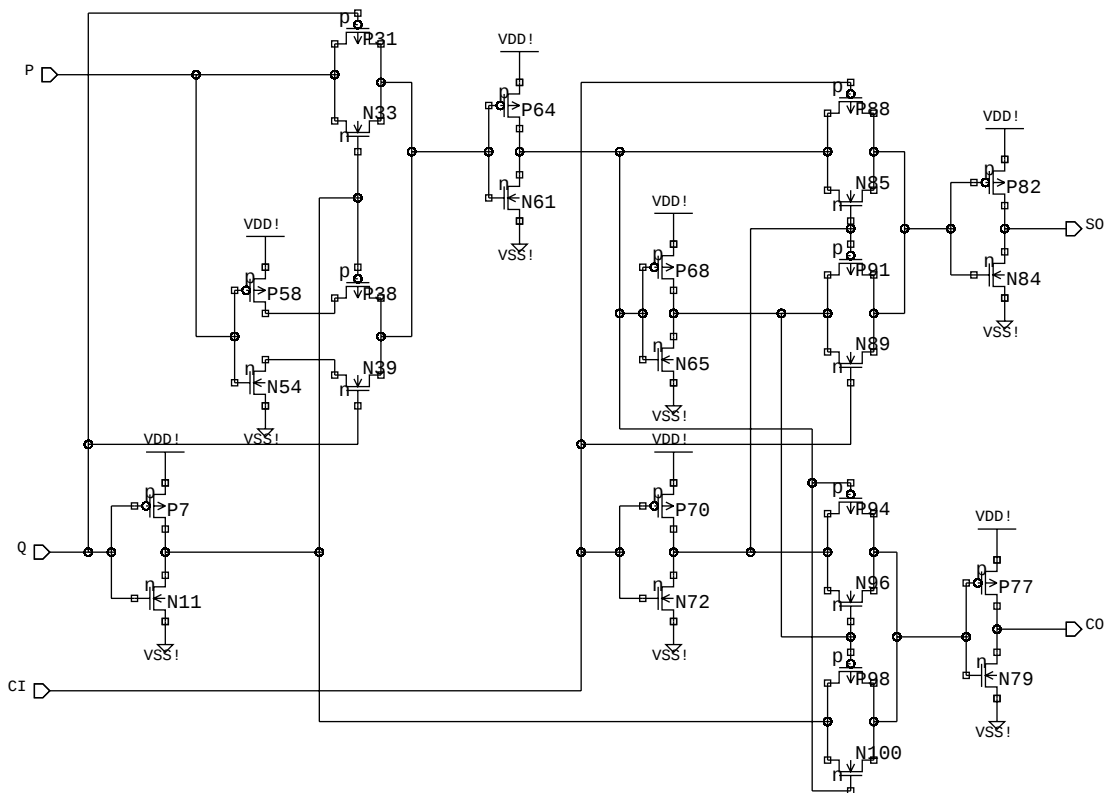
ATL35 CMOS Gate Array

DESCRIPTION: 1 bit full adder with buffered outputs



Truth Table:

P	Q	CI	S0	CO
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1



ADD3X

ATL35 CMOS Gate Array

DESCRIPTION: 1 bit full adder with buffered outputs

SITES: Core = 10 Ring = 0 IO = 0
LOADS: CI=2.0 P=2.0 Q=2.0

TIMING TABLE 3V:		(NomProc,25degC,Nom trf)			
Path	Rise Slope	Rise Int	Fall Slope	Fall Int	
CI -> S0	0.02771	0.36283	0.02524	0.34403	
CI -> C0	0.02743	0.30031	0.02534	0.34854	
P -> S0	0.02766	0.53600	0.02529	0.48636	
P -> C0	0.02751	0.53227	0.02533	0.61277	
Q -> S0	0.02750	0.63993	0.02527	0.47152	
Q -> C0	0.02755	0.54121	0.02530	0.30263	

