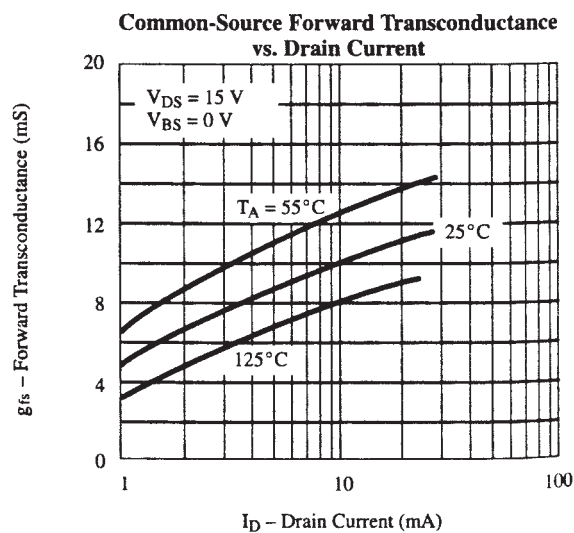
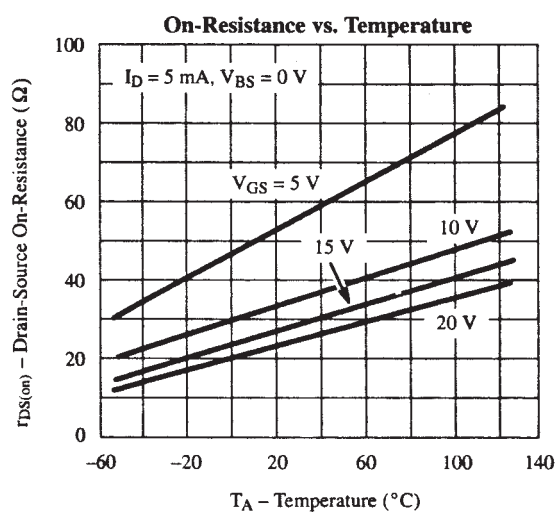
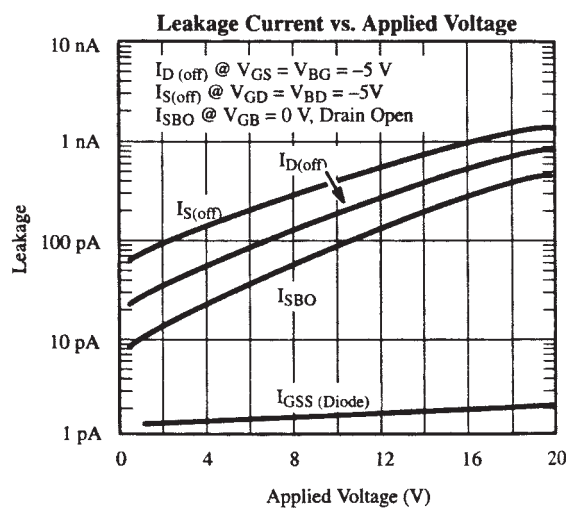
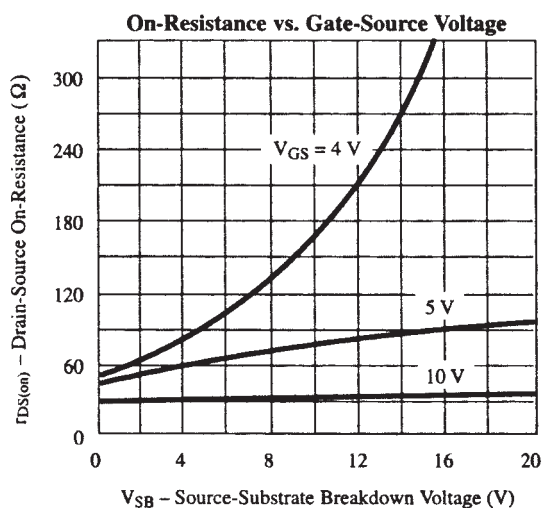


LINEAR SYSTEMS

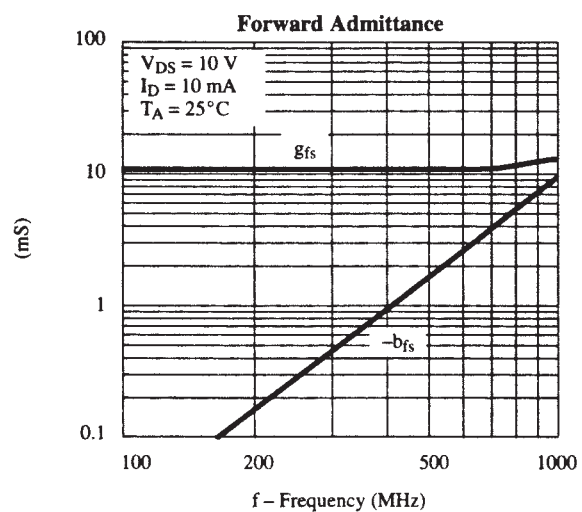
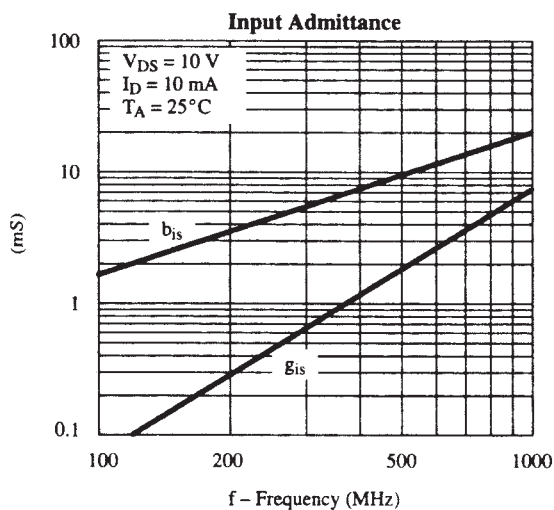
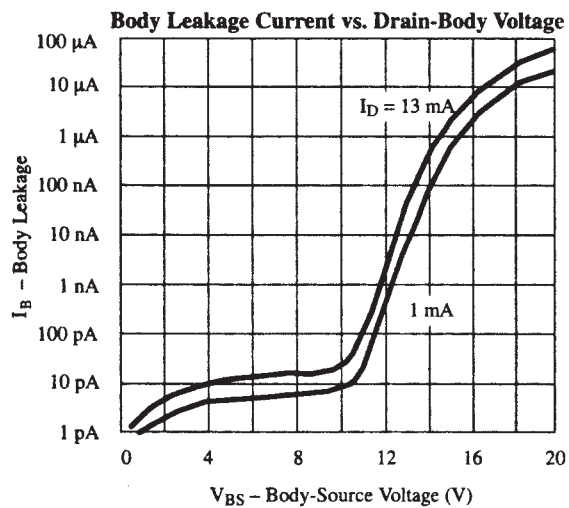
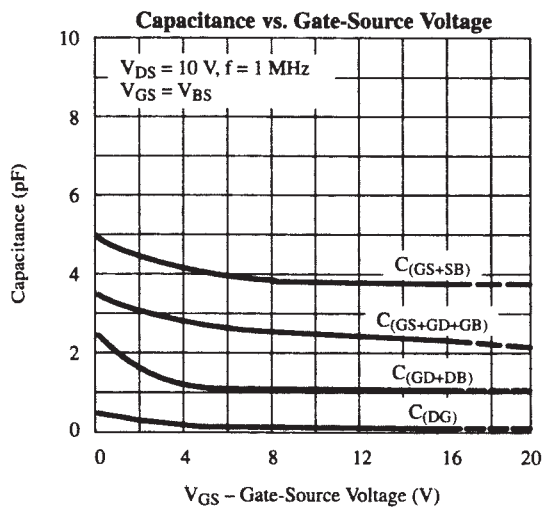
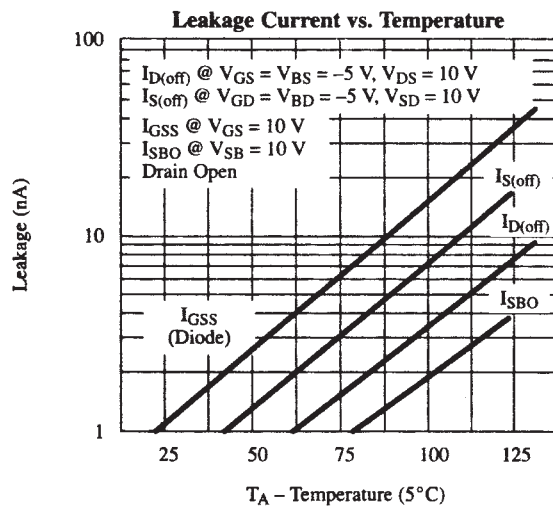
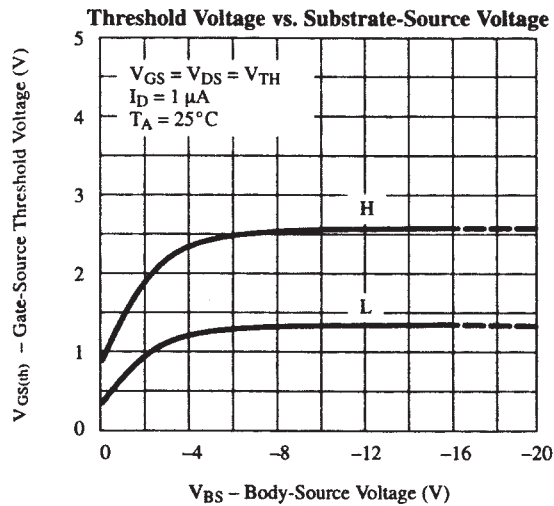
Linear Integrated Systems

SD5000/5001/5400/5401 QUAD N-CHANNEL LATERAL DMOS JFET SWITCH TYPICAL CHARACTERISTICS

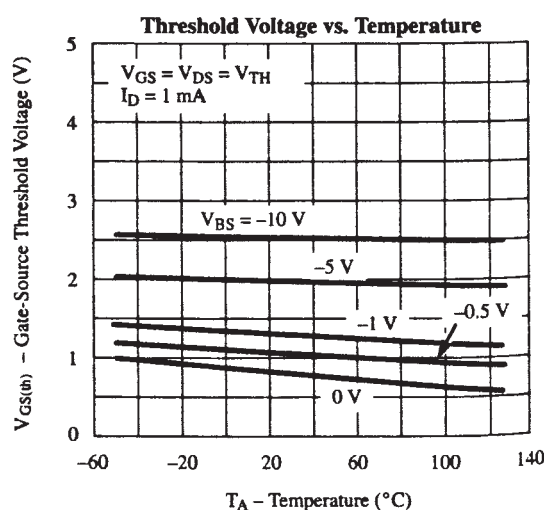
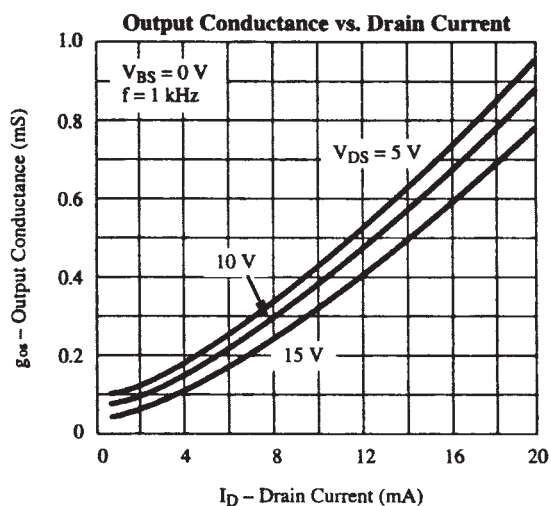
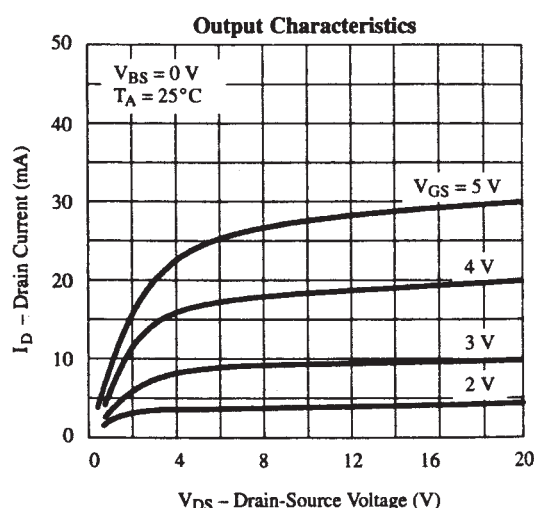
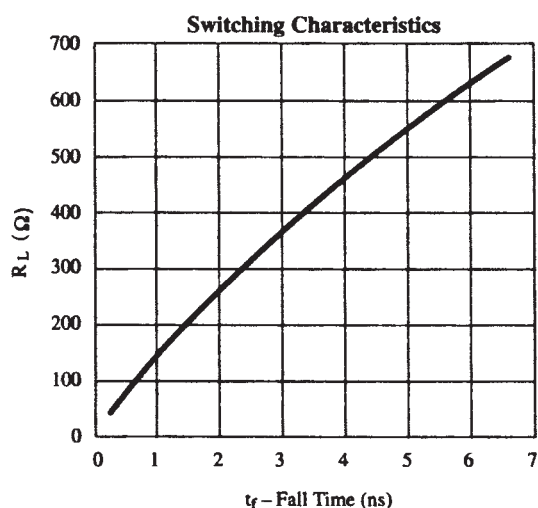
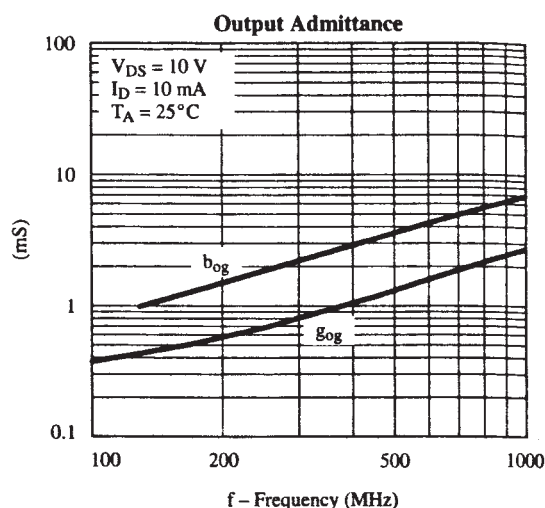
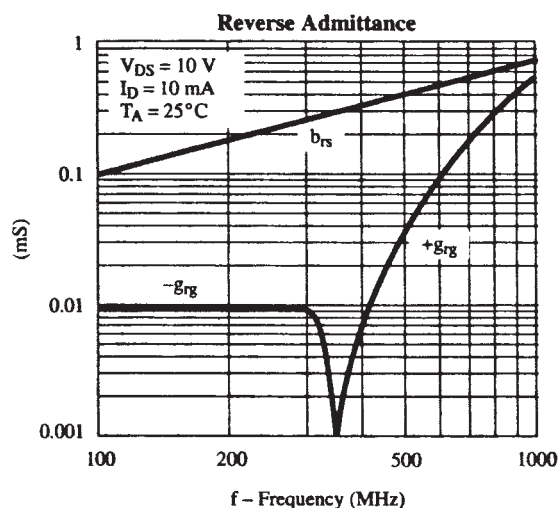
Typical Characteristics



Typical Characteristics (Cont'd)



Typical Characteristics (Cont'd)



Switching Time Test Circuit

