

## SIDC38D60C6

## Fast switching diode chip in EMCON 3-Technology

### **FEATURES:**

- 600V EMCON 3 technology 70 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

## This chip is used for:

• power module



## Applications:

drives

Chip Type	$V_R$	I <sub>F</sub>	Die Size	Package	Ordering Code
SIDC38D60C6	600V	150A	7.8 x 4.9 mm <sup>2</sup>	sawn on foil	Q67050-A4355-
0.2 0002 0000	0001	100/1	7.0 X 4.0 IIIII	Jawii dii idii	A101

### **MECHANICAL PARAMETER:**

Raster size	7.8 x 4.9				
Area total / active	38.22 / 33.38	$mm^2$			
Anode pad size	7.1 x 4.2				
Thickness	70	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	353 pcs				
Passivation frontside	Photoimide				
Anode metallization	3200 nm AlSiCu				
Cathode metallization	Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500μm				
Reject ink dot size Ø 0.65mm; max 1.2mm					
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



# SIDC38D60C6

## **Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$		600	V
Continuous forward current limited by	,		1)	
T <sub>jmax</sub>	I <sub>F</sub>		·	
Single pulse forward current	I <sub>FSM</sub>	$t_P = 10 \; ms \; sinusoidal$	tbd	Α
(depending on wire bond configuration)	7FSIM		tod	
Maximum repetitive forward current			450	
limited by T <sub>jmax</sub>	/ <sub>FRM</sub>		450	
Operating junction and storage temperature	$T_{\rm j}$ , $T_{ m stg}$		-40+175	°C

<sup>1)</sup> depending on thermal properties of assembly

Static Electrical Characteristics (tested on chip),  $T_i$ =25 °C, unless otherwise specified

Parameter	Symbol	Condi	Value			Unit	
raiailletei	Syllibol	Conditions		min.	Тур.	max.	
Reverse leakage current	$I_{R}$	V <sub>R</sub> =600V	<i>T<sub>j</sub></i> =25 °C			960	μΑ
Cathode-Anode breakdown Voltage	V <sub>Br</sub>	I <sub>R</sub> =0.25mA	<i>T<sub>j</sub></i> =25°C	600			V
Forward voltage drop	$V_{F}$	I <sub>F</sub> = 150A	<i>T<sub>j</sub></i> =25 °C	1.2	1.6	1.9	V

## Dynamic Electrical Characteristics (verified by design/characterization), inductive load

 $T_{\rm j}$  = 25 °C, unless otherwise specified

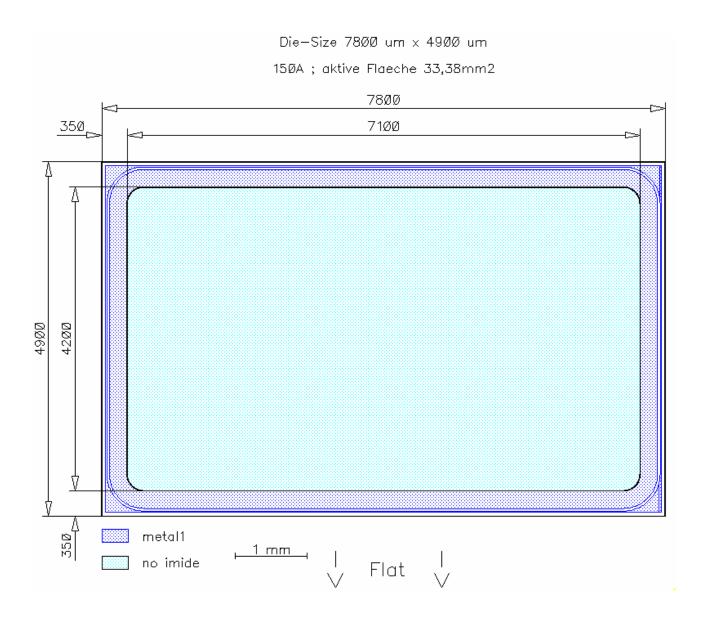
Parameter	Symbol	Conditions		Value 2)			I Imit
Farameter	Symbol			min.	Тур.	max.	Unit
Reverse recovery time	t <sub>rr1</sub>	I <sub>F</sub> =150A	$T_j = 25  ^{\circ}\text{C}$		tbd		
	$t_{rr2}$	di/dt=tbdA/ms $V_R=300V$	$T_j = 125  ^{\circ}\text{C}$		tbd		ns
Peak recovery current	I <sub>RRM1</sub>	I <sub>F</sub> =150A	$T_j = 25  ^{\circ}\text{C}$		tbd		_
	I <sub>RRM2</sub>	di/dt=tbdA/ms V <sub>R</sub> =300V	$T_j = 125  {}^{\circ}\text{C}$		tbd		A
Reverse recovery charge	Q <sub>rr1</sub>	$I_F = 150 A$	T <sub>j</sub> =25°C		tbd		
	Q <sub>rr2</sub>	di/dt=tbdA/ms V <sub>R</sub> =300V	T <sub>j</sub> =125°C		tbd		μC
Peak rate of fall of reverse recovery current	di <sub>rr1</sub> /dt	I <sub>F</sub> =150A	$T_j = 25$ °C		tbd		Δ /
	di <sub>rr2</sub> /dt	di/dt=tbdA/ms V <sub>R</sub> =300V	T <sub>j</sub> =125°C		tbd		- A/μs
Softness	S1	I <sub>F</sub> =150A	<i>T<sub>j</sub></i> =25 °C		tbd		
	S2	di/dt=tbdA/ms V <sub>R</sub> =300V	T <sub>j</sub> =125°C		tbd		1

<sup>&</sup>lt;sup>2)</sup> values also influenced by parasitic L- and C- in measurement and package.





## **CHIP DRAWING:**





## SIDC38D60C6

FURTHER ELECTRICAL CHARACTERISTICS:		
This chip data sheet refers to the device data sheet	tbd	
Description:		
AQL 0,65 for visual inspection according to failure	catalog	
Electrostatic Discharge Sensitive Device according	g to MIL-STD 883	
Test-Normen Villach/Prüffeld		

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