



# SAW Components

Data Sheet B4064





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B4064

Low-Loss Filter

810,0 MHz

Data Sheet

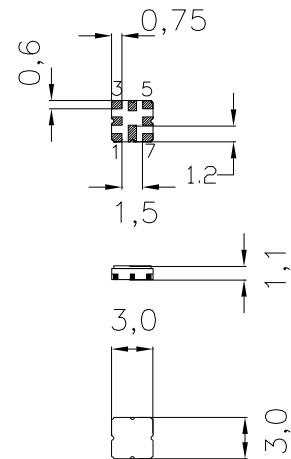
SMD ceramic package QCC8D

Features

- Low loss IF filter for HiperLAN
- Balanced to balanced operation
- Package for **Surface Mounted Technology (SMT)**

Terminals

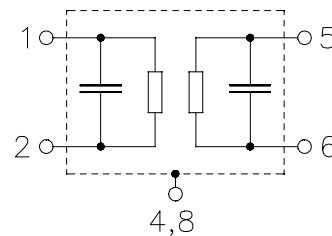
- Ni, gold-plated



Dimensions in mm, approx. weight 0,037 g

Pin configuration

- 1 Input
- 2 Input
- 5 Output
- 6 Output
- 3, 7 To be grounded
- 4, 8 Case - ground



Type	Ordering code	Marking and Package according to	Packing according to
B4064	B39811-B4064-U810	C61157-A7-A72	F61074-V8101-Z000

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	$T$	- 40/+ 85	°C	
Storage temperature range	$T_{stg}$	- 40/+ 85	°C	
DC voltage	$V_{DC}$	3	V	
Source power	$P_s$	0	dBm	source impedance 200 $\Omega$



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**Characteristics**

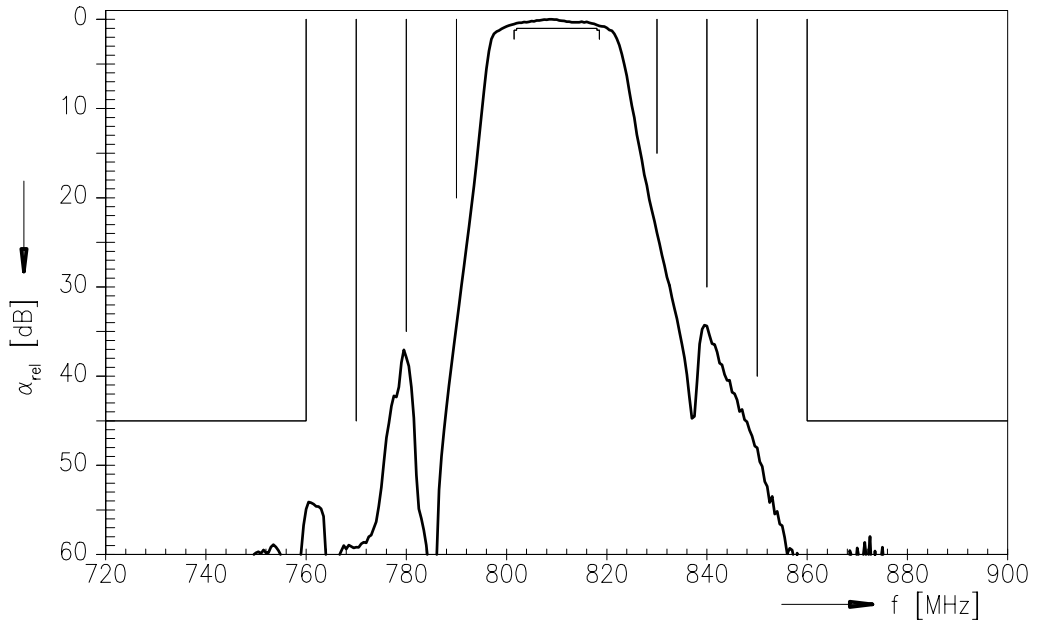
Operating temperature range:  $T_A = 0 \dots +70 \text{ }^\circ\text{C}$   
 Terminating source impedance:  $Z_S = 200 \text{ } \Omega$   
 Terminating load impedance:  $Z_L = 200 \text{ } \Omega$

		min.	typ.	max.	
<b>Nominal frequency</b>	$f_N$	—	810,0	—	MHz
<b>Minimum insertion attenuation</b>	$\alpha_{\min}$	—	1,7	4,0	dB
<b>Band width</b>	$B_{1\text{dB}}$	—	20	—	MHz
	$B_{3\text{dB}}$	—	25	—	MHz
<b>Amplitude ripple in passband (p-p)</b>	$\Delta\alpha$				
	$f_N \pm 8,0 \text{ MHz}$	—	0,6	1,0	dB
	$f_N \pm 8,5 \text{ MHz}$	—	0,7	1,2	dB
<b>Group delay ripple (p-p)</b>	$\Delta\tau$				
	$f_N \pm 8,5 \text{ MHz}$	—	25	50	ns
<b>Relative attenuation (relative to <math>\alpha_{\min}</math>)</b>	$\alpha_{\text{rel}}$				
	$f_N - 20,0 \text{ MHz}$	20	34	—	dB
	$f_N + 20,0 \text{ MHz}$	15	22	—	dB
	$f_N - 30,0 \text{ MHz}$	35	37	—	dB
	$f_N + 30,0 \text{ MHz}$	30	34	—	dB
	$f_N - 40,0 \text{ MHz}$	45	55	—	dB
	$f_N + 40,0 \text{ MHz}$	40	48	—	dB
	$f_N - 500 \text{ MHz} \dots f_N - 50,0 \text{ MHz}$ $f_N + 50,0 \text{ MHz} \dots f_N + 500 \text{ MHz}$	45	54	—	dB
	45	58	—	dB	



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Normalized frequency response



Normalized frequency response (pass band)





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